



VIDYA JYOTHI
INSTITUTE OF TECHNOLOGY
AN AUTONOMOUS INSTITUTION

(Accredited by NAAC & NBA, Approved by AICTE New Delhi & Permanently Affiliated to JNTUH)
Aziz Nagar Gate, C.B. Post, Hyderabad-500 075

Department of Electronics and Communication Engineering
(Accredited by NBA)

Minutes of the Board of studies in ECE meeting held on 29th March 2023 at 11.00 AM at Board Room.

The following members were present in the meeting.

S.No	Name of the Member	Designation	Signature
1.	Dr. M. Rajendra Prasad, Professor & Head ECE, VJIT	Chairman	
2.	Dr. T.Satya Savithri, Professor, JNTUH CEH	JNTUH Nominee	
3.	Dr.S.Sathes Kumaran, Professor, Anurag University	Member	
4.	Dr. P. Chandra Sekhar, Professor, Osmania University	Member	
5.	Dr. P.Nageswara Rao, Professor, Vardhaman College of Engineering.	Member	
6.	Mr. A. Hariharan, Associate Director, PACTRA EDGE, Hyderabad.	Member	
7.	Mr. N. Venkatesh, Sr. Director, Silicon Labs, Hyderabad.	Member	
8.	Dr. S. Thulasi Prasad, Professor, ECE, VJIT.	Member	
9.	Dr. K.Vasanth, Professor,ECE,VJIT	Member	
10.	Dr.P.Ganesan, Professor, ECE,VJIT	Member	
11.	Dr. V. G. Siva Kumar, Assoc. Professor, ECE, VJIT.	Member	
12.	Dr. M. Vadivel, Assoc. Professor, ECE, VJIT.	Member	
13.	Mrs. A. Jaya Lakshmi, Assoc. Professor, ECE, VJIT.	Member	
14.	Mr. S. Pradeep Kumar Reddy, Assoc. Professor, ECE, VJIT.	Member	



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Resolutions

1. All the previous minutes were incorporated as per previous BOS meeting

Noted and Approved

Item No. 1: Approval of B.Tech. (ECE) II, III and IV Year Course Structure of R-22 regulations

The chairman of BoS presented a detailed course structure of B.Tech.(ECE) II, III and IV Years of R-22 regulations.

Resolution (1): The members after a detailed discussion approved the course structure of B.Tech. (ECE) II, III and IV Years of R-22 regulations.

Noted and Approved.

Item No. 2: Approval of B.Tech. (ECE) II Year Syllabi of R-22 regulations

The Chairman presented a detailed syllabus of B.Tech.(ECE) II Year R-22 regulations.

Resolution (2): The members after thorough discussion approved the syllabi of B.Tech. (ECE) II Year R22 regulations.

Noted and Approved

Item No. 3: Approval of the courses along with their syllabi offered to other department students under R-22 regulations

The Chairman presented a detailed syllabi of courses which are offered to other Engg. Departments under R-22 regulations.

Resolution (3): The members after thorough discussion approved the courses offered to other engg. depts.

Noted and Approved

1. Roy	6. A. Arshi	11. J. J.
2. D. Arshi	7. N. Venkatesh	12. A. Arshi
3. B.	8. S. Arshi	13. A. Arshi
4. L. Arshi	9. S. Arshi	14. A. Arshi
5. S. Arshi	10. A. Arshi	



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Item No. 4: Approval of Panel of examiners

The Chairperson emphasized the necessity of Panel of Examiners. Their services will be utilized in the preparation of End-Semester Question paper(s) and Evaluation of End-Semester Examination Answer Scripts. They will be paid remuneration as per the recommendations of College Finance Committee.

Resolution (4): The committee on BoS in ECE to prepare the Panel of examiners in consultation with the senior teachers for all the B.Tech (ECE) courses under R-22 regulations. The same may be submitted to the Examination branch (Autonomous) for further processing.

Noted and Approved.

The title of subject Digital Electronics is changed/renamed as Digital Logic Design

Item no. 5 : Approval of Courses/ Equivalent Subjects (Detained Students of Old Regulations) along with their Syllabi offered to Students under R15, R18, R19, R20, R21 and R22 (upto 8th) Regulations

Resolution(5): Kindly grant power to the Chairman of BoS to take decision regarding approval of Courses/ Equivalent Subjects (Detained Students of old regulations) along with their syllabi offered to Students under R15, R18, R19, R20, R21 and R22 (upto II/II) Regulations

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VIDYA JYOTHI

INSTITUTE OF TECHNOLOGY

AN AUTONOMOUS INSTITUTION

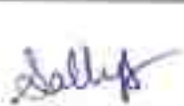
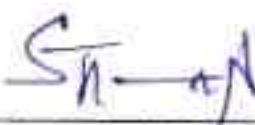
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Department of Electronics and Communication Engineering

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BoS Members' Signatures:

			
1. Dr. M. Rajendra Prasad, Professor, Head & Chairman of BoS, ECE, VJIT.	2. Dr. T. Satya Savithri, Professor of ECE, JNTUH CEH, University Nominee.	3. Dr. S. Sathees Kumaran, Professor, Anurag University, External Member.	4. Dr. P. Chandru Sekhar, Osmaina University, External Member.
			
5. Dr. P. Nageswara Rao, Professor, Vardhaman College of Engineering, External Member.	6. Mr. A. Hariharan, Associate Director, PACTRA EDGE, Hyderabad, External Member.	7. Mr. N. Venkatesh, Sr. Director, Silicon Labs, Hyderabad, External Member.	8. Dr. S. Thulasi Prasad, Professor, ECE, VJIT, Internal Member.
			
9. Dr. K. Vasanth, Professor, ECE, VJIT, Internal Member.	10. Dr. P. Ganesan, Professor, ECE, VJIT, Internal Member.	11. Dr. V. G. Siva Kumar, Associate Professor, ECE, VJIT, Internal Member.	12. Dr. M. Vadivel, Associate Professor, ECE, VJIT, Internal Member.
			
13. Mrs. A. Jaya Lakshmi, Associate Professor, ECE, VJIT, Internal Member.	14. Mr. S. Pradeep Kumar Reddy, Associate Professor, ECE, VJIT, Internal Member.		



VIDYA JYOTHI
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R22

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Aziz Nagar Gate, C.B. Post, Hyderabad-500 075

Department of Electronics and Communication Engineering

Syllabus

R22 REGULATIONS

R22
REGULATIONS

Department of ECE

Course Structure

The Syllabus is for the Students admitted in AY: 2022 - 2023





Department of Electronics and Communication Engineering

I YEAR I SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1.	A221001	Mathematics-I(Linear Algebra & Calculus)	3	1	0	4
2.	A221002	Applied Physics	3	1	0	4
3.	A221501	C Programming for Engineers	3	0	0	3
4.	A221003	English for Skill Enhancement	2	0	0	2
5.	A221381	Engineering Workshop	0	1	3	2.5
6.	A221401	Elements of Electronics and Communication Engineering	0	0	2	1
7.	A221081	Applied Physics Laboratory	0	0	3	1.5
8.	A221082	English Language and Communication Skills Laboratory	0	0	2	1
9.	A221581	C Programming for Engineers Laboratory	0	0	2	1
10.		Induction Programme				
Total			11	3	12	20

I YEAR II SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1.	A221005	Mathematics-II (Ordinary Differential Equations and Vector Calculus)	3	1	0	4
2.	A221006	Engineering Chemistry	3	1	0	4
3.	A222303	Engineering Graphics & Modeling	1	0	4	3
4.	A222204	Basic Electrical Engineering	2	0	0	2
5.	A222402	Electronic Devices and Circuits	2	0	0	2
6.	A222583	Python Programming Laboratory	0	2	2	3
7.	A222084	Engineering Chemistry Laboratory	0	0	2	1
8.	A222283	Basic Electrical Engineering Laboratory	0	0	2	1
Total			11	4	10	20

1. R. S. S.
2. S. S. S.
3. S. S.
4. S. S.
5. S. S.

6. A. S. S.
7. N. S. S.
8. S. S.
9. S. S.
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13. S. S.
14. S. S.



Department of Electronics and Communication Engineering

II YEAR I SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1	A223403	Analog Circuits	3	0	0	3
2	A223404	Network analysis and Transmission Lines	3	0	0	3
3	A223405	Digital Logic Design	3	0	0	3
4	A223406	Signals and Systems	3	1	0	4
5	A223407	Probability Theory and Stochastic Processes	3	0	0	3
6	A223481	Digital logic Design Laboratory	0	0	2	1
7	A223482	Basic Simulation Laboratory	0	0	2	1
8	A223483	Electronic Devices and Analog Circuits Laboratory	0	0	2	1
9	A223009	Professional Communication	2	0	0	1
		Total Credits	17	1	6	20

II YEAR II SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1	A224010	Complex analysis and Fourier transforms	3	0	0	3
2	A224408	Electromagnetic Waves	3	0	0	3
3	A224409	Analog and Digital Communications	3	0	0	3
4	A224410	Linear IC Applications	2	0	0	2
5	A224411	Electronic Circuit Analysis	3	0	0	3
6	A224484	Analog and Digital Communications Laboratory	0	0	2	1
7	A224485	Linear IC Applications Laboratory	0	0	2	1
8	A224486	Electronic Circuit Analysis Laboratory	0	0	2	1
9	A2244P1	Real Time Project/Field Based Project	0	0	4	2
10	A224011	Quantitative methods & logical reasoning	2	0	0	1
		Total Credits	16	0	10	20

1. R. S. S.
2. S. S. S.
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5. S.

6. S. S.
7. N. Venkatesh
8. S. S.
9. S. S.
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12. S.
13. S.
14. S.



Department of Electronics and Communication Engineering

III YEAR I SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1		Microprocessors and Microcontrollers	3	1	0	4
2		Control Systems	3	1	0	4
3		Antennas and Wave Propagation	3	0	0	3
4		Professional Elective-I	3	0	0	3
		1. Computer Organization and Operating Systems				
		2. Data Communications and Computer Networks				
		3. Information Theory and Coding				
5		Open Elective-I	3	0	0	3
		1. Fundamentals of Internet of Things				
		2. Principles of Signal Processing				
		3. Digital Electronics for Engineering				
6		Microprocessors and Microcontrollers Laboratory	0	0	2	1
7		Advanced English Communication Skills Laboratory	0	0	2	1
8		Antenna Laboratory	0	0	2	1
9		Environmental Science	2	0	0	0
		Total Credits	17	2	6	20

III YEAR II SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1		Digital Signal Processing	3	0	0	3
2		Business Economics & Financial Analysis	3	0	0	3
3		CMOS VLSI Design	3	0	0	3
4		Professional Elective-II	3	0	0	3
		1. Digital Image Processing				
		2. Mobile Communications and Networks				
		3. Embedded System Design				
5		Open Elective-II	3	0	0	3
		1. Electronic Sensors				
		2. Electronics for Health Care				
		3. Telecommunications for Society				
6		VLSI Laboratory	0	0	2	1
7		Embedded system Design Laboratory	0	0	2	1
8		Digital Signal Processing Laboratory	0	0	2	1
9		Industry Oriented Mini Project/Internship	0	0	4	2
10		Gender Sensitization	2	0	0	0
		Total Credits	17	0	10	20

1. R. Ravi
2. S. Sathya
3. P. Sathya
4. S. Sathya
5. S. Sathya

6. A. Sathya
7. N. Venkatesh
8. S. Sathya
9. S. Sathya
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Department of Electronics and Communication Engineering

IV YEAR I SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1		Microwave and Optical Communications	3	1	0	3
2		Electronic Measurements and Instrumentation	3	0	0	3
3		Professional Elective-III	3	0	0	3
		1.Radar Systems				
		2.CMOS Analog IC Design				
		3.Artificial Neural Networks				
4		Professional Elective-IV	3	0	0	3
		1.Network Security and Cryptography				
		2.Satellite Communications				
		3.Biomedical Instrumentation				
5		Open Elective-III	3	0	0	3
		1. Measuring Instruments				
		2. Communication Technologies				
		3. Fundamentals of Social Networks				
6		Microwave and Optical Communications Laboratory	0	0	4	2
7		Project Stage-I	0	0	6	3
		Total Credits	15	1	10	20

IV YEAR II SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1		Professional Elective-V	3	0	0	3
		1.Artificial Intelligence				
		2.5G and beyond Communication				
		3.Machine Learning				
2		Professional Elective-VI	3	0	0	3
		1.Multimedia Database Management Systems				
		2.System On-Chip Architecture				
		3.Wireless Sensor Networks				
3		Comprehensive Viva-Voce	0	0	0	3
4		Project Stage-II	0	0	22	9
5		Technical Seminar	0	0	0	2
		Total Credits	6	0	22	20

1. R. Ravi
2. S. S. S.
3. R.
4. S. S.
5. S. S.

6. A. S.
7. N. Venkatesh
8. S. S.
9. S. S.
10. S. S.

11. S. S.
12. S. S.
13. S. S.
14. S. S.



Department of Electronics and Communication Engineering

COURSE STRUCTURE FOR B.TECH II YEAR I SEMESTER

II YEAR I SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1	A223403	Analog Circuits	3	0	0	3
2	A223404	Network analysis and Transmission Lines	3	0	0	3
3	A223405	Digital Logic Design	3	0	0	3
4	A223406	Signals and Systems	3	1	0	4
5	A223407	Probability Theory and Stochastic Processes	3	0	0	3
6	A223481	Digital logic Design Laboratory	0	0	2	1
7	A223482	Basic Simulation Laboratory	0	0	2	1
8	A223483	Electronic Devices and Analog Circuits Laboratory	0	0	2	1
9	A223009	Professional Communication	2	0	0	1
		Total Credits	17	1	6	20

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Department of Electronics and Communication Engineering

ANALOG CIRCUITS

II B.Tech I Semester

L	T	P	C
3	0	0	3

Course Outcomes:

After going through this course the student can

1. Design the amplifiers with various biasing techniques.
2. Design single stage amplifiers using BJT and FET
3. Design multistage amplifiers and understand the concepts of High Frequency Analysis of BJT.
4. Utilize the Concepts of negative feedback to improve the stability of amplifiers.
5. Utilize the Concepts of positive feedback to provide sustained oscillations.

UNIT-I

BJT Biasing: Transistor Biasing and Stabilization - Operating point, DC & AC loadlines, Biasing - Fixed Bias, Self Bias, Bias Stability, Bias Compensation using Diode

Analysis and Design of Small Signal Low Frequency BJT Amplifiers: Transistor Hybrid model, Determination of h-parameters from transistor characteristics, Typical values of h-parameters in CE, CB and CC configurations, Transistor amplifying action, Analysis of CE, CC, CB Amplifiers and CE Amplifier with emitter resistance, low frequency response of BJT Amplifiers, effect of coupling and bypass capacitors on CE Amplifier.

UNIT-II

FET-Biasing Techniques

FET Amplifiers: Analysis of CS, CD, CG JFET Amplifiers, comparison of performance with BJT Amplifiers, Basic Concepts of MOSFET Amplifiers, MOS Small signal model, Common source amplifier with resistive, Diode connected and Current source loads, Source follower, Common Gate Stage, Cascode and Folded Cascode Amplifier - frequency response.

UNIT-III

Multistage Amplifiers: Classification of Amplifiers, Distortion in amplifiers, Different coupling schemes used in amplifiers, Frequency response and Analysis of multistage amplifiers, Cascade RC Coupled amplifiers, Cascode amplifier, Darlington pair.

Transistor at High Frequency: Hybrid - π model of Common Emitter transistor model, f_a , f_β and unitygain bandwidth, Gain-bandwidth product.

UNIT-IV

Feedback Amplifiers: Concepts of feedback-Classification of feedback amplifiers-General characteristics of Negative feedback amplifiers - Effect of Feedback on Amplifier characteristics - Voltage series, Voltage shunt, Current series and Current shunt Feedback configurations - Simple problems.

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Department of Electronics and Communication Engineering

UNIT-V

Oscillators: Condition for Oscillations, RC type Oscillators-RC phase shift and Wien-bridge Oscillators, LC type Oscillators-Generalized analysis of LC Oscillators, Hartley And Colpitts Oscillators, Frequency and amplitude stability of Oscillators, Crystal Oscillator.

TEXTBOOKS:

1. Jacob Millman, Christos CH alkias-Integrated Electronics, McGraw Hill Education.
2. Robert L. Boylestead, Louis Nashelsky -Electronic Devices and Circuits
3. theory, 11th Edition, 2009, Pearson

REFERENCEBOOKS:

1. David A. Bell-Electronic Devices and Circuits, 5th Edition, Oxford.
2. Adel S. Sedra, Kenneth C. Smith-Microelectronic Circuits- Theory and Applications,
3. Oxford. Chinmoy Saha, Arindam Halder, Debaati Ganguly-Basic Electronics-
4. Principles and Applications, 2018, Cambridge.

1. Roy
2. Saltyk
3. P
4. 15m
5. cheng

6. A. K. Saha
7. N. Venkatesh
8. S. K. Saha
9. S. K. Saha
10. S. K. Saha

11. S. K. Saha
12. S. K. Saha
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14. S. K. Saha



Department of Electronics and Communication Engineering

NETWORK ANALYSIS AND TRANSMISSION LINES

II B.Tech II Semester

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

After going through this course the student can:

1. Recite basic concepts of network parameters, theorems and transmission line theory.
2. Differentiate the changes of transient networks using Laplace transform
3. Compare and contrast the parameters, functions and synthesis of the network
4. Apply the concepts of theorems on networks and transmission line theory to solve impedance matching issues.
5. Solve the transmission lines and matching circuits problems using Smith chart

UNIT-I:

Network Theorems:

Source transformation - Superposition Theorem - Thevenin's theorem - Norton's theorem - Reciprocity theorem - Maximum power transfer theorem

UNIT-II:

Transient Analysis:

Transient response of RL, RC, RLC Circuits (Series and Parallel combinations) for DC excitations, Initial Conditions, Solution using Differential Equations approach and Laplace Transform Method. Transient response for different inputs such as step, ramp, pulse and impulse by using Laplace transforms method.

UNIT - III:

Two Port Networks:

Impedance Parameters, Admittance Parameters, Hybrid Parameters, Transmission (ABCD) Parameters, g parameters, Conversion of one of Parameter to another, Conditions for Reciprocity and Symmetry, Inter Connection of Two Port networks in series, Parallel and Cascaded configurations, Illustration problems.

UNIT - IV:

Transmission Lines - I:

Types, Parameters, Transmission Line Equations, Primary & Secondary Constants, Equivalent Circuit, Characteristic Impedance, Propagation Constant, Phase and Group Velocities, Infinite Line Concepts, Lossless / Low Loss Characterization, Types of Distortion, Condition for Distortion less line, Minimum Attenuation, Loading - Types of Loading.

UNIT- V:

Transmission Lines - II:

Input Impedance Relations, SC and OC Lines, Reflection Coefficient, VSWR, $\lambda/4$, $\lambda/2$, $\lambda/8$ Lines - Impedance Transformations, Smith Chart - Configuration and Applications, Single Stub Matching.

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TEXT BOOKS:

1. Abhijith Chakra barthi, Circuit Analysis and Synthesis, Dhanpat Rai and Company, 2018.
2. Umesh Sinha, Transmission Lines and Networks, Satya prakash Publications, 2010

REFERENCE BOOKS:

1. Van Valken Burg, Network Analysis, Pearson, 2016
2. Ryder J D, Networks, Lines and Fields, Prentice Hall India, 1999.

1. Roy
2. Sahay
3. P
4. Ives
Sichmger

6. A. Kulkarni
7. N. Venkatesh
8. S. R. Ravi
9. S. Ravi
10. S. Ravi

11. S. Ravi
12. S. Ravi
13. S. Ravi
14. S. Ravi



Department of Electronics and Communication Engineering

DIGITAL LOGIC DESIGN

II B.Tech I Semester

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Demonstrate the basic theorems of Boolean algebra, logic gates, combinational and sequential circuits and memories.
2. Analyze the combinational and sequential circuits and memories.
3. Design of logic circuits
4. Realization of gates using different logic families.
5. Explain the design and operation of different semiconductor memories

UNIT-I:

Number System and minimization techniques:

Number System:

Review of number system and base conversion, complements, signed binary numbers, Floating point number representation, Error detection (parity detection only).

Minimization techniques

Boolean Algebra, postulates, basic logic gates, Canonical and Standard Form, NAND and NOR implementation, Minimization of switching function using theorem, The Karnaugh Map Method- Up to Five Variable Maps, Tabular Method.

UNIT-II:

Combinational Circuits:

Adders & Subtractor, Binary Adder-Subtractor, Decimal Adder, Binary Multiplier, Magnitude Comparators, Multiplexers, De-multiplexers, Decoders, Encoders and Code converters, Hazards and Hazard Free Relations.

UNIT-III:

Sequential circuits-I:

Basic Architectural Distinctions between Combinational and Sequential circuits, Latches, Flip Flops: SR, JK, Race Around Condition in JK, JK Master Slave, D and T Type Flip Flops, Excitation Table of all Flip Flops, Design of a Clocked Flip-Flop, Timing and Triggering Consideration, Clock Skew, Conversion from one type of Flip-Flop to another.

Unit-IV:

Sequential Circuits-II:

Synchronous - Asynchronous - Comparison, Design of Single mode Counter, Ripple Counter, Ring Counter, Shift Register, Shift Register Sequences, Ring Counter Using Shift Register, MOD Counters, Finite state machine-capabilities and limitations, Mealy and Moore models.

1. Roy
2. Balaji
3. P. B.
4. Meera
5. Chitra

6. A. Ashwin
7. N. Venkatesh
8. S. Anu
9. S. Anu
10. A. Ashwin

11. A. Ashwin
12. A. Ashwin
13. A. Ashwin
14. A. Ashwin



Department of Electronics and Communication Engineering

UNIT-V:

Logic Families and Semiconductor Memories:

Logic Families:

DCTL, RTL, DTL, TTL and CML Logic –gate realization - Comparison,

Semiconductor Memories:

Introduction to ROM, PAL, PLA, CPLD, FPGA.

TEXT BOOKS:

1. Morris Mano , Digital Design, Prentice Hall India, 2006
2. A Anand Kumar, Switching Theory and Logic Design, Prentice Hall India, 2013

REFERENCE BOOKS:

1. Jain N P, Modern Digital Electronics, Tata McGraw Hill, 2007.
2. Fredriac J. Hill, Gerald R. Peterson, Introduction to Switching Theory and Logic Design Wiley, 1981.

1. Roy
2. Dalvi
3. S
4. Lee
5. S. S. S.

6. S. S. S.
7. N. Venkatesh
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Department of Electronics and Communication Engineering

SIGNALS AND SYSTEMS

II B.Tech I Semester

L	T	P	C
3	1	0	4

COURSE OUTCOMES:

After going through this course the student will be able to:

1. Understand the Mathematics, operations and classification of signals and systems
2. Apply the transform on standard and arbitrary signals
3. Infer the signal transmission through linear systems
4. Interpret the concepts of Laplace Transform and Z-Transform in analysis of systems
5. Understand the process of sampling and the effects of under sampling.

UNIT I:

Signal Analysis:

Analogy between Vectors and Signals, Orthogonal Signal Space, Signal approximation using Orthogonal functions, Mean Square Error, Closed or complete set of Orthogonal functions, Orthogonality in Complex functions, Classification of Signals and systems, Exponential and Sinusoidal signals, Concepts of Impulse function, Unit Step function, Signum function.

UNIT II:

Fourier series & Fourier Transforms:

Representation of Fourier series, Continuous time periodic signals - Dirichlet's conditions, Trigonometric Fourier Series and Exponential Fourier Series, Complex Fourier spectrum. Deriving Fourier Transform from Fourier series, Fourier Transform of arbitrary signal - standard signals - Periodic Signals - Properties - Introduction to Hilbert Transform.

UNIT III:

Signal Transmission through Linear Systems:

Linear System, Impulse response, Response of a Linear System, Linear Time Invariant(LTI) System, Linear Time Variant (LTV) System, Transfer function of a LTI System, Filter characteristic of Linear System, Distortion less transmission through a system, Signal bandwidth, System Bandwidth, Ideal LPF, HPF, and BPF characteristics, Causality and Paley- Wiener criterion for physical realization, Relationship between Bandwidth and rise time.

UNIT IV:

Laplace Transforms:

Laplace Transforms (L.T), Inverse Laplace Transform, Concepts of Region of Convergence (ROC) for Laplace Transforms, Properties of L.T, Relation between L.T and F.T of a signal, Laplace Transform of certain signals using waveform synthesis.

UNIT V:

Sampling Theorem and Z-Transforms:

Graphical and analytical proof for Band Limited Signals, Impulse Sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling - Aliasing, Introduction to Band Pass Sampling.

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Department of Electronics and Communication Engineering

Concept of Z-Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms, Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

TEXT BOOKS:

1. Lathi B P, Signals, Systems & Communications, B.S. Publications, 2003.
2. Alan V. Oppenheim, Alan S. Willsky, Syed Hamid Nawab, Signals and Systems, PrenticeHall India, 1997

REFERENCE BOOKS:

1. Simon S. Haykin, Barry Van Veen, Signals and Systems, Wiley, 2003
2. Rama Krishna Rao A, Signals and Systems, 2008, Tata McGraw Hill, 2008.

1. Roy
2. Dally
3. P
4. Vee
5. S. S. Haykin

6. A. V. Oppenheim
7. N. Venkatesh
8. S. S. Haykin
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Department of Electronics and Communication Engineering

PROBABILITY THEORY AND STOCHASTIC PROCESSES

II B.Tech I Semester

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

After going through this course the student will be able to

1. Demonstrate knowledge in Probability theory, Single and multiple random variables and Random processes and their characteristics
2. Analyze operations on single and multiplier and om variables and processes
3. Compute Simple probabilities using an appropriate sample space, Expectations from probability density functions, Least-square & maximum likelihood estimators for engineering problems Mean and Covariance functions for simple random processes.
4. Designsolutionsforcomplexengineeringproblemsinvolvingrandomprocesses.
5. Understand how random variables and stochastic processes can be described and analyzed

UNIT-I:

Probability & Random variables:

Probability:

Probability introduced through Sets and Relative Frequency: Experiments and Sample Spaces, Discrete and Continuous Sample Spaces, Events, Probability Definitions and Axioms, Joint Probability, Conditional Probability, Total Probability, Bay's Theorem, Independent Events.

Random Variable:

Definition, Conditions for a Function to be a Random Variable, Discrete, Continuous and Mixed Random Variable, Distribution and Density functions, Properties, Binomial, Poisson, Uniform, Gaussian, Exponential, Rayleigh, Methods of defining Conditioning Event, Conditional Distribution, Conditional Density and their Properties.

UNIT-II:

Operations on single & multiple random variables- expectations:

Expected Value of a Random Variable, Function of a Random Variable, Moments about the Origin, Central Moments, Variance and Skew, Chebychev's Inequality, Characteristic Function, Moment Generating Function, Joint Distribution Function and its Properties, Marginal Distribution Functions, Conditional Distribution and Density - Point Conditioning, Conditional Distribution and Density - Interval conditioning, Statistical Independence, Sum of Two and more Random Variables, Central Limit Theorem, Equal and Unequal Distribution. Expected Value of a Function of Random Variables- Joint Moments about the Origin, Joint Central Moments, Joint Characteristic Functions

UNIT-III:

Random processes - Temporal characteristics:

The Random Process Concept, Classification of Processes, Deterministic and Non deterministic Processes, Distribution and Density Functions, concept of Stationarity and Statistical Independence. First-Order Stationary Processes, Second- Order and Wide- Sense Stationarity, (N-Order) and Strict-Sense Stationarity, Time Averages and Ergodicity, Mean-

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Department of Electronics and Communication Engineering

Ergodic Processes, Correlation-Ergodic Processes, Autocorrelation Function and Its Properties, Cross-Correlation function and Its Properties, Random Signal Response of Linear Systems: System Response-Mean and Mean-squared Value of System Response, autocorrelation Function of Response, Cross-Correlation Functions of Input and Output.

UNIT-IV:

Random processes – Spectral characteristics:

The Power Spectrum: Properties, Relationship between Power Spectrum and Autocorrelation Function, the Cross-Power Density Spectrum, Properties, Relationship between Cross-Power Spectrum and Cross-Correlation Function, Spectral Characteristics of System Response: Power Density Spectrum of Response, Cross-Power Density Spectrums of Input and Output.

UNIT-V:

Noise sources:

Resistive/Thermal Noise Source, Arbitrary Noise Sources, Effective Noise Temperature, Noise equivalent bandwidth, Average Noise Figures, Average Noise Figure of cascaded networks, Narrow Band noise, Quadrature representation of narrow band noise & its properties.

TEXT BOOKS:

1. Peyton Z. Peebles, Probability, Random Variables & Random Signal Principles, Tata McGraw Hill, 2001.
2. Taub and Schilling, Principles of Communication systems, Tata McGraw Hill, 2008

REFERENCE BOOKS:

1. Athanasios Papoulis and S. Unnikrishna Pillai, Probability, Random Variables and Stochastic Processes, Prentice Hall India, 2002.
2. Murugesan K, Guruswamy P, Probability, Statistics & Random Processes, Anuradha Publications, 2003.

1. Roy
2. Sallu
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6. A. K. Jha
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Department of Electronics and Communication Engineering

DIGITAL LOGIC DESIGN LABORATORY

II B.Tech I Semester

L	T	P	C
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Course Outcomes:

Upon completing this course, the students will be able to

1. Design of all logic gates by using universal gates.
2. Design of adder and subtractor by using logic gates.
3. Design of MSI combinational circuits by using logic gates.
4. Design and analyze counters by using flip-flops.
5. Analyze the universal shift register.

List of Experiments

Note: Minimum 10 Experiments have to be conducted

1. Design of all logic gates using NAND/NOR gates and verify the truth tables.
2. Realization of Boolean Expressions using Logic Gates.
3. Design full adder & full subtractor using NAND/NOR gates and verify the truth table.
4. Verification of 4-bit Parallel adder /subtractor circuit.
5. Design of 4-bit gray to binary code converter.
6. Design any 4 variable functions using 8:1 Multiplexer and verify.
7. Design and realization 2-bit comparator.
8. Verification of 4-bit Magnitude comparator
9. Design full adder using 3*8 Decoder and verify.
10. Design T & D flip flops using JK flip flop and verify the truth table.
11. Verification of 4-bit Decade counter
12. Verification of Universal Shift Register

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Department of Electronics and Communication Engineering

BASIC SIMULATION LABORATORY

II B.Tech I Semester

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

After going through this course the student will be able to:

1. Evaluate the operation on signals and systems using arithmetic operations and transforms
2. Application of correlation and transforms on noise removal and signal extraction
3. Compute various statistical properties of a random noise and verify whether it is stationary
4. Determine the correlation & Convolution between Signals and sequences.
5. Validate the properties and waveform synthesis of various transforms

Minimum 10 experiments to be Simulated Using MATLAB:

1. Generation of various signals and sequences (Periodic and A periodic), such as Unit Impulse, Unit step, square, saw tooth, Triangular, Sinusoidal, Ramp, Sync.
2. Operations on Signals and Sequences such as Addition, multiplication, scaling, Shifting, Folding, computation of Energy and average power.
3. Finding the Even and Odd parts of Signal/sequence and Real and imaginary parts of signal.
4. Convolution between signals and sequences.
5. Auto correlation and cross correlation between signals and sequences.
6. Verification of Linearity and Time Invariance Properties of a given continuous/Discrete system.
7. Gibbs Phenomenon.
8. Finding the Fourier Transform of a given signal and plotting its magnitude and phase spectrum.
9. Waveform synthesis using Laplace Transform.
10. Locating the Zeros and Poles and plotting the Pole-Zero maps in S plane and Z-plane for the given transfer function.
11. Generation of Gaussian noise (Real and complex), Computation of its mean, M.S. value and its Skew, Kurtosis, and PSD, probability distribution function.
12. Sampling Theorem Verification.

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Department of Electronics and Communication Engineering

ELECTRONIC DEVICES AND ANALOG CIRCUITS LABORATORY

II B.Tech I Semester

L	T	P	C
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COURSE OUTCOMES:

After going through this course the student will be able to:

1. Outline the characteristics of different semiconductor devices.
2. Interpret the ripple factor, regulations of rectifiers.
3. Sketch the frequency response of small signal amplifiers.
4. Understand the concepts of UJT and observe its characteristics.
5. Design and analyze oscillator circuits.

Note: Minimum of 10 experiments:

1. Forward & Reverse Bias Characteristics of PN Junction Diode.
2. Zener diode characteristics.
3. Half Wave Rectifier with & without filters.
4. Full Wave Rectifier with & without filters.
5. Input & Output Characteristics of Transistor in CE Configuration
6. FET characteristics.
7. Clippers Clampers
8. UJT Characteristics
9. Frequency Response of CE Amplifiers
10. Frequency Response of Common Source FET amplifier.
11. Two Stage RC Coupled amplifiers
12. RC Phase shift Oscillator.

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COURSE STRUCTURE FOR B.TECH II YEAR II SEMESTER

II YEAR II SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1	A224010	Complex analysis and Fourier transforms	3	0	0	3
2	A224408	Electromagnetic Waves	3	0	0	3
3	A224409	Analog and Digital Communications	3	0	0	3
4	A224410	Linear IC Applications	2	0	0	2
5	A224411	Electronic Circuit Analysis	3	0	0	3
6	A224484	Analog and Digital Communications Laboratory	0	0	2	1
7	A224485	Linear IC Applications Laboratory	0	0	2	1
8	A224486	Electronic Circuit Analysis Laboratory	0	0	2	1
9	A2244P1	Real Time Project/Field Based Project	0	0	4	2
10	A224011	Quantitative methods & logical reasoning	2	0	0	1
		Total Credits	16	0	10	20

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Department of Electronics and Communication Engineering

ELECTROMAGNETIC WAVES

II B.Tech II Semester

L	T	P	C
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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Understand the electric field due to different charge distributions.
2. Demonstrate the EM Field Characteristics divergence and curl of fields.
3. Interpret the Maxwell's equations for static Electric and Magnetic fields and dynamic Electromagnetic fields.
4. Analyze the behavior of EM waves in different media.
5. Solving Engineering problems on EM Wave Propagation.

UNIT I: Electrostatics-I:

Introduction to Vectors and Coordinate system- Coulomb 's Law- Electric Field Intensity – Fields due to Different Charge Distributions, Electric Flux Density, Gauss Law and Applications, Electric Potential, Relation between E and V, Maxwell's Two Equations for Electrostatic Fields, Energy Density in Electrostatics.

Unit-II: Electrostatics-II:

Convection and Conduction Currents, Dielectric Constant, Dielectric polarization, Linear, Isotropic and Homogeneous Dielectrics, Continuity Equation, Relaxation Time, Poisson 's and Laplace 's Equations, Capacitors – Parallel Plate, Coaxial, Spherical, boundary conditions in static electric field, Uniqueness theorem.

UNIT III: Magneto statics:

Biot-Savart's Law-Magnetic Field Intensity due to finite length current carrying conductor, Ampere's Circuit Law and Applications, Magnetic Flux Density, Maxwell's two Equations for Magneto static Fields, Magnetic Scalar and Vector Potentials, Forces due to Magnetic Fields, Ampere's Force Law, Magnetostatic boundary conditions.

UNIT IV: EM Wave Characteristics -I (Time-Varying Maxwell's Equations):

Faraday's Law and Transformer EMF, Inconsistency of Ampere's Law and Displacement Current Density, Maxwell's Equations in Different Forms, Time-varying Boundary Conditions at a Surface -Dielectric-Dielectric and Dielectric-Conductor Interfaces, Lorentz condition for Time-varying potentials.

UNIT V: EM Wave Characteristics -II:

Wave Equations for Conducting and Perfect Dielectric Media, Uniform Plane Waves – Definitions, Relation between E & H, Sinusoidal Variations, Wave Propagation in Conductor & Dielectric media, Polarization. Reflection and Refraction of Plane Waves – Normal and Oblique Incidences for both Perfect Conductor and Perfect Dielectrics, Brewster Angle, Critical Angle and Total Internal Reflection, Surface Impedance, Poynting Vector and Poynting Theorem.

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TEXT BOOKS:

1. Matthew N.O. Sadiku and S.V. Kulkarni, Elements of Electromagnetic, Tata McGraw Hill, 2020.
2. E.C. Jordan and K.G. Balmain, Electromagnetic Waves and Radiating Systems, PHI, 2000.

REFERENCE BOOKS:

1. Engineering Electromagnetics – William H. Hayt Jr. and John A. Buck, McGrawHill, 2014.
2. Engineering Electromagnetics – Nathan Ida, Springer (India) Pvt. Ltd., New Delhi, 2005.

1. Roy
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Department of Electronics and Communication Engineering

ANALOG AND DIGITAL COMMUNICATIONS

II B.Tech II Semester

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COURSE OUTCOMES:

After going through this course the student can

1. Demonstrate fundamental knowledge in Elements of Analog and Digital Communication systems.
2. Analyze different types of analog and digital modulation systems and calculate total power & bandwidth.
3. Design an efficient Transmitter and Receiver based on SNR, bandwidth and equipment complexities.
4. Formulate and solve engineering problems in the core area of analog and digital communications in developing information transmitting systems and telemetry system.
5. Illustrate the impact of noise in analog communication systems and computation of Probability of error in digital modulation techniques

UNIT-I:

Amplitude Modulation:

Review of signals and systems, Amplitude Modulation: Time and Frequency domain representations – Power and Bandwidth, AM Generators: Square law modulator - Switching modulator, AM Detectors: Square law detector - Envelope detector, DSB-SC Modulation: Time domain and frequency domain representations, DSB-SC Generators: Balanced Modulators - Ring Modulator, DSB-SC Detectors: Coherent detector - COSTAS Loop, SSB Modulation: Time and Frequency domain representations - SSB Generators and Detectors, VSB Modulation: Time and Frequency domain representation
- Envelope detection of a VSB wave, Comparison and Applications of different AM Systems, Frequency Division Multiplexing.

UNIT-II:

Frequency Modulation:

Angle Modulation: Time domain representation - Single tone FM wave - NBFM and WBFM - Spectral analysis of single tone FM Wave - Power and Bandwidth, FM Generators: Indirect FM and Direct FM Generators, FM Detectors: Balanced Frequency discriminator - Ratio detector, Pre-emphasis & De-emphasis, Threshold effect, Comparison of FM between AM.

UNIT-III:

Pulse Analog and Digital Modulations:

Pulse Analog Modulation:

Sampling process, Pulse Amplitude Modulation and Demodulation, Pulse Width Modulation and demodulation, Pulse Position Modulation and Demodulation, Time Division Multiplexing.

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Department of Electronics and Communication Engineering

Pulse Digital Modulation:

Quantization process, Pulse Code Modulation (PCM), Differential PCM (DPCM), Delta Modulation (DM), Inter Symbol Interference (ISI) - Nyquist criterion, Optimal detection of digital signals

UNIT-IV:

Pass band Digital Modulations:

ASK - PSK - DPSK - FSK - QAM, Probability of error, Optimal Coherent detection of PSK and FSK

UNIT-V:

Noise in Communication Systems:

Output SNR & Noise Figure in Analog modulation systems: AM - DSBSC - SSB - FM, Output SNR in PCM and DM systems, Comparison of PCM and DM systems.

TEXT BOOKS:

1. Haykin S, Communications Systems, Wiley, 2001.
2. Sam Shanmugam, Digital and Analog Communication Systems, Wiley, 2006.

REFERENCE BOOKS:

1. Proakis J G, Digital Communications, McGraw Hill, 2000.
2. Kennedy - Davis, Electronics and Communication Systems, McGraw Hill 4th Edition, 2006.

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Department of Electronics and Communication Engineering

LINEAR IC APPLICATION

II B.Tech II Semester

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COURSE OUTCOMES:

Upon completing this course, the student will be able to

1. Understand the overview of IC classifications and comparison of analog and digital IC's.
2. Understand the operating principle of operational amplifiers and working of its IC version.
3. Acquire the knowledge of active filter basics and applications of op-amps.
4. Attain the knowledge of functional diagrams and applications of IC555 and IC565.
5. Acquire the knowledge about the Data converters.

UNIT-I

Integrated Circuits: Classification, chip size and circuit complexity, basic information of Op-amp, ideal and practical Op-amp, internal circuits, Op-amp characteristics, DC and AC Characteristics, 741 op-amp and its features, modes of operation-inverting, non-inverting, differential.

UNIT-II

Op-amp and Applications: Basic information of Op-amp, instrumentation amplifier, ac amplifier, V to I and I to V converters, Sample & hold circuits, multipliers and dividers, differentiators and integrators, comparators, Schmitt trigger, Multivibrators, introduction to voltage regulators, features of 723.

UNIT-III

Active Filters & Oscillators: Introduction, 1st order LPF, HPF filters, Band pass, Band reject and all-pass filters. Oscillator types and principle of operation-RC, Wien and quadrature type, waveform generators-triangular, saw tooth, square wave and VCO.

UNIT-IV

Timers & Phase Locked Loops: Introduction to 555 timer, functional diagram, monostable and astable operations and applications, Schmitt Trigger. PLL - introduction, block schematic, principles and description of individual blocks of 565.

UNIT-V

D-A and A-D Converters: Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, and IC 1408 DAC, Different types of ADCs - Parallel comparator type ADC, counter type ADC, successive approximation ADC dualSlope integration type ADC, DAC and ADC specifications.

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Department of Electronics and Communication Engineering

TEXT BOOKS:

1. Roy Choudhury D, Shail B. Jain, Linear Integrated Circuit, New Age International, 2012
2. Ramakant A. Gayakwad, OP-AMP and Linear Integrated Circuits, Prentice Hall India, 2012.

REFERENCE BOOKS:

1. Tony Chan Carusone, David A. Johns, Kenneth W. Martin, Analog Integrated Circuit Design, John Wiley & Sons Ltd, 2011.
2. Joseph Carr, Linear IC Applications: A Designer's Handbook, Reed Educational and Publishing Pvt.Ltd, 1996
3. S. Salivahanan, V.S. Kanchana Bhaskaran, Linear IC Applications, McGra Hill, 2020

1. Roy
2. Shail B. Jain
3. D. Choudhury
4. Ramakant A. Gayakwad
5. S. Salivahanan

6. Tony Chan Carusone
7. David A. Johns
8. Kenneth W. Martin
9. Joseph Carr
10. S. Salivahanan

11. V.S. Kanchana Bhaskaran
12. McGra Hill
13. S. Salivahanan
14. V.S. Kanchana Bhaskaran



Department of Electronics and Communication Engineering

ELECTRONIC CIRCUIT ANALYSIS

II B.Tech II Semester

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COURSE OUTCOMES:

Upon completing this course, the student will be able to

1. Design the power amplifiers
2. Design the tuned amplifiers and analyse its frequency response
3. Design Multivibrators and sweep circuits for various applications.
4. Design and understanding of time-base generators.
5. Utilize the concepts of synchronization, frequency division and sampling gates.

UNIT-I

Large Signal Amplifiers: Class A Power Amplifier- Series fed and Transformer coupled, Conversion Efficiency, Class-B Power Amplifier-Push Pull and Complimentary Symmetry configurations, Conversion Efficiency, Principle of operation of Class-AB and Class-C and D Amplifiers.

UNIT-II

Tuned Amplifiers: Introduction, single Tuned Amplifiers – Q-factor, frequency response, Double Tuned Amplifiers–Q-factor, frequency response, Concept of stagger tuning and synchronous tuning

UNIT-III

Multivibrators: Analysis and Design of Bistable, Monostable, Astable Multivibrators and Schmitt trigger using Transistors.

UNIT-IV

Time Base Generators: General features of a Time base Signal, Methods of Generating Time Base Waveform, concepts of Transistor Miller and Bootstrap Time Base Generator, Methods of Linearity improvement.

UNIT-V

Synchronization and Frequency Division: Pulse Synchronization of Relaxation Devices, Frequency division in Sweep Circuits, Stability of Relaxation Devices, Astable Relaxation Circuits, Monostable Relaxation Circuits, Synchronization of a Sweep Circuit with Symmetrical Signals, Sine wave frequency division with a Sweep Circuit, A Sinusoidal Divider using Regeneration and Modulation.

Sampling Gates: Basic operating principles of Sampling Gates, Unidirectional and Bi-directional Sampling Gates, Four Diode Sampling Gate, Reduction of pedestal in Gate Circuits

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TEXTBOOKS:

1. Jacob Millman, Christos C Halkias-Integrated Electronics, McGraw Hill Education.
2. J.Millman, H.Taub and Mothiki S.Prakash Rao- Pulse, Digital and Switching Waveforms-2nd Ed., TMH, 2008.

REFERENCEBOOKS:

1. David A.Bell-Electronic Devices and Circuits, 5th Ed., Oxford.
2. Robert L. Boylestead, Louis Nashelsky Electronic Devices and Circuits theory, 11th Ed., Pearson, 2009
3. Ronald J. Tocci-Fundamentals of Pulse and Digital Circuits, 3rd Ed., 2008.
4. David A. Bell-Pulse, Switching and Digital Circuits, 5th Ed., Oxford, 2015.

1. Rao
2. Balaji
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6. A. Ashwin
7. N. Venkatesh
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Department of Electronics and Communication Engineering

ANALOG AND DIGITAL COMMUNICATIONS LABORATORY

II B.Tech II Semester

L	T	P	C
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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Demonstrate knowledge in different Analog and Digital Communication Systems.
2. Compare the characteristics of various Analog and Digital modulation schemes and analyze the performances.
3. Develop various analog and digital modulation and demodulation systems
4. Explain how Pulse code modulation is applied to transform an analog signal into a digital one and transmitted through the digital communication network.
5. Design the shift keying based digital modulation techniques for the transmission of digital information

Note: Any 10 experiments to be conducted

1. Amplitude modulation and demodulation
2. DSBSC modulation and demodulation
3. SSB modulation and demodulation
4. Frequency modulation and demodulation
5. Pulse Amplitude Modulation and demodulation
6. Pre-emphasis and De-emphasis
7. Verification of Sampling Theorem
8. Pulse code modulation and demodulation
9. Delta modulation and demodulation
10. PSK Modulation and demodulation
11. FSK Modulation and demodulation
12. DPSK and QPSK Modulation and demodulation

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Department of Electronics and Communication Engineering

LINEAR IC APPLICATIONS LABORATORY

II B.Tech II Semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Design and analyze filter circuits using op-amp.
2. Design and analyze the various linear application of op-amp.
3. Design and analyze the various application of 555 timer.
4. Design and analyze the performance of oscillators.
5. Design and analyze IC 565, IC 723 and converters.

Note: Minimum 10 Experiments have to be conducted

1. Design of 2-input adder and subtractor using IC 741.
2. Design of Integrator and Differentiator circuit using IC 741.
3. Design of first order LPF and HPF using IC 741.
4. Design of Astable multivibrator using IC 741.
5. Design of triangular wave generator using IC 741.
6. Design of RC phase shift oscillator using IC 741.
7. Design of Wein-bridge oscillator using IC 741.
8. Analyze the operation of Weighted resistor type DAC and R-2R Ladder type DAC
9. Design Astable Multivibrator Circuit using IC 555 Timer.
10. Design Monostable Multivibrator Circuit using IC 555 Timer.
11. Calculation of Capture Range & Lock Range Using IC 565 PLL
12. Design a Voltage Regulator using IC 723.

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Department of Electronics and Communication Engineering

ELECTRONICS CIRCUIT ANALYSIS LABORATORY

II B.Tech II Semester

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COURSE OUTCOMES:

Upon completing this course, the students will be able to

1. Design power amplifiers and find its efficiency
2. Design tuned amplifiers and find its Q-factor
3. Design various multivibrators and sweep circuits.
4. Understand the Schmitt trigger response for various gain
5. Design the unidirectional bidirectional Sampling gates

Note: Minimum 10 Experiments have to be conducted

1. Design transformer coupled class A power amplifier and draw the input and output waveforms find its efficiency
2. Design class B power amplifier and draw the input and output waveforms, and find its efficiency.
3. Design the complementary symmetry push-pull amplifier and draw the input and output waveforms.
4. Design class C power amplifier and draw the input and output waveforms
5. Design a single tuned amplifier and determine the Q of its tuned circuit practically.
6. Design a Bistable Multivibrator and draw the wave forms at base and collector of transistors.
7. Design an Astable Multivibrator and draw the wave forms at base and collector of transistors.
8. Design a Monostable Multivibrator and draw the input and output waveforms
9. Design and Analyze the operation of Schmitt trigger circuit.
10. Design a Bootstrap sweep circuit using BJT and draw its output time base waveform
11. Design a Miller sweep circuit using BJT and draw its output time base waveform.
12. Design unidirectional and bidirectional sampling gates

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Department of Electronics and Communication Engineering

ECE - Subjects offered to Other Departments - R22

S.No.	Year / Semester	Name of the Department	Name of the Subjects
1	II - I	IT and AI-DS	Digital Electronics
2	II - I	IT and AI-DS	Digital Electronics Laboratory

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Department of Electronics and Communication Engineering

DIGITAL ELECTRONICS

II B.Tech I Semester (IT & AI-DS)

L	T	P	C
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Course Outcomes:

Student will be able to:

1. Understand various number systems, conversions, range and error detecting and correcting codes and their significance.
2. Evaluate the minimization of logic gates using Boolean algebraic principles and k-maps.
3. Design various simple and complex combinational circuits with real time applications.
4. Analyze the basic principles behind Flip flops & the design of sequential circuits with real time applications.
5. Illustrate various types of memory devices and their design.

UNIT - I:

Number Systems: Binary, Octal, Hex Decimal, and Conversions, range; Binary additions and subtractions (using 1c, and 2c), concept of overflow; representations of negative numbers using 1's and 2's complement and range; BCD numbers: Representation of 8421, 2421, Ex-3, Gray and self complementary codes; additions and subtractions on 8421 codes; Error detecting codes: even, odd parity, hamming codes; Error correcting codes: hamming codes,

UNIT - II:

Boolean Algebra and Digital Logic GATES, Basic Boolean laws and properties; Boolean functions; canonical and standard forms (SOP, POS); Gate minimization using three and four variable K-Map's with and without don't cares.

UNIT - III:

Definition of combinational circuits, Encoders, Decoders, Multiplexers, D-Multiplexers; design procedure for half, full adder and subtractor, decimal (8421) adder, Combinational Circuit Design for BCD code converters;

UNIT - IV:

Sequential circuits, latches, Flip Flops; Analysis of clocked sequential circuits, State Reduction and Assignment, Register, Ripple Counters, Synchronous Counters design, Other Counters-Ring Counter, Johnson Counter.

UNIT - V:

Types of Memory - Main memory - random access memory, ROM, Types of ROM; RAM, Types of RAM, Sequential memories, PLD, PLA, PAL, PROM.

TEXT BOOKS:

1. M. Morris Mano, Digital Design, Third Edition, Pearson Education/PHI, 2001.
2. A. Anand Kumar, Fundamentals of Digital Circuits, PHI Edition, 2006.

REFERENCE BOOKS:

1. John F. Wakerly, Digital Design: Principles and Practices, 4th Edition, Pearson / Prentice Hall, 2005.
2. Roth, Fundamentals of Logic Design, Fifth Edition, Thomson, 2004

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Department of Electronics and Communication Engineering

Logic Design
DIGITAL ELECTRONICS LABORATORY

II Year B.Tech. I Semester (IT & AI-DS)

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

After going through this course the student will be able to:

1. Analyze the functionality

Note: Minimum 10 Experiments have to be conducted

List of Experiments:

1. Verify the functionality of logic gates
2. Verification of De-Morgan's laws
3. Implementation and verification of full adder and full subtractor using logic gates.
4. Implementation and verification of 4X1 multiplexer & Demultiplexer using logic gates.
5. Implementation and verification of 2X4 Decoder and 1X4 De-multiplexer using logic gates.
6. Implementation of given function and verification using IC 74LS151 (8X1 multiplexer).
7. Verify the functionality of Flip-flops.
8. Design and verify the 4-bit ripple counter.
9. Design and verify the decade counter.
10. Verify the functionality of 4-bit magnitude comparator using IC 74LS85.
11. Verify the functionality of Universal Shift Register IC 74LS194/195.

Handwritten signatures and marks for the experiments:

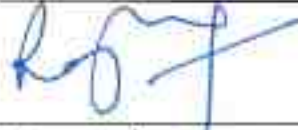
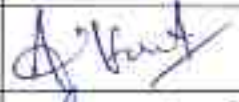
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Department of Electronics and Communication Engineering
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Minutes of the Board of studies in ECE meeting held on 22nd January 2024 at 11.00 AM in Board Room.

The following members were present in the meeting.

S.No	Name of the Member	Designation	Signature
1.	Dr. M. Rajendra Prasad, Professor & Head ECE, Vidya Jyothi Institute of Technology	Chairman	
2.	Dr. T.SatyaSavithri, Professor, JNTUH CEH	JNTUH Nominee	
3.	Dr. P. Chandra Sekhar, Professor & Vice Principal, University College of engineering, Osmania.	Member	
4.	Dr. A. Nageswar Rao, Senior Manager (Design), SLRDC, Communication Department, HAL, Hyderabad.	Member	
5.	Mr. A. Hariharan, Associate Director, PACTRA EDGE, Hyderabad.	Member	
6.	Dr. P. Nageswara Rao, Professor & HOD Vardhaman College of Engineering.	Member	
7.	Dr. S. Sathees Kumaran, Professor, Anurag University	Member	
8.	Dr. S. Thulasi Prasad, Professor, ECE, VJIT.	Member	
9.	Dr. P. Ganesan, Professor, ECE, VJIT	Member	
10.	Dr. V. G. Siva Kumar, Assoc. Professor, ECE, VJIT.	Member	
11.	Dr. M. Vadivel, Assoc. Professor, ECE, VJIT.	Member	
12.	Dr. S. Pradeep Kumar Reddy, Assoc. Professor, ECE, VJIT.	Member	
13.	Dr. B. Prudhvi Nadh, Assistant Professor, ECE, VJIT.	Member	
14.	Mrs. A. Jaya Lakshmi, Assoc. Professor, ECE, VJIT.	Member	



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BoS Members' Signatures:

S.No	Name of the Member	Designation	Signature
1.	Dr. M. Rajendra Prasad, Professor & Head ECE, Vidya Jyothi Institute of Technology	Chairman	
2.	Dr. T. Satya Savithri, Professor, JNTUH CEH	JNTUH Nominee	
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13.	Dr. B. Prudhvi Nadh, Assistant Professor, ECE, VJIT.	Member	
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14.	Mrs. A. Jaya Lakshmi, Assoc. Professor, ECE, VJIT.	Member	



RESOLUTIONS

1. All the previous minutes were incorporated as per previous BOS meeting

Noted and Approved

Item No. 1: Approval of B.Tech. (ECE) III and IV Year Course Structure of R-22 regulations

The chairman of BoS presented a detailed course structure of B.Tech.(ECE) III and IV Years of R-22 regulations.

Resolution (1): The members after a detailed discussion approved the course structure of B.Tech. (ECE) III and IV Years of R-22 regulations.

Noted and Approved.

Item No. 2: Approval of B.Tech. (ECE) III and IV Year Syllabi of R-22 regulations

The Chairman presented a detailed syllabus of B.Tech.(ECE) III and IV Year R-22 regulations.

Resolution (2): The members after thorough discussion approved the syllabi of B.Tech. (ECE) III and IV Years R22 regulations.

Noted and Approved

Item No. 3: Approval of Panel of examiners

The Chairperson emphasized the necessity of Panel of Examiners. Their services will be utilized in the preparation of End-Semester Question paper(s) and Evaluation of End-Semester Examination Answer Scripts. They will be paid remuneration as per the recommendations of College Finance Committee.

Resolution (3): The committee on BoS in ECE to prepare the Panel of examiners in consultation with the senior teachers for all the B.Tech (ECE) courses under R-22 regulations. The same may be submitted to the Examination branch (Autonomous) for further processing.

Noted and Approved.

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I B.Tech. I Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1.	A221001	Mathematics-I(Linear Algebra & Calculus)	3	1	0	4
2.	A221002	Applied Physics	3	1	0	4
3.	A221501	C Programming for Engineers	3	0	0	3
4.	A221003	English for Skill Enhancement	2	0	0	2
5.	A221381	Engineering Workshop	0	1	3	2.5
6.	A221401	Elements of Electronics and Communication Engineering	0	0	2	1
7.	A221081	Applied Physics Laboratory	0	0	3	1.5
8.	A221082	English Language and Communication Skills Laboratory	0	0	2	1
9.	A221581	C Programming for Engineers Laboratory	0	0	2	1
10.		Induction Programme				
		Total	11	3	12	20

I B.Tech. II Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1.	A221005	Mathematics-II (Ordinary Differential Equations and Vector Calculus)	3	1	0	4
2.	A221006	Engineering Chemistry	3	1	0	4
3.	A222303	Engineering Graphics & Modeling	1	0	4	3
4.	A222204	Basic Electrical Engineering	2	0	0	2
5.	A222402	Electronic Devices and Circuits	2	0	0	2
6.	A222583	Python Programming Laboratory	0	2	2	3
7.	A222084	Engineering Chemistry Laboratory	0	0	2	1
8.	A222283	Basic Electrical Engineering Laboratory	0	0	2	1
		Total	11	4	10	20

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II B.Tech. I Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1	A223403	Analog Circuits	3	0	0	3
2	A223404	Network analysis and Transmission Lines	3	0	0	3
3	A223405	Digital Logic Design	3	0	0	3
4	A223406	Signals and Systems	3	1	0	4
5	A223407	Probability Theory and Stochastic Processes	3	0	0	3
6	A223481	Digital logic Design Laboratory	0	0	2	1
7	A223482	Basic Simulation Laboratory	0	0	2	1
8	A223483	Electronic Devices and Analog Circuits Laboratory	0	0	2	1
9	A223009	Professional Communication	2	0	0	1
		Total Credits	17	1	6	20

II B.Tech. II Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1	A224010	Complex analysis and Fourier transforms	3	0	0	3
2	A224408	Electromagnetic Waves	3	0	0	3
3	A224409	Analog and Digital Communications	3	0	0	3
4	A224410	Linear IC Applications	2	0	0	2
5	A224411	Electronic Circuit Analysis	3	0	0	3
6	A224484	Analog and Digital Communications Laboratory	0	0	2	1
7	A224485	Linear IC Applications Laboratory	0	0	2	1
8	A224486	Electronic Circuit Analysis Laboratory	0	0	2	1
9	A2244P1	Real Time Project/Field Based Project	0	0	4	2
10	A224011	Quantitative methods & logical reasoning	2	0	0	1
		Total Credits	16	0	10	20

1. Prof 2. Aditya 3. K. S. 4. Aditya 5. A. K. 6. K. S. 7. A. K. 8. G. P. 9. A. K. 10. A. K. 11. A. K. 12. A. K. 13. A. K. 14. A. K.



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III B.Tech. I Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1		Microprocessors and Microcontrollers	3	1	0	4
2		Control Systems	3	1	0	4
3		Antennas and Wave Propagation	3	0	0	3
4		Professional Elective-I	3	0	0	3
		1. Computer Organization and Operating Systems				
		2. Data Communications and Computer Networks				
		3. Information Theory and Coding				
5		Open Elective-I	3	0	0	3
		1. Introduction to Micro controllers				
		2. Electronic Measuring Instruments				
		3. Digital Electronics for Engineering				
6		Microprocessors and Microcontrollers Laboratory	0	0	2	1
7		Advanced English Communication Skills Laboratory	0	0	2	1
8		Antenna Laboratory	0	0	2	1
9		Environmental Science	2	0	0	0
Total Credits			17	2	6	20

III B.Tech. II Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1		Digital Signal Processing	3	0	0	3
2		Business Economics & Financial Analysis	3	0	0	3
3		CMOS VLSI Design	3	0	0	3
4		Professional Elective-II	3	0	0	3
		1. Digital Image Processing				
		2. Mobile Communications and Networks				
		3. Embedded System Design				
5		Open Elective-II	3	0	0	3
		1. Fundamentals of Internet of Things				
		2. Electronics for Health Care				
		3. Telecommunications for Society				
6		VLSI Laboratory	0	0	2	1
7		Embedded system Design Laboratory	0	0	2	1
8		Digital Signal Processing Laboratory	0	0	2	1
9		Industry Oriented Mini Project/Internship	0	0	4	2
10		Gender Sensitization	2	0	0	0
Total Credits			17	0	10	20

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IV B.Tech. I Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1		Microwave and Optical Communications	3	1	0	3
2		Electronic Measurements and Instrumentation	3	0	0	3
3		Professional Elective-III	3	0	0	3
		1.Radar Systems				
		2.CMOS Analog IC Design				
		3.Artificial Neural Networks				
4		Professional Elective-IV	3	0	0	3
		1.Network Security and Cryptography				
		2.Satellite Communications				
		3.Biomedical Instrumentation				
5		Open Elective-III	3	0	0	3
		1. Principles of Signal Processing				
		2. Communication Technologies				
		3. IOT for Real Time Applications				
6		Microwave and Optical Communications Laboratory	0	0	4	2
7		Project Stage-I	0	0	6	3
Total Credits			15	1	10	20

IV B.Tech. II Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1		Professional Elective-V	3	0	0	3
		1.Artificial Intelligence				
		2.5G and beyond Communication				
		3.Machine Learning				
2		Professional Elective-VI	3	0	0	3
		1.Multimedia Database Management Systems				
		2.System On-Chip Architecture				
		3.Wireless Sensor Networks				
3		Comprehensive Viva-Voce	0	0	0	3
4		Project Stage-II	0	0	22	9
5		Technical Seminar	0	0	0	2
Total Credits			6	0	22	20

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III B.Tech. I Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1		Microprocessors and Microcontrollers	3	1	0	4
2		Control Systems	3	1	0	4
3		Antennas and Wave Propagation	3	0	0	3
4		Professional Elective-I	3	0	0	3
		1.Computer Organization and Operating Systems				
		2.Data Communications and Computer Networks				
		3.Information Theory and Coding				
5		Open Elective-I	3	0	0	3
		4. Introduction to Micro controllers				
		5. Electronic Measuring Instruments				
		6. Digital Electronics for Engineering				
6		Microprocessors and Microcontrollers Laboratory	0	0	2	1
7		Advanced English Communication Skills Laboratory	0	0	2	1
8		Antenna Laboratory	0	0	2	1
9		Environmental Science	2	0	0	0
		Total Credits	17	2	6	20

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MICRO PROCESSORS AND MICRO CONTROLLERS

III B.Tech I Semester

L	T	P	C
3	1	0	4

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Acquire knowledge about Microprocessors, Microcontroller and its need.
2. Ability to identify basic architecture of different Microprocessors & Microcontroller
3. Develop systems for interfacing of different peripheral devices microprocessor & Microcontrollers
4. Write a program to interface microprocessor and microcontroller for different applications.
5. Develop microcontroller application for different domain

UNIT-I:

8086 Architecture:

8086 Architecture-Functional diagram, Register Organization, Memory Banks, Memory Segmentation, Programming Model, memory addresses, Physical Memory Organization, Signal descriptions of 8086, Minimum & Maximum Mode configuration of 8086, Timing diagrams.

UNIT-II:

Instruction Set and Assembly Language Programming of 8086:

Instruction formats, Addressing modes, Instruction Set, Assembler Directives, Macros, Simple Programs involving Logical, Branch and Call Instructions, sorting, Evaluating Arithmetic Expressions, String Manipulations.

UNIT-III:

Interfacing RAM with 8086:

8255 PPI – Modes – Interfacing with 8086, 8251 – Modes - Interfacing with 8086, 8259 Interfacing with 8086, 8257 DMA – Modes, Interfacing 8086 with Stepper Motor and DAC 0800.

UNIT-IV:

Introduction to Microcontrollers:

Overview of 8051 Microcontroller, Architecture, I/O Ports, Memory Organization, Addressing Modes and instruction set of 8051

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UNIT-V:

8051 Real Time Control:

Timers/Counters, Serial Communication and Interrupts; Programming 8051 Timers and Counters, Programming the Serial Communication interrupts, Programming Timer Interrupts, Programming external Hardware Interrupts.

TEXT BOOKS:

1. Ray A K and Bhurchandani K M, Advanced Microprocessors and Peripherals , Tata McGraw-Hill, 2006.
2. Kenneth. J. Ayala, The 8051 Microcontroller ,Cengage Learning, 2005

REFERENCE BOOKS:

1. D. V. Hall, Microprocessors and Interfacing, Tata McGraw-Hill, 2006
2. Uma Rao K., Andhe Pallavi , The 8051 Microcontrollers, Architecture and Programming and Applications, Pearson, 2009

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CONTROL SYSTEMS

III B.Tech I semester

L	T	P	C
3	1	0	4

COURSE OUTCOMES:

At the end of the Course, the student will be able to:

1. Understand and analyzing different linear-time-invariant systems using transfer function.
2. Analyze system response in time domain for first and second order systems and evaluate static error.
3. Understand the concept of stability and its assessment for linear-time invariant systems.
4. Analyze system response in frequency domain and understanding compensation networks.
5. Realize the concept of state variable, state space and analyze the stability of linear Time discrete systems.

UNIT I:

Introduction to control Systems:

Concepts of control systems, open & closed loop control systems-examples, Industrial Control systems examples, Mathematical models of physical systems.

Transfer Function Representation: Block diagram representation of systems considering electrical systems as examples- Block diagram algebra – Representation by signal flow graph- Reduction using Mason's gain formula

UNIT II:

Time Response Analysis: Standard test signals – Time response of first order systems- Characteristic Equation of feedback controls systems, Transient response of second order systems – Time domain specifications – Steady state response – Steady state errors & error constants – Effects of proportional derivative, proportional integral systems and PID controllers. Application of Proportional, Integral and Derivative Controllers.

UNIT III:

Stability analysis in S- Domain:

The concept of stability – Routh's stability criterion – qualitative stability & conditional stability – limitations of Routh's stability

Root Locus technique: The root locus concept – construction of root loci-effects of adding poles and zeros to $G(s)H(s)$ on the root loci.

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UNIT IV:

Frequency-response analysis:

Relationship between time and frequency response, Polar plots, Bode plots, Nyquist stability criterion, Relative stability using Nyquist criterion – gain and phase margin, Closed-loop frequency response, Introduction to compensation techniques

UNIT V:

State space analysis:

Concepts of state, state variables and state model, derivation of state models from block diagrams, Diagonalization-Solving the Time invariant state Equation- State Transition Matrix and it's Properties-Concepts of Controllability and Observability.

TEXT BOOKS:

1. Nagor kani A, Control Systems Engineering, CBS PUB & DIST, 2020
2. Nagrath IJ & Gopal M, Control Systems Engineering, New Age International, 2009.

REFERENCE BOOKS:

1. A.Anand Kumar, Control Systems, PHI Publications, Second Edition, 2014.
2. Jagan N.C, Control Systems, BS Publications, 2014.

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ANTENNA AND WAVE PROPAGATION

III B.Tech I semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Understand different antennas, field analysis and their applications to antenna elements.
2. Distinguish the mechanism of radiation, different antenna characteristics, mathematical relations their estimates in practical cases.
3. Analyze and design the working of different antenna's and to interpret the radiation pattern of planar arrays from the knowledge of linear arrays.
4. Obtain the capability to differentiate and report the electromagnetic radiation levels in the Atmosphere and any radio transmissions.
5. Design Microwave antenna Systems from specification

UNIT I:

Antenna Basics:

Basic Antenna Parameters – Patterns, Beam Area, Radiation Intensity, Beam Efficiency, Directivity-Gain-Resolution, Antenna Apertures, Effective Height, Fields from Oscillating Dipole, Field Zones, Front - to-back Ratio, Antenna Theorems, Radiation, Retarded Potentials – Helmholtz Theorem.

Thin Linear Wire Antennas:

Radiation from Small Electric Dipole, Quarter Wave Monopole and Half Wave Dipole – Current Distributions, Field Components, Radiated Power, Radiation Resistance, Beam Width, Directivity, Effective Area and Effective Height, Natural Current Distributions, Far Fields and Patterns of Thin Linear Centre-fed Antennas of Different Lengths. Loop Antennas - Small Loop, Comparison of Far Fields of Small Loop and Short Dipole, Radiation Resistances and Directivities of Small Loops (Qualitative Treatment).

UNIT II:

Antenna Arrays:

Point Sources – Definition, Patterns, arrays of 2 Isotropic Sources - Different Cases, Principle of Pattern Multiplication, Uniform Linear Arrays – Broadside Arrays, End fire Arrays.

Antenna Measurements:

Introduction, Concepts - Reciprocity, Near and Far Fields, Coordinate System, Sources of Errors, Patterns to be Measured, Directivity Measurement, Gain Measurements (by Comparison, Absolute and 3-Antenna Methods).

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UNIT III:

VHF, UHF and Microwave Antennas - I:

Arrays with Parasitic Elements, Yagi-Uda Array, Folded Dipoles and their Characteristics, Helical Antennas – Helical Geometry, Helix Modes, Horn Antennas – Types, Fermat's Principle, Optimum Horns.

VHF, UHF and Microwave Antennas – II:

Microstrip Antennas – Introduction, Features, Advantages and Limitations, Rectangular Patch Antennas – Geometry and Parameters, Characteristics of Microstrip Antennas, Reflector Antennas – Introduction, Flat Sheet and Corner Reflectors, Paraboloidal Reflectors – Geometry, Pattern Characteristics, Feed Methods, Reflector Types – Related Features.

UNIT IV:

Wave Propagation:

Definitions, Categorizations and General Classifications, Different Modes of Wave Propagation, Ray/Mode Concepts.

Ground Wave Propagation:

Plane Earth Reflections, Space and Surface Waves, Wave Tilt, Curved Earth Reflections.

Space Wave Propagation:

Field Strength Variation with Distance and Height, Effect of Earth's Curvature, Absorption, Super Refraction, M-Curves and Duct Propagation, Scattering Phenomena, Troposphere Propagation.

UNIT V:

Sky Wave Propagation:

Structure of Ionosphere, Refraction and Reflection of Sky Waves by Ionosphere, Ray Path, Critical Frequency, MUF, LUF, OF, Virtual Height and Skip Distance, Relation between MUF and Skip Distance, Multi-hop Propagation.

TEXT BOOKS:

1. Kraus J D, Marhefka R J and Ahmad S. Khan , Antennas and Wave Propagation, Tata McGraw-Hill, 2010.
2. Jordan E C and Balmain K G, Electromagnetic Waves and Radiating Systems, Prentice Hall India, 2000.

REFERENCE BOOKS:

1. Balanis C A, Antenna Theory, John Wiley, 2005.
2. K.D. Prasad & Satya Prakashan, Antennas and Wave Propagation, Tech India Publications, 2001.

1. Roy 2. Shetty 3. J. 4. Jagan 5. A. K. 6. Suresh 7. R. 8. S. 9. S. 10. S. 11. S. 12. S. 13. S. 14. S.



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COMPUTER ORGANIZATION & OPERATING SYSTEMS
(Professional Elective-I)

III B.Tech. I Semester

L	T	P	C
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Course Outcomes:

After completion of this course the student will able to

1. Visualize the organization of different blocks in a computer.
2. Utilize the micro-level operations to control different units in a computer.
3. Understand the memory system and different ways of communicating with IO devices and standard IO interfaces.
4. Understand the design of memory unit, learn the overview of operating system.
5. Learn about major activities of an OS with regard to file management and the operating system functions.

UNIT-I

Basic Structure of Computers: Computer Types, Functional Units, Basic Operation Concepts, Bus Structures, Software, Performance, Multiprocessors and Multi Computers, Data Representation, Fixed Point Representation, Floating Point Representation.

Register Transfer Language and Micro Operations: Register Transfer Language, Register Transfer, Bus and Memory Transfer, Arithmetic Micro Operations, Logic Micro Operations, Shift Micro Operations, Arithmetic Logic Shift Unit.

UNIT-II:

Basic Computer Organization and Design:

Instruction Codes, Computer Registers, Computer Instructions, Instruction Cycle, Timing and Control, Memory Reference Instructions, Input, Output and Interrupt.

Central Processing Unit:

Stack Organization, Instruction Formats, Addressing Modes, Data Transfer and Manipulation, Program Control, Reduced Instruction Set Computer (RISC).

UNIT-III:

The Memory System: Basic Concepts, Semiconductor RAM, Types Of Read Only Memory (ROM), Cache Memory, Performance Considerations, Secondary Storage RAID, Direct Memory Access (DMA).

Input-Output Organization: Peripheral Devices, Input-Output Interface, Asynchronous Data Transfer Modes, Priority Interrupt, Input-Output Processor (IOP), Introduction to Peripheral Components, Interconnect (PCI) Bus, Introduction to Standard Serial Communication Protocols like RS232, USB, IEEE1394.

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UNIT - IV:

Operating Systems Overview: Overview of Computer Operating Systems- Functions, Protection and Security, Distributed Systems, Special Purpose Systems, Operating Systems Structures Operating System Services and Systems Calls, System Programs, Operating System Generation.

Memory Management: Swapping, Contiguous Memory Allocation, Paging, Structure of Paging Table, Segmentation, Virtual Memory, Demand Paging, Page-Replacement Algorithms, Allocation of Frames, Deadlock characterization.

UNIT - V:

File System Interface: The Concept of a File, Access Methods, Directory Structure, File System Mounting, File Sharing, Protection.

File System Implementation: File System Structure, File system Implementation, Directory Implementation, Allocation Methods, Free-Space Management.

TEXT BOOKS:

1. Carl Hamacher, ZvonksVranesic, SaeedZaky - Computer Organization, 5th Edition, McGraw Hill.
2. M. Moris Mano -Computer Systems Architecture, 3rd Edition, Pearson
3. Abraham Silberchatz, Peter B. Galvin, Greg Gagne -Operating System Concepts, 8th Edition, John Wiley.

REFERENCE BOOKS:

1. William Stallings- Computer Organization and Architecture , 6th Edition, Pearson
2. Stallings - Operating Systems – Internals and Design Principles, 6th Edition, Pearson Education, 2009,
3. Principles of Operating Systems, B.L. Stuart, Cengage Learning, India Edition.

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DATA COMMUNICATIONS AND COMPUTER NETWORKS
(Professional Elective-I)

III B.Tech. I Semester

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Course Outcomes: Upon completing this course, the student will be able to

1. Know the Categories and functions of various Data communication Networks
2. Design and analyze various error detection techniques.
3. Demonstrate the mechanism of routing the data in network layer
4. Know the significance of various Flow control and Congestion control Mechanisms
5. Know the Functioning of various Application layer Protocols.

UNIT – I: INTRODUCTION TO DATA COMMUNICATIONS:

Components, Data Representation, Data Flow, Networks- Distributed Processing, Network Criteria, Physical Structures, Network Models, Categories of Networks Interconnection of Networks, The Internet - A Brief History, The Internet Today, Protocol and Standards - Protocols, Standards, Standards Organizations, Internet Standards. Network Models, Layered Tasks, OSI model, Layers in OSI model, TCP/IP Protocol Suite.

UNIT – II: DATA LINK LAYER:

Links, Access Networks, and LANs- Introduction to the Link Layer, The Services Provided by the Link Layer, Types of errors, Redundancy, Detection vs Correction, Forward error correction Versus Retransmission Error-Detection and Correction Techniques, Parity Checks, Check summing Methods, Cyclic Redundancy Check (CRC), Framing, Flow Control and Error Control protocols, HDLC, Multiple Access Protocols, Random Access, ALOHA, Controlled access, MAC Protocol.

UNIT – III: THE NETWORK LAYER:

Introduction, Forwarding and Routing, Network Service Models, Virtual Circuit and Datagram Networks-Virtual-Circuit Networks, Datagram Networks, Origins of VC and Datagram Networks, Inside a Router-Input Processing, Switching, Output Processing, Queuing, The Routing Control Plane, The Internet Protocol (IP): Forwarding and Addressing in the Internet-Datagram format, Ipv4 Addressing, Internet Control Message Protocol (ICMP), Ipv6

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UNIT – IV: TRANSPORT LAYER:

Introduction and Transport Layer Services : Relationship Between Transport and Network Layers, Overview of the Transport Layer in the Internet, Multiplexing and Demultiplexing, Connectionless Transport: UDP -UDP Segment Structure, UDP Checksum, Principles of Reliable Data Transfer-Building a Reliable Data Transfer Protocol, Pipelined Reliable Data Transfer Protocols, Go-Back-N(GBN), Selective Repeat(SR), Connection Oriented Transport: TCP- The TCP Connection, TCP Segment Structure, Congestion Control.

UNIT – V: APPLICATION LAYER:

Principles of Networking Applications – Network Application Architectures, Processes Communicating, Transport Services Available to Applications, Transport Services Provided by the File Transfer: FTP - FTP Commands and Replies, Electronic Mail in the Internet- STMP, Comparison with HTTP, DNS-The Internet's Directory Service – Service Provided by DNS, Overview of How DNS Works, DNS Records and messages.

TEXTBOOKS:

1. Kurose James E, Keith W- Computer Networking A Top-Down Approach, 6th Edition, Pearson.
2. Behrouz A. Forouzan - Data Communications and Networking, 4th Edition, McGraw-Hill Education

REFERENCES:

1. Bhusan Trivedi - Data communication and Networks, Oxford university press, 2016
2. Andrew S Tanenbaum - Computer Networks, 4th Edition, Pearson Education
3. W. A. Shay - Understanding Communications and Networks, 3rd Edition, Cengage Learning.

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INFORMATION THEORY AND CODING
(Professional Elective-I)

III B.Tech. I Semester

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COURSE OUTCOMES:

At the end of the course, students will be able to:

1. Understand the concept of information theory, coding techniques and errors related to it.
2. Compare the different coding techniques.
3. Formulate codes using different coding techniques
4. Apply different coding techniques to develop an error free communication system.
5. Inspect error detection and correction in various coding technique.

UNIT-I:

Basics of Information Theory:

Entropy, Entropy for discrete ensembles, Information rate, source coding: Shannon's noiseless coding theorem, Shannon's noisy coding theorem, Mutual Information, Shannon- Hartley law

UNIT-II:

Source Coding:

Encoding of the Source Output, Shannon's Encoding Algorithm, Shannon Fano Encoding Algorithm, Huffman codes, Extended Huffman coding, Arithmetic Coding, Lempel – Ziv Algorithm, Calculations of Channel capacity and bounds for Discrete Channel, Applications to continuous channels

UNIT-III:

Information Channels:

Communication Channels, Channel Models, Channel Matrix, Joint probability Matrix, Binary Symmetric Channel, System Entropies, Mutual Information, Channel Capacity, Channel Capacity of : Binary Symmetric Channel, Binary Erasure Channel, Morgan's Theorem, Continuous Channels

UNIT-IV:

Error Control Coding:

Examples of Error control coding, methods of Controlling Errors, Types of Errors, Linear Block Codes: matrix description of Linear Block Codes, Error Detection and Error Correction Capabilities of Linear Block Codes, Single Error Correcting hamming Codes, Binary Cyclic Codes: Algebraic Structure of Cyclic Codes, Encoding using an (n-k) Bit Shift register, Syndrome Calculation, Error Detection and Correction

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UNIT-V:

Convolution Arithmetic Codes:

Convolution Encoder, Time domain approach, Transform domain approach, Code Tree, Trellis and State Diagram, Viterbi Algorithm

TEXT BOOKS:

1. Abramson N, Information and Coding, McGraw Hill, 1963.
2. Mansurpur M, Introduction to Information Theory, McGraw Hill, 1987.

REFERENCE BOOKS:

1. Ash R B, Information Theory, Prentice Hall, 1970.
2. Chitode J S, Information Theory and coding, Technical publication, 2009.

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Interpret the internal organization of 8051 with its unique features.
2. Understand the basic concept of various addressing modes, instruction set and simple Assembly programs
3. Provide Knowledge about special function registers in 8051.
4. Understand the fundamental concept of interfacing.
5. Study the basic concepts of Advanced ARM processors

UNIT-I:

8051 Microcontrollers:

Introduction-Microprocessors & microcontrollers- Comparison - RISC, CISC, Harvard and Von Neumann architectures-Architecture- Pin Description – I/O Ports- PC, DPTR, PSW register, Memory organization

UNIT-II:

8051 Addressing modes: Introduction – addressing modes in 8051 – Immediate, Register, Direct, Register indirect, indexed and implied addressing modes.

Basic assembly language programming concepts: Assembly language program format- Instruction set of 8051- Simple programs.

UNIT-III:

Special Function Registers: Timer and Counters in 8051 – Modes of operation – TCON, TMOD, - Serial communication in 8051 – SCON, SBUF, PCON registers, Interrupt in 8051, Interrupt enable register. Interrupt priority registers - Simple programs.

UNIT-IV:

Interfacing the 8051: Interfacing LCD, Matrix keyboard, 7 Segment display, Analog to Digital Converter, Digital to analog converter, Stepper motor with 8051

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UNIT-V:

ARM Architecture: Introduction to ARM-Features of ARM-ARM Processor family-ARM Processor categories- Application of ARM-ARM Architecture – Register & Special registers-PSR, APSR, IPSR, EPSR, PRIMASK and control register- ARM instruction set.

TEXT BOOKS:

1. The 8051 Microcontroller, Kenneth, J. Ayala, Cengage Learning, 3rd Ed, 2004.
2. Raj Kamal , Microcontrollers Architecture, Programming, Interfacing and System Design, Pearson, 2005.

REFERENCE BOOKS:

1. Mazidi and Mazidi, The 8051 Microcontroller and Embedded Systems, Prentice Hall India, 2000.
2. ARM System Developers guide, Andrew N SLOSS, Dominic SYMES, Chris WRIGHT, Elsevier, 2012

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Department of Electronics and Communication Engineering

ELECTRONIC MEASURING INSTRUMENTS
(Open Elective-I)

III B.Tech I Semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Acquire knowledge in Characteristics of Instruments, measurement of electrical quantities
2. Measure and analyze the signals using CRO,
3. Study the performance electronic systems using signal generators and wave analyzers
4. Acquire knowledge on Transducers and understanding their applications in real time world
5. Understanding the acquisition and storing data of non-electrical and electrical quantities

UNIT-I: Measurements and measuring systems:

Functional Diagram of Instrumentation System, Static characteristics: Accuracy - Precision - Resolution - Sensitivity - measurement Errors, Dynamic Characteristics: Speed of response - fidelity - Lag - Dynamic error - Statistical Analysis, Basic meter movement, DC Ammeters: Multi-range, DC voltmeters: Multi-range, AC voltmeters: Rectifier type - Thermocouple Type, Ohmmeters: Series type and Shunt type, Multimeter

UNIT-II: Oscilloscopes:

Oscilloscope block diagram, Cathode Ray Tube, Vertical Deflection System, Horizontal Deflection System, Dual Beam CRO, Dual Trace CRO, Measurements with CRO: Amplitude - Time period - Frequency - Phase, Lissajous patterns, Sampling Oscilloscope, Analog Storage Oscilloscope, Digital Storage Oscilloscope,

UNIT-III: Signal Generators and Signal analyzers:

Signal Generators:

Function generator, RF signal generators, Sweep frequency generator,

Signal analyzers:

Wave analyzers: Frequency Selective Wave Analyzer - Heterodyne Wave Analyzer, Total Harmonic Distortion Analyzer, Spectrum Analyzer.

UNIT-IV: Transducers:

Classification of Transducers, Strain gauges, Capacitive Transducers, Linear Variable Differential Transducer (LVDT), Piezoelectric Transducer, Thermocouple, Thermistor, Measurement of Displacement, Velocity, Acceleration, Vibration, Force, Pressure, Fluid Flow, Liquid Level and Temperature.

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UNIT-V: Bridges and Recorders:

Bridges:

Wheat Stone Bridge, Kelvin Bridge, Maxwell Bridge, Schering Bridge and Wien Bridge

Recorders:

Magnetic disk and tape Recorders, LED, LCD, Dot matrix display, Strip Chart Recorder, X-Y Recorder

TEXT BOOKS:

1. Albert D. Helfrick, Cooper William D, Modern Electronic Instrumentation and Measurement Techniques, Prentice-Hall of India, 1997.
2. Kalsi H S, Electronic instrumentation, Tata McGraw Hill, 2015.

REFERENCE BOOKS:

1. David A. Bell, Electronic Instrumentation & Measurements, Prentice-Hall, 2003.
2. Sawhney A K, Puneet Sawhney, A Course in Electrical and Electronic Measurements and Instrumentation, Dhanpat Rai & Company, 2016

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DIGITAL ELECTRONICS FOR ENGINEERING
(Open Elective-I)

III B.Tech I Semester

L	T	P	C
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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Demonstrate the basic theorems of Boolean algebra, logic gates, combinational and sequential circuits and memories.
2. Analyze the combinational and sequential circuits and memories.
3. Design of logic circuits
4. Understanding the concepts of sequential circuits
5. Demonstrate the basic counter circuits

UNIT-I:

Number System and minimization techniques:

Number System:

Review of number system and base conversion, complements, signed binary numbers, Floating point number representation, Error detection & Correction (Hamming Code), Boolean Algebra, postulates

UNIT-II:

Minimization techniques

Basic logic gates, Canonical and Standard Form, Minimization of switching function using theorem, The Karnaugh Map Method-Up to Four Variable Maps.

UNIT-III:

Combinational Circuits:

Adders & Subtractor, Binary Adder-Subtractor, Decimal Adder, Binary Multiplier, Magnitude Comparators, Multiplexers, De-multiplexers, Decoders, Encoders Hazards and Hazard Free Relations.

UNIT-IV:

Sequential circuits-I:

Basic Architectural Distinctions between Combinational and Sequential circuits, Latches, Flip Flops: SR, JK, Race Around Condition in JK, JK Master Slave, D and T Type Flip Flops, Excitation Table of all Flip Flops, Design of a Clocked Flip-Flop, Timing and Triggering Consideration, Clock Skew, Conversion from one type of Flip-Flop to another.

Unit-V:

Sequential Circuits-II:

Synchronous – Asynchronous – Comparison, Design of Single mode Counter, Ripple Counter, Ring Counter, Shift Register, Shift Register Sequences, Ring Counter Using Shift Register, MOD Counters

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TEXT BOOKS:

1. Morris Mano, Digital Design, Prentice Hall India, 2006
2. A Anand Kumar, Switching Theory and Logic Design, Prentice Hall India, 2013

REFERENCE BOOKS:

1. Jain N P, Modern Digital Electronics, Tata McGraw Hill, 2007.
2. Fredriac J. Hill, Gerald R. Peterson, Introduction to Switching Theory and Logic Design
Wiley, 1981

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MICRO PROCESSORS AND MICRO CONTROLLERS LABORATORY

III B.Tech I Semester

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COURSE OUTCOMES:

After going through this course the student will be able to;

1. Apply the fundamentals of assembly level programming of microprocessors and microcontrollers.
2. Build a program on a microprocessor using instruction set of 8086 and 8051.
3. Evaluate Assembly language program for 8086 and 8051 microcontroller to interface peripheral devices for simple applications
4. Develop assembly language programs for various applications using 8051 microcontroller
5. Understand the development of prototype using combination of hardware and software

Note: Minimum 12 Experiments have to be conducted

1. Introduction to MASM.
2. Programs for 16 bit Arithmetic Operations for 8086.
3. Program for sorting an array for 8086.
4. Program for searching a number or character in a string for 8086.
5. Programs for String Manipulations for 8086.
6. Interfacing to 8086 and programming to control Stepper Motor.
7. Interfacing ADC to 8086.
8. Interfacing DAC to 8086.
9. Serial Communication between Two Microprocessors using 8255.
10. Programming using Arithmetic, Logical and Bit Manipulation Instructions of 8051.
11. Program and verify timer/counter in 8051
12. Program and verify interrupt handling in 8051
13. UART operation in 8051
14. Interfacing LCD to 8051.
15. Data transfer from peripheral to memory through DMA Controller 8237/8257

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ANTENNA LABORATORY

III B.Tech. I Semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Illustrate the different ways of measuring antenna parameters.
2. Differentiate the different Radiation pattern of the antennas
3. Study the characteristics of Antennas
4. Articulate the performance of Antennas based on the measurements
5. Formulate an antenna design using Antenna technologies.

LIST OF THE EXPERIMENTS: (Perform any 10 experiments)

1. Measurement of parameters of Half wave Dipole Antenna
2. Measurement of parameters of monopole Antenna
3. Measurement of parameters of large loop Antennas
4. Measurement of parameters of small loop Antennas
5. Measurement of parameters of Helical Antenna
6. Measurement of parameters of 2- element Antenna Array
7. Measurement of parameters of Yagi-Uda Antenna
8. Measurement of parameters of Folded Dipole Antenna
9. Performance analysis of N-element linear array
10. Design and simulation of Crossed Yagi Antenna
11. Study of performance of 2x2 Helical Antenna Array
12. Design and analysis Planar reflector antenna
13. Design and study of 3x3 dipole Antenna Array

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III B.Tech. II Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1		Digital Signal Processing	3	0	0	3
2		Business Economics & Financial Analysis	3	0	0	3
3		CMOS VLSI Design	3	0	0	3
4		Professional Elective-II	3	0	0	3
		4. Digital Image Processing				
		5. Mobile Communications and Networks				
		6. Embedded System Design				
5		Open Elective-II	3	0	0	3
		4. Fundamentals of Internet of Things				
		5. Electronics for Health Care				
		6. Telecommunications for Society				
6		VLSI Laboratory	0	0	2	1
7		Embedded system Design Laboratory	0	0	2	1
8		Digital Signal Processing Laboratory	0	0	2	1
9		Industry Oriented Mini Project/Internship	0	0	4	2
10		Gender Sensitization	2	0	0	0
		Total Credits	17	0	10	20

1 Prof 2 Salim 3 Ali 4 Hafeez 5 A. H. 6 Imran
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DIGITAL SIGNAL PROCESSING

III B.Tech. II Semester

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Define the concepts of Fourier transforms, digital filters with their effect of errors.
2. Illustrate speed and memory requirements of Fourier transforms on signals.
3. Relate the effects of finite word length on systems.
4. Formulate frequency filtering, impulse response filters with its structure.
5. Ability to understand various applications of DSP such as multi rate signal processing, telecommunication

UNIT-I:

Introduction to DSP- applications-advantages

Discrete Fourier Transform:

DTFT, DFT-Complexity calculation- Properties of DFT- linear convolution- Circular convolution- Sectioned convolution- Relation between DTFT, DFS, DFT and Z-Transform.

UNIT-II:

Fast Fourier Transform:

Fast Fourier Transform (FFT), Radix-2 decimation-in-time and decimation-in-frequency FFT Algorithms, Inverse FFT- Convolution of sequences using FFT

UNIT-III:

IIR Digital Filters:

Analog filter approximations –Butterworth and Chebyshev- Design of IIR digital filters from analog filters- Impulse invariant technique – warping effect- bilinear transformation method – Spectral transformations, realization of IIR filters- direct, canonic, cascade and parallel forms.

UNIT-IV:

FIR Digital Filters:

Characteristics of FIR Digital filters - frequency response – Gibbs Phenomenon- Design of FIR filters - window techniques – Frequency Sampling - Comparison of IIR and FIR filters, realization of FIR filters- direct& cascade forms

UNIT-V:

Finite Word Length Effects:

Quantization- Quantization error- Types- Limit cycles- Overflow oscillations –Scaling

Multirate Signal Processing: Introduction - down sampling- Decimation – up sampling – Interpolation -Sampling Rate Conversion

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11. R. Jyoti 12. S. Jyoti 13. R. Jyoti 14. S. Jyoti 15. A. Jyoti



TEXT BOOK:

1. John G. Proakis, Digital Signal Processing, Principles, Algorithms, and Applications, Pearson, 2007.
2. Tarun Kumar Rawat, Digital Signal Processing, Oxford Publications, 2015

REFERENCES BOOKS:

1. Andreas Antoniou, Digital Signal Processing, TATA McGraw Hill, 2006
2. Ashok Ambardar, Digital Signal Processing, Cenage Learning, 2007.

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CMOS VLSI DESIGN

III B.Tech. II Semester

L	T	P	C
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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Enumerate different steps involved in Integrated Circuits technology for MOS transistor and explain the primary and secondary effects of MOSFET and BICMOS.
2. Summarize the fabrication process involved in VLSI circuits
3. Outline the design process involved in VLSI design flow for design of MOS transistors.
4. Understand and apply the concepts of memories in design.
5. Design digital circuits using Verilog HDL.

UNIT-I:

Introduction:

Introduction to IC Technology, Fabrication process of NMOS, PMOS CMOS and BiCMOS

Basic Electrical Properties:

$I_{ds} - V_{ds}$ relationships, MOS transistor Threshold Voltage- V_t , Transconductance- g_m , g_{ds} , figure of merit- ω_0 , Pass transistor, NMOS Inverter, Various pull ups, Ratioed Circuits CMOS Inverter analysis and design, Bi-CMOS Inverters.

UNIT-II:

VLSI Circuit Design Processes:

VLSI Design flow, MOS Layers - Stick Diagrams - Lambda(λ)-based Design rules - wires - Contacts - Transistors- Layout Diagrams - NMOS - PMOS - CMOS Inverters - Gates - Scaling of MOS circuits.

UNIT-III:

Gate Level Design:

Logic Gates - Pass transistors, Transmission gate- Switch logic - Alternate gate circuits, Latches- Time delays - Driving large capacitive loads - Wiring capacitance, Fan - in, Fan - out, Choice of layers.

Programmable Logic Devices:

PROM - PLA - PAL-Design Approach - CPLDs - FPGA -Parameters influencing low power design.

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UNIT-IV:

Introduction to Verilog HDL:

Overview of Digital Design with Verilog HDL, typical HDL-flow, Concurrency, Simulation and Synthesis, Functional verification;

Gate Level Modeling:

Introduction, Modeling using basic Verilog gate primitives, description of AND, OR, NOT type gates, Design of Flip – Flops with Gate Primitives, Delays

UNIT-V:

Dataflow Modeling:

Continuous assignments, delay specification, expressions, operators, operands, operator types;

Behavioral Modeling:

Structured procedures, initial and always, blocking and non-blocking statements, The Case Statement, for Loop, While Loop, Design of Flip flop, Shift register

TEXT BOOKS:

1. Kamran Eshraghian, Douglas A. Pucknell, Sholeh Eshraghian, Essentials of VLSI Circuits and Systems, Prentice, 2005.
2. T.R. Padmanabhan, Digital Design Through Verilog HDL, Wiley, 2003.

REFERENCE BOOKS:

1. John P. Uyemura, CMOS logic circuit Design, Springer, 1999.
2. Lal Kishore K, Prabhakar V S V, VLSI Design, I.K International, 2010.

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DIGITAL IMAGE PROCESSING
(Professional Elective-II)

III B.Tech. II Semester

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. State the Digital Image Fundamentals and operation associated with various stages of image processing.
2. Illustrate the mathematics involved in various stages of image processing.
3. Demonstrate the operations various stages of image processing.
4. Contrast the different types of operation and its impact on images.
5. Understand the anatomy of image compression in Image Transmission

UNIT-I:

Fundamentals of Image Processing:

Elements of Digital Image Processing Systems – Image sensing and Acquisition- Elements of Visual Perception – structure of human eye – light- luminance- brightness and contrast- image formation- Basic steps of image processing- Sampling -Quantization and Digital Image representation - Basic relationships between pixels – neighborhood, adjacency, connectivity, distance measures..

UNIT-II:

Image Enhancement in Spatial & frequency domain:

Image Enhancement in Spatial domain:

Introduction-Point Processing-Histogram processing- Arithmetic and logical operations- Fundamentals of Spatial filtering-masking-Spatial filters for Smoothing - Spatial filters for Sharpening.

Image Enhancement in Frequency domain:

Need for transform-Basics of filtering in frequency domain-Image smoothing in frequency domain-Image sharpening in frequency domain

UNIT-III:

Image Restoration:

Introduction- Degradation model –Noise models-Spatial domain filtering for restoration- Mean Filters – Order Statistics filters – Adaptive filters –frequency domain filtering for noise removal - Band reject Filters – Band pass Filters – Notch Filters –Degradation function estimation- Inverse filtering – Wiener filter.

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UNIT-IV:

Image Segmentation and Morphological processing:

Image Segmentation:

Segmentation concepts - Point - Line - Edge Detection-Thresholding based segmentation- Local-Global and Adaptive Thresholding- Region based segmentation-Region growing-Region splitting and merging.

Morphological processing:

Introduction- structuring element - erosion - dilation - Opening - closing.

UNIT-V:

Image Compression:

Introduction-Redundancy in images-Fidelity Criteria-Image compression model-Lossless compression-Huffman coding -Lossless Predictive coding- Lossy compression- lossy predictive coding- Transform coding -Image compression standards- JPEG and JPEG 2000.

TEXT BOOKS:

1. Rafael C. Gonzales, Richard E. Woods, Digital Image Processing, Pearson, 2007.
2. Anil K. Jain, Fundamentals of Digital Image Processing, Pearson, 2005.

REFERENCE BOOKS:

1. Rafael C. Gonzalez, Richard E. Woods, Richard Eugene Woods, Steven L. Eddins, Digital Image processing using MATLAB, Tata McGraw Hill, 2010.
2. William K Pratt, Digital Image Processing, John Wiley & Sons, 2002.

1. Prof 2. Dattatraya 3. Ravi 4. Nagaraj 5. A. K. S. 6. Anurag 7. P. 8. S. P. 9. S. 10. S. 11. Anand 12. Prof 13. B. S. 14. S. P.



MOBILE COMMUNICATIONS AND NETWORKS
(Professional Elective-II)

III B.Tech. II Semester

L	T	P	C
3	0	0	3

Course Outcomes: Upon completing this course, the student will be able to:

1. Known the evolution of cellular and mobile communication system.
2. Explore the Co-Channel and Non-Co-Channel interferences.
3. Known how to overcome the different fading effects?
4. Familiar with cell coverage for signal and traffic, diversity, techniques, frequency management, Channel assignment and types of handoff.
5. Demonstrate the difference between cellular and Adhoc Networks and design goals of MAC Layer protocol.

UNIT - I

Introduction to Cellular Mobile Radio Systems: Limitations of Conventional Mobile Telephone Systems, Basic Cellular Mobile System, First, Second, Third and Fourth Generation Cellular Wireless Systems, Uniqueness of Mobile Radio Environment-Fading-Tie Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time.

Fundamentals of Cellular Radio System Design: Concept of Frequency Reuse, Co-Channel Interference, Co-Channel Interference Reduction Factor, Desired C/I from a Normal Case in a Omni Directional Antenna System, System Capacity Improving Coverage and Capacity in Cellular Systems-Cell Splitting, Sectoring, Microcell Zone Concept.

UNIT - II

Co-Channel Interference: Measurement of Real Time Co-Channel Interference, Design of Antenna System, Antenna Parameters and their effects, diversity techniques-space diversity, polarization diversity, frequency diversity, time diversity.

Non Co-Channel Interference: Adjacent Channel Interference, Near end far end interference, cross talk, effects on coverage and interference by power decrease, antenna height decrease, effects of cell site components.

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UNIT - III

Cell Coverage for Signal and Traffic: Signal Reflections in flat and Hilly Terrain, effects of Human Made Structures, phase difference between direct and reflected paths, constant standard deviation, straight line path loss slope, general formula for mobile propagation over water and flat open area, near and long-distance propagation, path loss from a point to point prediction model in different conditions, merits of lee model.

Frequency Management and Channel Assignment: Numbering and Grouping, Setup Access and Paging Channels, Channel Assignments to Cell Sites and Mobile Units.

UNIT - IV

Handoffs and Dropped Calls: Handoff Initiation, types of Handoff, Delaying Handoff, advantages of Handoff, Power Difference Handoff, Forced Handoff, Mobile Assisted and Soft Handoff, Intersystem handoff, Introduction to Dropped Call Rates and their Evaluation.

UNIT - V

Ad Hoc Wireless Networks: Introduction, Cellular and Ad Hoc wireless Networks, Applications and Ad Hoc Wireless Networks, Issues in Ad Hoc Wireless Networks, Ad Hoc Wireless Internet, MAC Protocols for Ad Hoc Wireless, Introduction, issues in designing AMAC Protocol for Ad Hoc wireless Networks, Design Goals of AMAC protocol for Ad Hoc Wireless Networks, Classification of MAC Protocols.

TEXT BOOKS:

1. W.C.Y. Lee - Mobile Cellular Telecommunications, 2nd edition, Mc Graw Hill, 1989.
2. Theodore. S. Rapoport - Wireless Communications, 2nd editon, Pearson Education, 2002.

REFERENCE BOOKS:

1. C. Siva ram Murthy and B.S. Manoj - Ad Hoc Wireless Networks: Architectures and Protocols, PHI, 2004.
2. Simon Haykin, Michael Moher - Modern Wireless Communications, Pearson Education, 2005.
3. Vijay Garg - Wireless Communications and Networking, Elsevier Publications, 2007.
4. Andrea Goldsmith - Wireless Communications-, Cambridge University Press, 2005.

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III B.Tech. II Semester

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Course Outcomes:

At the end of the course the student should be able to

1. Expected to understand the selection procedure of Processors in the embedded domain.
2. Design Procedure for Embedded Firmware.
3. Expected to visualize the role of Real time Operating Systems in Embedded Systems
4. Expected to evaluate the Correlation between task synchronization and latency issues
5. To Enumerate the need for Task Communications in a Multiprocessor Environment

UNIT I:

Introduction to Embedded Systems:

Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

UNIT II:

Typical Embedded System:

Core of the Embedded System: General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

UNIT III:

Embedded Firmware:

Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

UNIT IV:

RTOS Based Embedded System Design:

Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

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UNIT V:

Task Communication:

Shared Memory, Message Passing, Remote Procedure Call and Sockets; Task Synchronization:
Task Communication/Synchronization Issues, Task Synchronization Techniques, Device
Drivers, How to Choose an RTOS.

TEXT BOOKS:

1. Shibu K V, Introduction to Embedded Systems, McGraw-Hill Education, 2009.
2. Lyla B. Das, Embedded Systems an Integrated Approach, Pearson Education, 2013.

REFERENCE BOOKS:

1. Raj Kamal, Embedded systems architecture, programming and design, McGraw-Hill Education, 2003
2. Frank Vahid, Tony Givargis, Embedded System Design A Unified Hardware/Software Introduction, John Wiley, 2003

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Department of Electronics and Communication Engineering

FUNDAMENTALS OF INTERNET OF THINGS
(Open Elective-II)

B.Tech. ECE III Year II Semester

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Course Outcomes: Upon completing this course, the students will be able to

- Know basic protocols in sensor networks.
- Program and configure Arduino boards for various designs.
- Python programming and interfacing for Raspberry Pi.
- Design IoT applications in different domains.

UNIT – I Introduction to Internet of Things: Characteristics of IoT, Physical design of IoT, Functional blocks of IoT, Sensing, Actuation, Basics of Networking, Communication Protocols, Sensor Networks.

UNIT - II Machine-to-Machine Communications: Difference between IoT and M2M, Interoperability in IoT, Introduction to Arduino Programming, Integration of Sensors and Actuators with Arduino.

UNIT – III Introduction to Python programming: Introduction to Raspberry Pi, Interfacing Raspberry Pi with basic peripherals, Implementation of IoT with Raspberry Pi.

UNIT - IV Implementation of IoT with Raspberry Pi: Introduction to Software defined Network (SDN), SDN for IoT, Data Handling and Analytics.

UNIT - V Cloud Computing: Sensor-Cloud, Smart Cities and Smart Homes, Connected Vehicles, Smart Grid, Industrial IoT.

Case Study: Agriculture, Healthcare, Activity Monitoring

TEXT BOOKS:

1. "The Internet 'of' Things: Enabling Technologies, Platforms, and Use Cases", by Pethuru Rai and Anupama C. Raman (CRC Press)
2. "Make sensors": Terokarvinen, kemo, karvinen and villeyvaltokari, 1st edition, maker media, 2014.
3. "Internet of Things: A Hands-on Approach", by Arshdeep Bahga and Vijay Madisetti

REFERENCE BOOKS:

1. Vijay Madisetti, Arshdeep Bahga, "Internet of Things: A Hands-On Approach"
2. Waltenegus Dargie, Christian Poellabauer, "Fundamentals of Wireless Sensor Networks: Theory and Practice"
3. Beginning Sensor networks with Arduino and Raspberry Pi – Charles Bell, Apress, 2013

3. Beginning Sensor networks with Arduino and Raspberry Pi – Charles Bell, Apress, 2013



ELECTRONICS FOR HEALTH CARE
(Open Elective-II)

III B.Tech, II Semester

L	T	P	C
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Course Outcomes: Upon completion of this course, the students will be able to

1. Know about health care data and its conversion to information and to knowledge.
2. Acquire knowledge on (Electronic Health Records) EHRs and their Implementation.
3. Understand the working of electronic devices used for the patient monitoring.
4. Know the concepts of Telemedicine and therapeutic devices used inside the human body
5. Understand the importance of Therapeutic devices

UNIT I:

Health care data, Information and Knowledge: Definitions and Concepts, Converting Data to Information to Knowledge, Clinical Data Warehouses, What makes Health Informatics Difficult, Why Health IT fails Sometimes, Terminology of Analytics, Challenges to Data Analytics, Research and application of analytics, Role of Informatics in analytics.

UNIT II:

Electronic Health Records: Introduction, Need for Electronic Health Records, Institute of Medicine's Vision for EHRs, Electronic Health Record Key Component, Electronic Prescribing, Electronic Health Record Adoption, Electronic Health Record Adoption and Meaningful use Challenges, Electronic Health Record Examples, Logical Steps to Selecting and Implementing an EHR

UNIT III:

Patient Monitoring Systems: System Concepts, Cardiac Monitor, Bedside Patient Monitoring Systems, Central Monitors, Measurement of Heart Rate, Measurement of Pulse Rate, Blood Pressure Measurement, Measurement of Temperature, Measurement of Respiration Rate, Catheterization Laboratory Instrumentation.

UNIT IV:

Biomedical Telemetry and Telemedicine: Wireless Telemetry, Single Channel Telemetry Systems, Multi-channel Wireless Telemetry Systems, Multi-patient Telemetry, Implantable Telemetry Systems, Transmission of Analog Physiological Signals, Over Telephone, Telemedicine.

UNIT V:

Therapeutic devices: Need for Cardiac Pacemaker, Implantable Pacemakers, DC Defibrillator, and Electronics in the Anaesthetic Machine.

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INSTITUTE OF TECHNOLOGY
AN AUTONOMOUS INSTITUTION

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Department of Electronics and Communication Engineering
(Accredited by NBA)

TEXT BOOKS:

1. Robert E. Hoyt MD FACP "Health Informatics" sixth edition 2007.
2. R. S. Kandpur "Biomedical Instrumentation Technology and Applications" second edition Tata McGraw-Hill.

REFERENCES:

1. Edward H. Shortliffe, James J. Cimino "Biomedical Informatics, Computer applications in Health care and Biomedicine" third edition Springer.
2. G.V.R.K. Acharyulu, Bhimaraya Metri, L. Kalyan Viswanath REDDY "Health care and Hospital Management Contemporary Issues and Strategies"

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TELECOMMUNICATIONS FOR SOCIETY
(Open Elective-II)

III B.Tech. II Semester

L	T	P	C
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Course Outcomes: Upon completion of this course, the students will be able to

1. Understand the concepts of simplex, half duplex, and full duplex of one-way and two-way circuits.
2. Get knowledge on subscriber loop design and VF repeaters of voice telephony.
3. Get brief overview of video transmission and its broadcasting standards in television transmission.
4. Know different of modes of television transmission.
5. Understand the concept of CATV.

UNIT I:

INTRODUCTORY TOPICS IN TELECOMMUNICATIONS:

End-Users, Nodes, and Connectivities, Telephone Numbering and Routing, Use Of Tandem Switches in a Local Area Connectivity, Introduction to the Busy Hour and Grade Of Service, Simplex, Half-Duplex, and Full Duplex, One-Way and Two-Way Circuits, Network Topologies, Variations in Traffic Flow, Quality Of Service.

UNIT II:

QUALITY OF SERVICE AND TELECOMMUNICATION IMPAIRMENTS:

Objective, Quality of Service: Voice, Data, and Image, Signal-to-Noise Ratio, Voice Transmission, Data Circuits, Video (Television), The Three Basic Impairments and How They Affect the End-User, Amplitude Distortion, Phase Distortion, Noise Level, Typical Levels, Echo and Singing.

UNIT III:

TRANSMISSION ASPECTS OF VOICE TELEPHONY:

Definition of the Voice Channel, Operation of the Telephone Subnet, Subscriber Loop Design, Design of Local Area Wire-Pair Trunks (Junctions), VF Repeater (Amplifiers).

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UNIT IV:

TELEVISION TRANSMISSION:

Background and Objectives, An Appreciation of Video Transmission, Critical Video Parameters, Video Transmission Standards (Criteria for Broadcasters), Methods of Program Channel Transmission, The Transmission of Video Over LOS Microwave, TV Transmission by Satellite Relay, Digital Television, Conference Television, Brief Overview of Frame Transport for Video Conferencing.

UNIT V:

COMMUNITY ANTENNA TELEVISION (CABLE TELEVISION):

Objective and Scope, The Evolution of CATV, System Impairments and Performance Measures, Hybrid Fiber-Coax (HFC) Systems, Digital Transmission of CATV Signals, Two-Way CATV Systems, Two-Way Voice and Data over CATV Systems Based on the DOCSIS 2.0 Specification.

TEXT BOOKS:

1. Roger L. Freeman "Fundamentals of Telecommunications" 2nd Edition, John Wiley & Sons Publications 2005.
2. Annabel Z. Dodd "The Essential Guide to Telecommunications" 5th Edition, Prentice Hall 2012.

REFERENCES:

1. JYRKI T. J. PENTTINEN "THE TELECOMMUNICATIONS HANDBOOK" John Wiley & Sons Publications 2015.
2. Prof. Dr. Muhammad EL-SABA "Telecommunications systems and data networks" 3rd Edition 2015.

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VLSI LABORATORY

III B.Tech, II Semester

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COURSE OUTCOMES:

At the end of the course the student should be able

1. To learn Hardware Descriptive Language (Verilog).
2. To learn the fundamental principles of Digital System Design using HDL and FPGA.
3. To learn the fundamental principles of VLSI circuit design in digital domain
4. To analyze various combinational and sequential circuits using Verilog HDL.
5. To provide hands on design experience with EDA platforms.

Perform any 10 Experiments:

1. Verification of Logic Gates
2. Verification of Demorgan's Law
3. Design of 8 to 1 multiplexer
4. Design of 1 to 8 Demultiplexer
5. Design of 2 to 4 Encoder
6. Design of 4-bit comparator
7. Design of 4 bit binary to gray converter
8. Design of full adder using 3 modeling styles
9. Design of flip flops SR, D, JK, and T
10. Design Ripple Counter
11. Design Modulo Counter
12. Design Shift Register
13. Design Inverter using PMOS / NMOS
14. Design of full adder using decoder and multiplexer
15. Design System using finite state Machine

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EMBEDDED SYSTEM DESIGN LABORATORY

III B.Tech. II Semester

L	T	P	C
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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Code the ARM cortex M0+ processor instruction set.
2. Articulate the concept of interfacing I/O devices with FRDM kit.
3. Simulate I/O Devices with Arduino using Tinkercad.
4. Simulate sensors with Arduino using Tinkercad.
5. Formulate a system design using Embedded technologies.

Perform any 10 Experiments:

Note: perform below experiments using FRDM Boards/Tinkercad Simulation.

1. Study of FRDM Development Board
2. Blinking of LED using FRDM Kit
3. Breath out 2 LEDs using FRDM Kit
4. Color Circle using FRDM Kit
5. Interface to Accelerometer sensor using FRDM kit
6. LED intensity control using touch sensor using FRDM kit
7. Simulate Potentiometer with Arduino using Tinkercad.
8. Simulate servomotor with Arduino using Tinkercad.
9. Simulate Ultrasonic sensor with Arduino using Tinkercad.
10. Simulate LDR with Arduino using Tinkercad.
11. Simulate 2-wire LCD with Arduino using Tinkercad.
12. Simulate SPI LCD with Arduino using Tinkercad.

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DIGITAL SIGNAL PROCESSING LABORATORY

III B.Tech. II semester

L	T	P	C
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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Formulate programs for performing time & frequency operation on signals and systems.
2. Design and implement impulse response filters and Multirate system for a given sequence
3. Analyze and Observe Magnitude and phase characteristics (Frequency response Characteristics) of digital IIR-Butterworth, Chebyshev filters
4. Analyze and Observe Magnitude and phase characteristics (Frequency response Characteristics) of digital FIR filters using window techniques
5. Develop various DSP Algorithms using MATLAB Software package

Note: Minimum 10 Experiments have to be conducted

1. Generation of Sinusoidal Waveform / Signal based on Recursive Difference Equations.
2. To find DFT / IDFT of given DT Signal.
3. To find Frequency Response of a System given in Transfer Function/ Differential equation form.
4. Implementation of FFT of given Sequence.
5. Determination of Power Spectrum of a given Signal(s).
6. Implementation of LP FIR Filter for a given Sequence/Signal.
7. Implementation of HP IIR Filter for a given Sequence/Signal.
8. Generation of Narrow Band Signal through Filtering.
9. Generation of DTMF Signals.
10. Implementation of Decimation Process.
11. Implementation of Interpolation Process.
12. Implementation of I/D Sampling Rate Converters.
13. Step and Ramp Response of First order and Second Order Systems.

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Department of Electronics and Communication Engineering
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IV B.Tech. I Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1		Microwave and Optical Communications	3	1	0	3
2		Electronic Measurements and Instrumentation	3	0	0	3
3		Professional Elective-III	3	0	0	3
		1.Radar Systems				
		2.CMOS Analog IC Design				
		3.Artificial Neural Networks				
4		Professional Elective-IV	3	0	0	3
		1.Network Security and Cryptography				
		2.Satellite Communications				
		3.Biomedical Instrumentation				
5		Open Elective-III	3	0	0	3
		4. Principles of Signal Processing				
		5. Communication Technologies				
		6. IOT for Real Time Applications				
6		Microwave and Optical Communications Laboratory	0	0	4	2
7		Project Stage-I	0	0	6	3
Total Credits			15	1	10	20

1. Prof. 2. Dr. 3. Dr. 4. Dr. 5. Dr. 6. Dr.

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MICROWAVE AND OPTICAL COMMUNICATIONS

IV B.Tech, I Semester

L	T	P	C
3	1	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Understands the basics of waveguides in the EM wave transmission and waveguide components for microwave frequencies.
2. Classify different types of Microwave Tubes and there functionality
3. Understand a Microwave test bench setup for parameters measurements and study the Microwave Solid State Devices
4. Analyze the concepts of LED, LASER, PIN, and APD's used in optical fibers.
5. Analyze fiber alignment, connectors and splicing methods of optical fibers Evaluate power coupling from transmitter to fibers and design of fiber optic link

UNIT I: WAVEGUIDES AND WAVEGUIDE COMPONENTS:

Microwave Spectrum and Band designation, Rectangular Waveguides - TE/TM mode analysis, Cut-off Frequencies, Dominant and Degenerate Modes, Impossibility of TEM Mode.

Waveguide Components: Attenuators-Resistive Card, Rotary Vane types, scattering matrix parameters: Properties, S- parameters of E-Plane, H-Plane, Magic TEE Junctions and Directional coupler, Ferrite Components - Faraday Rotation Law, Isolator, Circulator.

UNIT II: MICROWAVE TUBES:

Limitations and Losses of conventional Tubes at Microwave Frequencies, Re-entrant Cavities,

O-TYPE TUBES : 2 Cavity Klystrons – Velocity Modulation and Bunching process, Reflex Klystrons – Structure, principle of working.

HELIX TWTS: Significance, Types and Characteristics of Slow Wave Structures; Structure and operation of TWT.

M-TYPE TUBES

Introduction, Magnetrons – 8-Cavity Cylindrical Travelling Wave Magnetron.

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UNIT III: MICROWAVE SOLID STATE DEVICES & MEASUREMENTS

MICROWAVE MEASUREMENTS: Description Of Microwave Bench- Different Blocks, Microwave Power Measurement- Measurement of Attenuation, VSWR & Impedance

MICROWAVE SOLID STATE DEVICES -TEDs - Introduction, Gunn Diodes -RWH Theory, Characteristics, Modes of Operation.

UNIT IV: BASICS OF OPTICAL FIBER COMMUNICATION

Block diagram of Optical Communication, Total Internal Reflection, Numerical Aperture, Cut off wavelength, Optical Fiber types- Step index fiber, Graded index fiber, Losses in Optical Fiber cables, Optical Sources- LED, LASERS, Optical Detectors- Avalanche photodiode, PIN photodiode. (Qualitative treatment).

UNIT V: OPTICAL FIBER CONNECTORS & SYSTEM DESIGN

OPTICAL FIBER CONNECTORS-Connector types, Single mode fiber connectors, Connector Return loss, Fiber Splicing- Splicing techniques, Splicing single mode fibers, Multimode fiber Joints, single mode fiber joints.

OPTICAL SYSTEM DESIGN - Point to point links -Link power budget, Line coding in Optical links, WDM, Eye pattern.

TEXT BOOK:

1. Samuel Y. Liao, Microwave Devices and Circuits, Pearson, 2003.
2. Kulkarni M., Microwave and Radar Engineering, UMESH Publications, 2003
3. Optical Fiber Communication Principle & Practice, John M Senior, Pearson Publications, 3rd edition.

REFERENCES BOOKS:

1. David M. Pozar, Microwave Engineering, John Wiley, 2011.
2. Raghuvanshi G S, Microwave Engineering, Cengage Learning, 2012.
3. Optical Fiber Communication- Gerd Keiser, TMH, 4th Ed., 2008.

1. Roy 2. Sankar 3. Lee 4. Vignesh
5. Sankar 6. Sankar 7. Sankar 8. Sankar
9. Sankar 10. Sankar 11. Sankar 12. Sankar
13. Sankar 14. Sankar



ELECTRONIC MEASUREMENTS AND INSTRUMENTATION

IV B.Tech. I Semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Acquire knowledge in Characteristics of Instruments, measurement of electrical quantities
2. Measure and analyze the signals using CRO.
3. Study the performance electronic systems using signal generators and wave analyzers
4. Acquire knowledge on Transducers and understanding their applications in real time world
5. Understanding the storing data of non-electrical and electrical quantities

UNIT-I: MEASUREMENTS AND MEASURING SYSTEMS:

Functional Diagram of Instrumentation System, Static characteristics: Accuracy - Precision - Resolution - Sensitivity - measurement Errors, Dynamic Characteristics: Speed of response - fidelity - Lag - Dynamic error - Statistical Analysis, Basic meter movement, DC Ammeters: Multi-range, DC voltmeters: Multi-range, AC voltmeters: Rectifier type - Thermocouple Type, Ohmmeters: Series type and Shunt type, Multimeter

UNIT-II: OSCILLOSCOPES:

Oscilloscope block diagram, Cathode Ray Tube, Vertical Deflection System, Delay Line, Horizontal Deflection System, Dual Beam CRO, Dual Trace CRO, Measurements with CRO: Amplitude - Time period - Frequency - Phase, Lissajous patterns, Sampling Oscilloscope, Analog Storage Oscilloscope, Digital Storage Oscilloscope.

UNIT-III: SIGNAL GENERATORS AND SIGNAL ANALYZERS:

Signal Generators:

AF Sine & Square wave generator, Function generators, RF signal generators, Sweep frequency generator.

Signal analyzers:

Wave analyzers: Frequency Selective Wave Analyzer - Heterodyne Wave Analyzer - Application of Wave Analyzers, Total Harmonic Distortion, Spectrum Analyzer.

1. Raghav 2. Shalini 3. Lee 4. Anshu 5. Akshay
6. Divya 7. D. 8. G. N. 9. A. 10. S. H.
11. M. 12. S. 13. B. A. 14. S. P.



Department of Electronics and Communication Engineering
(Accredited by NBA)

UNIT-IV: TRANSDUCERS:

Classification of Transducers, Strain gauges, Capacitive Transducers, Linear Variable Differential Transducer (LVDT), Piezoelectric Transducer, Thermocouple, Thermistor, Measurement of Displacement, Velocity, Acceleration, Vibration, Force, Pressure, Fluid Flow, Liquid Level and Temperature.

UNIT-V: BRIDGES AND RECORDERS:

Bridges:

Wheat Stone Bridge, Kelvin Bridge, Maxwell Bridge, Schering Bridge and Wien Bridge

Recorders:

Magnetic disk and tape Recorders, LED, LCD, Dot matrix display, Strip Chart Recorder, X-Y Recorder

TEXT BOOKS:

1. Albert D. Helfrick, Cooper William D, Modern Electronic Instrumentation and Measurement Techniques, Prentice-Hall of India, 1997.
2. Kalsi H S, Electronic instrumentation, Tata McGraw Hill, 2015.

REFERENCE BOOKS:

1. David A. Bell, Electronic Instrumentation & Measurements, Prentice-Hall, 2003.
2. Sawhney A K, Puneet Sawhney, A Course in Electrical and Electronic Measurements and Instrumentation, Dhanpat Rai & Company, 2016

1. R. S. 2. 3. 4. 5. 6. 7. 8. 9. 10. 11. 12. 13. 14. 15.



Department of Electronics and Communication Engineering
(Accredited by NBA)
RADAR SYSTEMS
(Professional Elective-III)

IV B.Tech. I Semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Understand the concepts of radar fundamentals; noise analysis and evaluation of radar;
2. Differentiate various types of radar transmitters and receivers.
3. Relate the different types of radar transmitter and receiver.
4. Categorize the type of radar system and noise analysis based on applications.
5. Correlate the different methods of Radar Reception and Receivers

UNIT – I:

Basics of Radar:

Introduction- Maximum Unambiguous Range- Radar Waveforms-- Radar Block Diagram and Operation- Radar Frequencies and Applications. Prediction of Range Performance-Minimum Detectable Signal- Receiver Noise.

Radar Equation:

Simple and Modified form of Radar Range Equation with Illustrative Problems - SNR- Envelop Detector-False Alarm Time and Probability- Integration of Radar Pulses- Radar Cross Section of Targets (simple targets - sphere- cone-sphere)- Transmitter Power- PRF and Range Ambiguities- System Losses (qualitative treatment)- Illustrative Problems.

UNIT – II:

CW and Frequency Modulated Radar:

Doppler Effect- CW Radar – Block Diagram- Isolation between Transmitter and Receiver- Non-zero IF Receiver- Receiver Bandwidth Requirements- Applications of CW radar- Illustrative Problems.

FM-CW Radar:

Range and Doppler Measurement- Block Diagram and Characteristics- FM-CW altimeter- Measurement Errors- Multiple Frequency CW Radar

UNIT – III:

MTI and Pulse Doppler Radar:

Introduction- Principle- MTI Radar with Power Amplifier Transmitter and Power Oscillator Transmitter- Delay Line Cancellers – Filter Characteristics- Blind Speeds- Double Cancellation- Staggered PRFs- Range Gated Doppler Filters- MTI Radar Parameters- Limitations to MTI Performance- MTI versus Pulse Doppler Radar.

1. Roy 2. Salun 3. Lee 4. Vajape 5. D. B. S. 6. S. S. 7. S. S. 8. S. S. 9. S. S. 10. S. S. 11. S. S. 12. S. S. 13. S. S. 14. S. S.



UNIT – IV:

Tracking Radar:

Tracking With Radar- Sequential Lobing- Conical scan-Mono pulse Tracking Radar-Amplitude Comparison Mono pulse (One-And Two-Coordinates)-Phase Comparison Monopulse- Tracking In Range- Acquisition and Scanning Patterns- Comparison Of Trackers.

UNIT – V:

Detection of Radar Signals in Noise:

Introduction- Matched Filter Receiver-Response Characteristics and Derivation- Correlation Function and Cross-Correlation Receiver- Efficiency of Non-Matched Filters- Matched Filter with Non-White Noise.

Radar Receivers:

Noise Figure and Noise Temperature- Display-Types- Duplexers-Branch types And Balanced type- Circulators as Duplexers. Introduction to Phased Array Antennas-Basic concepts- Radiation Pattern- Beam Steering and Beam Width changes- Advantages and Limitations- Applications.

TEXT BOOKS:

1. Merrill I. Skolnik , Introduction to radar systems, Tata McGraw Hill special Indian edition, 2007.
2. Kulkarni M , Microwave and Radar Engineering , UMESH Publications, 2003

REFERENCE BOOKS:

1. Byron Edde, Radar: Principles, Technology, Applications, Pearson, 2004.
2. Peyton Z. Peebles, Radar Principles, Wiley, 1998.

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CMOS ANALOG IC DESIGN
(Professional Elective-III)

IV B.Tech. I Semester

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Course Outcomes

After studying the course, each student is expected to be able to

1. Design basic building blocks of CMOS Analog ICs.
2. Carryout the design of single and two stage operational amplifiers and voltage references.
3. Determine the device dimensions of each MOSFETs involved.
4. Design various amplifiers like differential, current and operational amplifiers.
5. Understand the Characterization of Comparator.

UNIT - I

MOS Devices and Modeling

The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.

UNIT - II

Analog CMOS Sub-Circuits

MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascode current Mirror and Wilson Current Mirror, Current and Voltage References, Bandgap Reference.

UNIT- III

CMOS Amplifiers

Inverters, Differential Amplifiers, Cascode Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.

UNIT-IV

CMOS Operational Amplifiers

Design of CMOS Op-Amps, Compensation of Op-Amps, Design of Two-Stage Op-Amps, Power-Supply, Rejection Ratio of Two-Stage Op-Amps, Cascode Op-Amps, Measurement Techniques of OP-Amp.

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UNIT - V

Comparators

Characterization of Comparator, Two-Stage, Open-Loop Comparators, Other Open-Loop Comparators, Improving the Performance of Open-Loop Comparators, Discrete-Time Comparators.

TEXT BOOKS:

1. Philip E. Allen and Douglas R. Holberg – CMOS Analog Circuit Design, Oxford University Press, International Second Edition/Indian Edition, 2010.
2. Paul R. Gray, Paul J. Hurst, S. Lewis and R.G. Meyer - Analysis and Design of Analog Integrated Circuits, 5th edition, Wiley India, 2010.

REFERENCE BOOKS:

1. David A. Johns, Ken Martin- Analog Integrated Circuit Design, Wiley Student Edn, 2013.
2. Behzad Razavi – Design of Analog CMOS Integrated Circuits, TMH.
3. Baker, Li and Boyce – CMOS Circuit Design, Layout and Simulation, PHI.

1. Roy 2. Saluja 3. Lee 4. Huijs 5. De Groot
6. Meyer 7. R 8. S 9. J 10. J
11. S 12. S 13. S 14. S



ARTIFICIAL NEURAL NETWORKS
(Professional Elective-III)

IV B.Tech. I Semester

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COURSE OUTCOMES:

Upon completing this course, the student will be able to

1. Explore the similarity of Biological networks and Neural networks.
2. Perform the training of neural networks using various learning rules.
3. Demonstrate the concepts of forward and backward propagations.
4. Construct the Hopfield models.
5. Demonstrate essence of Neuro Dynamics and Hopfield Models.

UNIT-I

Introduction: A Neural Network, Human Brain, Models of a Neuron, Neural Networks viewed as Directed Graphs, Network Architectures, Knowledge Representation, Artificial Intelligence and Neural Networks

Learning Process: Error Correction Learning, Memory Based Learning, Hebbian Learning, Competitive, Boltzmann Learning, Credit Assignment Problem, Memory, Adaption, Statistical Nature of the Learning Process

UNIT-II

Single Layer Perceptrons: Adaptive Filtering Problem, Unconstrained Organization Techniques, Linear Least Square Filters, Least Mean Square Algorithm, Learning Curves, Learning Rate Annealing Techniques, Perceptron –Convergence Theorem, Relation Between Perceptron and Bayes Classifier for a Gaussian Environment

Multilayer Perceptron: Back Propagation Algorithm XOR Problem, Heuristics, Output Representation and Decision Rule, Computer Experiment, Feature Detection

UNIT-III

Back Propagation: Back Propagation and Differentiation, Hessian Matrix, Generalization, Cross Validation, Network Pruning Techniques, Virtues and Limitations of Back Propagation Learning, Accelerated Convergence, Supervised Learning

1. Roy 2. Sankar 3. Ka 4. Vagave 5. Adhikari 6. Ince
7. D 8. S. J. 9. J. 10. Jha 11. R. 12. S. P.
13. B. A. C. 14. J. P.



UNIT-IV

Self-Organization Maps (SOM): Two Basic Feature Mapping Models, Self-Organization Map, SOM Algorithm, Properties of Feature Map, Computer Simulations, Learning Vector Quantization, Adaptive Patter Classification

UNIT-V

Neuro Dynamics: Dynamical Systems, Stability of Equilibrium States, Attractors, Neuro Dynamical Models, Manipulation of Attractors as a Recurrent Network Paradigm

Hopfield Models – Hopfield Models, restricted Boltzmann machine.

TEXT BOOKS:

1. Simon S. Haykin - Neural Networks a Comprehensive Foundations, PHI
2. Jacek M. Zurada - Introduction to Artificial Neural Systems, JAICO Publishing House, 2006.

REFERENCE BOOKS:

1. Li Min Fu - Neural Networks in Computer Intelligence, TMH 2003
2. James A Freeman David M S Kapura - Neural Networks, Pearson , 2004.
3. B. Vegnanarayana -Artificial Neural Networks, Prentice Hall of India P Ltd, 2005

1. Prof 2. Sallu 3. Ka 4. Ragu 5. Arjun
6. Suresh 7. R 8. S. P. 9. A. 10. S. P.
11. S. P. 12. S. P. 13. B. A. 14. S. P.



NETWORK SECURITY AND CRYPTOGRAPHY
(Professional Elective-IV)

IV B.Tech. I Semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Describe network security fundamental concepts and principles
2. Encrypt and decrypt messages using block ciphers and network security technology and protocols
3. Analyze key agreement algorithms to identify their weaknesses
4. Identify and assess different types of threats, malware, spyware, viruses, vulnerabilities
5. Analyze IP Security, firewall and viruses.

UNIT- I:

Introduction: Security Services, Mechanisms and Attacks, A Model for Internetwork security, Classical Techniques: Conventional Encryption model, Steganography, Classical Encryption Techniques.

Modern Techniques: Simplified DES, Block Cipher Principles, Data Encryption standard, Strength of DES, Block Cipher Design Principles.

UNIT- II:

Encryption: Triple DES, International Data Encryption algorithm, Blowfish, RC5, Characteristics of Advanced Symmetric block Ciphers. Placement of Encryption function, Traffic confidentiality, Key distribution, Random Number Generation

UNIT - III

Public Key Cryptography: Principles, RSA Algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Cryptography

Number Theory: Prime and Relatively prime numbers, Modular arithmetic, Fermat's and Euler's theorems, Testing for primality, Euclid's Algorithm, the Chinese remainder theorem, Discrete logarithms.

UNIT- IV

Message Authentication and Hash Functions: Authentication requirements and functions, Message Authentication, Hash functions, Security of Hash functions and MACs.

Hash and Mac Algorithms: MD-5, Message digest Algorithm, Secure Hash Algorithm, Digital signatures and Authentication protocols: Digital signatures, Authentication Protocols, Digital signature standards.

Authentication Applications: Kerberos, Electronic Mail Security: Pretty Good Privacy, SIME/MIME.

1. Prof. S. Sathya 2. 3. 4. 5. A. B. Sathya
6. 7. 8. 9. 10.
11. 12. 13. 14.



UNIT – V:

IP Security: Overview, Architecture, Authentication, Encapsulating Security Payload, Key Management, Web Security: Web Security requirements, Secure sockets layer and Transport layer security, Secure Electronic Transaction.

Intruders, Viruses and Worms: Intruders, Viruses and Related threats.

Fire Walls: Fire wall Design Principles, Trusted systems.

TEXT BOOKS:

1. Cryptography and Network Security: Principles and Practice - William Stallings, Pearson Education.
2. Network Security: The complete reference, Robert Bragg, Mark Rhodes, TMH, 2004.

REFERENCE BOOKS:

1. Network Security Essentials (Applications and Standards) by William Stallings Pearson Education.
2. Fundamentals of Network Security by Eric Maiwald (Dreamtech press)
3. Principles of Information Security, Whitman, Thomson.
4. Introduction to Cryptography, Buchmann, Springer.

1. Prof 2. Stallings 3. K. 4. Nagar 5. A. H. 6. 7. 8. 9. 10. 11. 12. 13. 14.



SATELLITE COMMUNICATIONS
(Professional Elective-IV)

IV B.Tech. I Semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Demonstrate the historical background, basic concepts and frequency allocations for satellite communications.
2. Compare and contrast between various multiple accesses systems for satellite communication system.
3. Understand the propagation effects of signal in Satellite transmission
4. Design of satellite links for specified CNR.
5. Visualize satellite subsystems like telemetry, tracking, command and monitor power systems etc.

UNIT-I:

Introduction to Satellite Communication:

Principles and architecture of satellite Communication, Brief history of Satellite systems, advantages, limitations, applications and frequency bands used for satellite communication.

Orbital Mechanics:

Orbital equations, Kepler's laws, Apogee and Perigee for an elliptical orbit, evaluation of velocity, orbital period, angular velocity etc. of a satellite, concepts of Solar day and Sidereal day. Placement of Satellite in a Geo-stationary orbit

UNIT-II:

Satellite sub systems:

Attitude and orbit control system, TT& C subsystem, control subsystem, power systems, communication subsystems, satellite antenna equipment

Satellite link:

Basic Transmission Theory, System noise temperature and G/T ratio, Basic link analysis, Interference analysis, Design of satellite links for a specified C/N (with and without frequency reuse), Link budget

UNIT - III:

Propagation effects:

Introduction, Atmospheric Absorption, Cloud attenuation, Tropospheric and Ionospheric scintillation and low angle fading, Rain induced attenuation, Rain induced cross polarization interference.

1. Roy 2. Sallit 3. Mea 4. Nagar 5. Arjun 6. Ingo
7. B 8. S-N 9. A 10. J 11. A 12. P
13. B-A 14. C



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Multiple Access:

Frequency division multiple access(FDMA), inter modulation, calculation of C/N, Time division multiple access(TDMA) – frame structure, Burst structure, Satellite switched TDMA, on-board processing, Demand Assignment multiple Access (DAMA), CDMA spread spectrum transmission and reception.

UNIT – IV:

Earth station Technology:

Transmitters, Receivers, Antenna, Tracking systems, Terrestrial interface, Power test methods, Lower orbit considerations, Satellite Navigation and Global Positioning systems: radio and satellite navigation, GPS position location principles, GPS receivers

UNIT – V:

Typical Phenomena in Satellite Communication:

Solar Eclipse on satellite and its effects, Remedies for Eclipse, Sun Transit Outage phenomena, its effects and remedies, Doppler frequency shift phenomena and expression for Doppler shift.

TEXT BOOKS:

1. Timothy pratt, Charles Bostian, Jeremy Allnutt , Satellite communications, John Wiley, 2003
2. Pritchard, Satellite communications engineering, Pearson, 1993.

REFERENCE BOOKS:

1. Madhavendra Richharia, Satellite communications; Design principles, Macmillan, 2017
2. Tri T. Ha , Digital satellite communications, McGraw-Hill, 1990

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BIOMEDICAL INSTRUMENTATION
(Professional Elective-IV)

IV B.Tech. I Semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Summarize the requirement of biomedical instrumentation and adversity involved in human measurement.
2. Understand the concept of Bio Potentials in a Human Body
3. Utilize the concept of electrode and its responses used in real time.
4. Outline the divergent responses involved in cardiovascular and respiratory system.
5. Compare the various processes involved in bio telemetry.

UNIT-I:

Introduction:

The age of Biomedical Engineering, Development of Biomedical Instrumentation, Man-Instrumentation system, Components, Physiological system of the body, Problem encountered in measuring a living system. Transducers & Electrodes: The Transducers & Transduction principles, Active transducers, Passive Transducers, Transducer for Biomedical Applications.

UNIT-II:

Sources of Bioelectric potentials:

Resting & Action potentials, propagation of active potential, The Bioelectric potentials-ECG, EEG, EMG, and Invoked responses Electrodes: Electrode theory, Biopotential Electrodes-Microelectrodes Body surface electrodes, Needle Electrodes, Biochemical Transducers, Reference electrodes, PH electrodes, Blood Gas electrodes.

UNIT-III:

Cardiovascular Measurements:

Electrocardiography – ECG amplifiers, Electrodes & leads, ECG recorders - Three channel, Vector Cardiographs, ECG system for stress testing, Continuous ECG recording (Holterrecording), Blood pressure measurement, Blood flow measurement, Heart sound measurements, Patient Care & Monitoring- Elements of Intensive Care monitoring, patient monitoring displays, Diagnosis, Calibration & Reparability of patient monitoring equipment, pacemakers & Defibrillators

1. Roy 2. Sathish 3. Kes 4. Satish 5. A. K. Reddy 6. Indu
7. R 8. S. P. N 9. J. P. 10. J. 11. A. 12. S. P.
13. R. S. 14. S. P.



UNIT-IV:

Measurements in Respiratory system:

Physiology of respiratory system Measurement of breathing mechanics- Spiro meter, Respiratory Therapy equipment's: Inhalators ventilators & Respirators, Humidifiers, Nebulizers & Aspirators. Diagnostic Techniques: Ultrasonic Diagnosis Echocardiography, Echo Encephalography, Ophthalmic scans, X-Ray & Radio-isotope Instrumentation, Computerized Axial Tomography Scanners

UNIT-V:

Bio Telemetry:

The components of Biotelemetry system Implantable units, Telemetry for ECG measurements during exercise, for Emergency patient monitoring. Physiological Effects of Electric Current Safety of Medical Electronic Equipments, Shock hazards from Electrical equipment and prevention against them

TEXT BOOKS:

1. Leslie Cornwell, Biomedical Instrumentation and Measurements, Prentice Hall India, 1980
2. Arumugam M, Biomedical Instrumentation, Anuradha Publications, 1994

REFERENCE BOOKS:

1. Khandpur R.S, Biomedical Instrumentation, Tata McGraw-Hill, 2003 .
2. Willis J Tompkins, Biomedical DSP: C Language Examples and Laboratory Experiments for the IBM PC, Prentice Hall India, 2006.

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PRINCIPLES OF SIGNAL PROCESSING
(Open Elective-III)

IV B.Tech. I Semester

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Course Outcomes Upon completing this course, the student will be able to

1. Differentiate various signal functions.
2. Understand the characteristics of linear time invariant systems.
3. Understand the concepts of sampling theorem and signal to noise ratios.
4. Understanding the Concept of Z-Transform of a Discrete Sequence and systems
5. Demonstrate the effect of Noise on signals.

UNIT I: Signal Analysis:

Introduction to Signals and Systems, Classification of Signals and systems, Exponential and Sinusoidal signals, Concepts of Impulse function, Unit Step function, Signum function, Operation on signals.

UNIT II: Signal Transmission through Linear Systems Linear System:

Impulse response, Response of a Linear System, Linear Time Invariant (LTI) System, Linear Time Variant (LTV) System, Transfer function of a LTI System, Signal bandwidth, System Bandwidth, Ideal LPF, HPF, and BPF characteristics, Convolution and Correlation of Signals, Concept of convolution in Time domain and Frequency domain,

UNIT III: Sampling theorem:

Sampling theorem for band limited signals, Impulse Sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling – Aliasing, Introduction to Band Pass Sampling.

UNIT IV: Z-Transforms:

Concept of Z-Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms,

Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

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UNIT V: Noise:

Introduction to Noise, Resistive/Thermal Noise Source, Shot Noise Sources, Effective Noise Temperature, Noise equivalent bandwidth, Average Noise Figures, Average Noise Figure of cascaded networks.

TEXT BOOKS:

1. Signals, Systems & Communications - B.P. Lathi, B.S. Publications, 2013.
2. Probability, Random Variables & Random Signal Principles - Peyton Z. Peebles, TMH, 4 th Ed., 2001.

REFERENCES:

1. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawabi, 2 Ed.
2. Statistical Theory of Communication - S.P Eugene Xavier, New Age Publications, 2003

1. Roy 2. Shetty 3. Ka 4. Nagar 5. A. K. 6. Kumar
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Department of Electronics and Communication Engineering

COMMUNICATION TECHNOLOGIES
(Open Elective-III)

IV B.Tech. I Semester

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Course Outcomes: Upon completing this course, the student will be able to

1. Understand the information theory and its coding styles.
2. Acquire knowledge on satellite communication and broadcasting services.
3. Know GSM, LTE and 5G mobile networks.
4. Know about network security through encryption and decryption.
5. Understand the principles of Encryption and Authentication.

UNIT I:

INFORMATION THEORY: Shannon Capacity, Multimedia Data, Data Processing, Boolean Logics, Information Content, Entropy, Source Coding, Channel Coding, Modulation Schemes, Internet.

UNIT II:

WIRELESS COMMUNICATION TECHNOLOGIES: WLAN, Wifi, Bluetooth, Other Wireless PAN And WAN Technologies, Satellite Communications, Broadcast Services.

UNIT III:

CELLULAR MOBILE NETWORKS: GSM (2G), UMTS (3G), LTE (4G), 5G Mobile Networks, Mobile Network Planning Aspects.

UNIT IV:

FREE SPACE OPTICAL COMMUNICATIONS: Optical fiber, FTTC, FTTH, FTTBS, Free Space Optical Link, Channel Model with Different Factors, Deep Space Optical Communications.

UNIT V:

NETWORK SECURITY AND MANAGEMENT: Symmetrical Encryption, Asymmetrical Encryption, Authentication, Hash-Value, Integrity Check, Telecommunications Management Network, SNMP, Functionalities of Network Management, Trends and Future Development.

1. Roy 2. Sathish 3. Vas 4. Abhinav 5. A. Harish
6. Anurag 7. S 8. S-N 9. A 10. S
11. S 12. S 13. S 14. S



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Department of Electronics and Communication Engineering

TEXT BOOKS:

1. Shun-Ping Chen, "Fundamentals of Information and Communication Technologies" 2020
2. B.P. Lathi, "Communication systems" - BS Publications, 2006.

REFERENCES:

1. Simon Haykin, John Wiley "Digital Communications" 2005.
2. Herbert Taub, Donald L. Schilling, Gautham Saha "Principles of Communication systems" 3rd edition McGraw-Hill 2008.

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IOT FOR REAL TIME APPLICATIONS
(Open Elective-III)

IV B.Tech. I Semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Design remote data sensing and aggregation system for health care
2. Develop energy efficient IoT systems for smart cities
3. Design Precision agricultural systems
4. Develop smart automation system for Industries
5. Investigate various IoT based innovative systems for societal benefits

UNIT I:

IoT FOR HEALTHCARE:

Architecture of IoT for Healthcare, IoT based Health Monitoring System using Arduino, Smart continuous glucose monitoring (CGM) and insulin pens, Remote Patient Monitoring- IoT Heart Rate Monitoring, remote monitoring of physiological parameters, ECG, EEG, Diabetics and BP.

UNIT II:

IoT ENABLED SMARTCITIES:

Energy Consumption Monitoring, Smart Energy meters to minimize power consumptions, Smart home powered by IoT, Smart Grid and Solar Energy Harvesting, Intelligent Parking System

UNIT III:

IoT FOR SMARTAGRICULTURE:

Animal Intrusion detection in farms, soil moisture detection and Irrigation system, Pest monitoring and control, Livestock monitoring system, IoT based Greenhouse Environment Monitoring and controlling.

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UNIT IV:

IoT BASED INDUSTRIAL AUTOMATION:

IoT based gas leakage monitoring system, Temperature and liquid level monitoring in boilers, Fire detection system, wireless video surveillance robot, Automatic Solar Tracker

UNIT V:

IoT FOR SOCIETY:

Medical Waste Management, Weather update system with IoT Women security system, GPS Smart Sole, wearable glove to enables into speech conversation, IoT based air pollution meter

TEXT BOOKS:

1. Fadi Al-Turjman, Intelligence in IoT- enabled Smart Cities, 2019, 1st edition, CRC Press, ISBN-10:1138316849
2. Giacomo Veneri, and Antonio Capasso, Hands-on Industrial Internet of Things: Create a powerful industrial IoT infrastructure using Industry 4.0, 2018, Packt Publishing.

REFERENCES BOOKS:

1. Krishna P. Venkata, Sasikumar Gurumoorthy, Mohammad S. Obaidat, Internet of Things and Personalized Healthcare Systems, Springer-2019.
2. Pattnaik, Prasant Kumar, Mohanty, Suneeta, Mohanty, Saturupa, SmartHealth care Analytics in IoT Enabled Environment, Springer-2020.

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MICROWAVE AND OPTICAL COMMUNICATIONS LABORATORY

IV B.Tech. I Semester

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COURSE OBJECTIVES

At the end of the Course, the students able to

1. Understand the behavioral aspects of various microwave sources
2. Analyze the measurement procedures of important parameters in microwave engineering.
3. Evaluate the S-Parameters of various three and four port devices
4. Understand the characteristics of various optical sources.
5. Measure the losses, intensity modulation for the analog optical fiber and data rate for digital Optical fiber.

Note: Any 12 of the following experiments

LIST OF EXPERIMENTS:

1. Reflex Klystron Characteristics.
2. Gunn Diode Characteristics
3. Directional Coupler Characteristics
4. VSWR Measurement
5. Measurement of Waveguide Parameters
6. Measurement of Scattering parameters of a Magic Tee
7. Measurement of Scattering parameters of a Circulator
8. Attenuation Measurement
9. Characterization of LED.
10. Characterization of Laser Diode.
11. Intensity modulation of Laser output through an optical fiber.
12. Measurement of Data rate for Digital Optical link.
13. Measurement of Numerical Aperture of fiber cable.
14. Measurement of losses for Optical link

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Aziz Nagar Gate, C.B. Post, Hyderabad-500 075

Department of Electronics and Communication Engineering
(Accredited by NBA)

IV B.Tech. II Semester

| S.No. | Course Code | Course Title | L | T | P | Credits |
|-------|-------------|--|----------|----------|-----------|-----------|
| 1 | | Professional Elective-V | 3 | 0 | 0 | 3 |
| | | 1.Artificial Intelligence | | | | |
| | | 2.5G and beyond Communication | | | | |
| | | 3.Machine Learning | | | | |
| 2 | | Professional Elective-VI | 3 | 0 | 0 | 3 |
| | | 1.Multimedia Database Management Systems | | | | |
| | | 2.System On-Chip Architecture | | | | |
| | | 3.Wireless Sensor Networks | | | | |
| 3 | | Comprehensive Viva-Voce | 0 | 0 | 0 | 3 |
| 4 | | Project Stage-II | 0 | 0 | 22 | 9 |
| 5 | | Technical Seminar | 0 | 0 | 0 | 2 |
| | | Total Credits | 6 | 0 | 22 | 20 |

1. Prof. 2. Prof. 3. Prof. 4. Prof. 5. Prof. 6. Prof.
7. Prof. 8. Prof. 9. Prof. 10. Prof. 11. Prof. 12. Prof.
13. Prof. 14. Prof.



ARTIFICIAL INTELLIGENCE
(Professional Elective-V)

IV B.Tech. II Semester

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COURSE OUTCOMES:

At the end of course, the student will be able to

1. Understand the basics of the theory and practice of Artificial Intelligence as a discipline and about intelligent agents.
2. Explain search techniques and gaming theory.
3. Learn to apply knowledge representation techniques and problem solving Strategies to common AI applications.
4. Understand the basic ideas of software agents utilized in Artificial Intelligence.
5. Illustrate the basic applications of Artificial Intelligence.

UNIT I:

Introduction-Definition - History & overview of Artificial Intelligence, Applications - Future of Artificial Intelligence - Characteristics of Intelligent Agents-Typical Intelligent Agents - Problem Solving Approach to Typical AI problems.

UNIT II:

Problem solving Methods - Search Strategies- Uninformed and Informed - Heuristics - Uninformed Search: depth first search, breadth first search - Heuristic Search Strategies: Greedy Best First Search, A* Search - Beyond Classical Search: Hill-climbing search, Simulated annealing search.

UNIT III:

Knowledge Representation First Order Predicate Logic - Unification - Forward Chaining- Backward Chaining - Resolution - Knowledge Representation - Ontological Engineering- Categories and Objects - Events - Mental Events and Mental Objects - Reasoning Systems for Categories -Reasoning with Default Information

UNIT IV:

Software Agents Architecture for Intelligent Agents - Agent communication -Negotiation and Bargaining - Argumentation among Agents - Trust and Reputation in Multi-agent systems.

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UNIT V:

Applications: AI applications – Language Models – Information Retrieval- Information Extraction – Natural Language Processing – Machine Translation – Speech Recognition.

TEXT BOOKS:

1. S. Russell and P. Norvig, "Artificial Intelligence: A Modern Approach, Prentice Hall, Third Edition, 2009.
2. Artificial Intelligence: A Modern Approach, 4th Edition, Stuart Russell, peter Norvig University of California at Berkeley, Pearson education, 2020.

REFERENCES:

1. M. Tim Jones, —Artificial Intelligence: A Systems Approach (Computer Science), Jones and Bartlett Publishers, Inc.; First Edition, 2008
2. Nils J. Nilsson, —The Quest for Artificial Intelligence, Cambridge University Press, 2009.

1. R. J. 2. S. Russell 3. P. Norvig 4. S. Russell 5. P. Norvig
6. S. Russell 7. P. Norvig 8. S. Russell 9. P. Norvig 10. S. Russell
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IV B.Tech. II Semester

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COURSE OUTCOMES:

At the end of the course the student should be able to

1. Understand basic concepts of MIMO systems
2. Contrast among Mobile Wireless Technologies
3. Demonstrate the channel effects in radio wave propagation.
4. Recite the Design Considerations for mm Wave communication
5. Know the scope of Future Mobile Technologies

UNIT I: Multiple Input Multiple Output (MIMO) Communications:

Spatial Multiplexing, Spatial Diversity, Beamforming in MIMO systems, Hybrid Precoding, 5G Communication Landscape, Related work on 5G.

UNIT II:

Introduction to Mobile Wireless Technology Generations:

5G, WISDOM, GIMVC, Requirements of 5G, standardization of WISDOM, Vision of 5G, WISDOM Concept and Challenges, Cellular D2D Communication, D2D Using Physical Layer Network Coding, Using FFR and Using Cognitive Radio.

SMNAT: Introduction, Network Architecture and the Process, Implementation of SMNAT for In-Band-D2D and Interoperability with WISDOM, Description of Network elements of SMNAT and Call Flow for Session Establishment.

UNIT III:

Radio Wave Propagation for Mm Wave:

Introduction, Large-scale Propagation Channel Effects, Small-Scale Channel Effects, Spatial Characterization of Multipath and Beam Combining, Outdoor Channel Models, Indoor Channel Models.

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UNIT IV:

Higher layer Design Considerations for Mm Wave:

Challenges when Networking Mm Wave Devices, Beam Adaptation Protocols, Relaying for Coverage Extension, Support for Multimedia Transmission, Multiband considerations, Performance of Cellular networks, Mm Wave Standardization: ECMA-387, IEEE 802.11ad.

UNIT V: BEYOND 2020

Major Challenges Surrounding Future Cyber Security, Users Awareness, Spectrum Related Security Issues in CRNs. Challenges for 2020 and beyond, Future Mobile Technologies, High Altitude Stratospheric Platform Station Systems, Human Bond Communications, CONASENSE.

TEXTBOOKS:

1. Ramjee Prasad, *5G: 2020 and Beyond*, River Publishers
2. T. S. Rappaport, R. W. Heath Jr., R. C. Daniels, and J. M. Murdock, *Millimeter Wave Wireless Communication*, Pearson Education, 2015.

REFERENCES:

1. M. Manish, G. Devendra, P. Pattanayak, and N. Ha, *5G and Beyond Wireless Systems PHY Layer Perspective*, Springer Series in Wireless Technology
2. M. Vaezi, Z. Ding, and H. V. Poor, *Multiple Access techniques for 5G Wireless Networks and Beyond*, Springer Nature, Switzerland, 2019

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MACHINE LEARNING
(Professional Elective-V)

IV B.Tech. II Semester

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COURSE OUTCOMES:

After completion of the course student will be able to

1. Understand basics of Artificial Neural Networks computational model.
2. Understand the essentials of feature engineering, state-of-art tools and concepts of machine Learning
3. Design and evaluate different types of supervised learning algorithms for classification and Regression tasks
4. Design and evaluate different types of unsupervised learning algorithms for clustering tasks.
5. Design and evaluate strong learners for better real time prediction ensemble learning Algorithms

UNIT-I:

Artificial Neural Networks: Introduction, Understanding the Biological Neuron, Exploring the Artificial Neuron, Types of Activation Functions, Early Implementations of ANN, Architectures of Neural Network: Single-layer and multi-layer feed forward ANNs, Recurrent network, Learning Process in ANN, Back propagation.

UNIT-II:

Introduction to Machine Learning: What is Machine Learning, Why Machine Learning, Types of Machine Learning models, Challenges of Machine Learning, Applications of Machine Learning, Essential libraries and Tools, Generalization over fitting and under fitting, Bias-variance trade-off, and metrics.

UNIT-III:

Supervised Learning: Classification and Regression, Linear Regression: Single and Multiple, Logistic Regression, K-Nearest Neighbour, Naive Bayes Classifier, Decision Tree.

UNIT-IV:

Unsupervised Learning and Pre-processing: Types of Unsupervised Learning, Challenges in Unsupervised Learning, Applications of Unsupervised Learning, Pre-processing and Scaling, clustering, K-Means Clustering, Agglomerative Clustering.

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UNIT-V:

Ensemble Learning and Random Forest: Voting Classifiers, Bagging and pasting, Random Patches and Random subspaces, Random Forest, Boosting-AdaBoost.

TEXT BOOKS:

1. Saikat Dutt, Subramanian Chandramouli, Amit Kumar Das "Machine Learning", Pearson Education India, 2018.
2. Aurélien Géron, "Hands-on Machine Learning with Scikit-Learn, Keras, and TensorFlow: Concepts, Tools, and Techniques to Build Intelligent Systems" O'Reilly Media, Inc, 2017.

REFERENCE BOOKS:

1. Andreas C. Müller, Sarah Guido, "Introduction to Machine Learning with Python", O'Reilly Media, Inc, October 2016.
2. Tom M. Mitchell, "Machine Learning", McGraw-Hill Education (India) Private Limited, 2013.
3. Ethem Alpaydin, "Introduction to Machine Learning (Adaptive Computation and Machine Learning)", The MIT Press, 2004.
4. Stephen Marsland, "Machine Learning: An Algorithmic Perspective", CRC Press, 2009.

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MULTIMEDIA DATABASE MANAGEMENT SYSTEMS
(Professional Elective-VI)

IV B.Tech. II Semester

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Course Outcomes

At the end of the course the student should be able to

1. Gain knowledge of fundamentals of DBMS, database design and normal forms
2. Master the basics of SQL for retrieval and management of data.
3. Be acquainted with the basics of transaction processing and concurrency control.
4. Familiarity with database storage structures and access techniques
5. Understand the indexing in multimedia DBMS

UNIT - I

Database System Applications: A Historical Perspective, File Systems versus a DBMS, the Data Model, Levels of Abstraction in a DBMS, Data Independence, Structure of a DBMS

Introduction to Database Design: Database Design and ER Diagrams, Entities, Attributes, and Entity Sets, Relationships and Relationship Sets, Additional Features of the ER Model, Conceptual Design With the ER Model

UNIT - II

Introduction to the Relational Model: Integrity constraint over relations, enforcing integrity constraints, querying relational data, logical data base design, introduction to views, destroying/altering tables and views.

Relational Algebra, Tuple relational Calculus, Domain relational calculus.

UNIT - III

SQL: Queries, Constraints, Triggers: form of basic SQL query, UNION, INTERSECT, and EXCEPT, Nested Queries, aggregation operators, NULL values, complex integrity constraints in SQL, triggers and active data bases.

Schema Refinement: Problems caused by redundancy, decompositions, problems related to decomposition, reasoning about functional dependencies, FIRST, SECOND, THIRD normal forms, BCNF, lossless-join decomposition, multi-valued dependencies, FOURTH normal form, FIFTH normal form.

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UNIT - IV

Transaction Concept, Transaction State, Implementation of Atomicity and Durability, Concurrent Executions, Serializability, Recoverability, Implementation of Isolation, Testing for serializability, Lock Based Protocols, Timestamp Based Protocols, Validation- Based Protocols, Multiple Granularity, Recovery and Atomicity, Log-Based Recovery, Recovery with Concurrent Transactions.

UNIT - V

Data on External Storage, File Organization and Indexing, Cluster Indexes, Primary and Secondary Indexes, Index data Structures, Hash Based Indexing, Tree base Indexing, Comparison of File Organizations, Indexes and Performance Tuning, Intuitions for tree Indexes, Indexed Sequential Access Methods (ISAM), B+ Trees: A Dynamic Index Structure.

TEXT BOOKS:

1. Database Management Systems, Raghurama Krishnan, Johannes Gehrke, *Tata Mc Graw Hill* 3rd Edition
2. Database System Concepts, Silberschatz, Korth, *Mc Graw hill*, V edition.

REFERENCE BOOKS:

1. Database Systems design, Implementation, and Management, Peter Rob & Carlos Coronel 7th Edition.
2. Fundamentals of Database Systems, ElmasriNavrate, *Pearson Education*
3. Introduction to Database Systems, C. J. Date, *Pearson Education*
4. Oracle for Professionals, The X Team, S.Shah and V. Shah, *SPD*.
5. Database Systems Using Oracle: A Simplified guide to SQL and PL/SQL.Shah, *PHI*.
6. Fundamentals of Database Management Systems, M. L. Gillenson, *Wiley Student Edition*.

1. Raghurama 2. Silberschatz 3. ElmasriNavrate 4. The X Team 5. Shah
6. Coronel 7. Date 8. Shah 9. Shah 10. Gillenson
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SYSTEM ON CHIP ARCHITECTURE
(Professional Elective-VI)

IV B.Tech. II Semester

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Course Outcomes:

At the end of the course the student should be able to

1. Expected to understand SOC Architectural features.
2. Acquire the knowledge on processor selection criteria and limitations
3. Acquires the knowledge of memory architectures on SOC.
4. Understands the interconnection strategies and their customization on SOC.
5. Elaborate Reconfiguration Technologies in SOCA.

UNIT – I:

Introduction to the System Approach: System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory and Addressing. System level interconnection, An approach for SOC Design, System Architecture and Complexity.

UNIT – II:

Processors: Introduction, Processor Selection for SOC, Basic concepts in Processor Architecture, Basic concepts in Processor Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT – III:

Memory Design for SOC: Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – 1, and D – Caches, Multilevel Caches, Virtual to real translation, SOC Memory System, Models of Simple Processor – memory interaction.

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UNIT - IV:

Interconnect Customization: Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses, Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time, SOC Customization:

UNIT - V:

Configuration: An overview, Customizing Instruction Processor, Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance - Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

TEXT BOOKS:

1. Computer System Design System-on-Chip by Michael J. Flynn and Wayne Luk, Wiley India Pvt. Ltd.
2. ARM System on Chip Architecture – Steve Furber – 2nd Ed., 2000, Addison Wesley Professional.

REFERENCE BOOKS:

1. Design of System on a Chip: Devices and Components – Ricardo Reis, 1st Ed., 2004, Springer
2. Co-Verification of Hardware and Software for ARM System on Chip Design (Embedded Technology) – Jason Andrews – Newnes, BK and CDROM
3. System on Chip Verification – Methodologies and Techniques –Prakash Rashinkar, Peter Paterson and Leena Singh L, 2001, Kluwer Academic Publishers.

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WIRELESS SENSOR NETWORKS
(Professional Elective-VI)

IV B.Tech. II Semester

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COURSE OUTCOMES:

Upon completion of the course, the student will be able to:

1. Analyze and compare various architectures of Wireless Sensor Networks
2. Understand Design issues and challenges in wireless sensor networks
3. Analyze and compare various data gathering and data dissemination methods.
4. Design, Simulate and Compare the performance of various routing and MAC protocol
5. Understand the design considerations of WSN

UNIT -I:

Introduction to Sensor Networks, unique constraints and challenges, Advantage of Sensor Networks, Applications of Sensor Networks, Types of wireless sensor networks

UNIT - II:

Mobile Ad-hoc Networks (MANETs) and Wireless Sensor Networks, Enabling technologies for Wireless Sensor Networks. Issues and challenges in wireless sensor networks

UNIT - III:

Routing protocols, MAC protocols: Classification of MAC Protocols, S-MAC Protocol, B-MAC protocol, IEEE 802.15.4 standard and ZigBee

UNIT -IV:

Dissemination protocol for large sensor network. Data dissemination, data gathering, and data fusion; Quality of a sensor network; Real-time traffic support and security protocols.

UNIT -V:

Design Principles for WSNs, Gateway Concepts Need for gateway, WSN to Internet Communication, and Internet to WSN Communication. Single-node architecture, Hardware components & design constraints, Operating systems and execution environments, introduction to TinyOS and nesC.

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TEXT BOOKS:

1. Ad-Hoc Wireless Sensor Networks- C. Siva Ram Murthy, B. S. Manoj, Pearson
2. Principles of Wireless Networks – Kaveh Pahlavan and P. Krishna Murthy, 2002, PE

REFERENCE BOOKS:

1. Wireless Digital Communications – Kamilo Feher, 1999, PHL
2. Wireless Communications-Andrea Goldsmith, 2005 Cambridge University Press.
3. Mobile Cellular Communication – Gottapu Sasibhushana Rao, Pearson Education, 2012.
4. Wireless Communication and Networking – William Stallings, 2003, PHL.

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