

VIDYA JYOTHI INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

I Yr – II SEM

ELECTRICAL CIRCUITS LAB

1. Measurement of voltage, current, and equivalent resistance of various circuits and verification of Kirchhoff's laws
2. Verification of Thevenin's Theorem & Verification of Norton's Theorem
3. Verification of Maximum Power Transfer Theorem on DC excitation for different loads (R, RL)
4. Verification of Compensation Theorem & Verification of Superposition Theorem
5. Verification of Reciprocity Theorem & Verification of Millman's Theorem
6. Resonance in series and parallel RL and RC circuits
7. Determination of self-inductance, mutual inductance, and coefficient of coupling
8. Current locus diagrams of RL and RC series circuits
9. Determination of Z and Y parameters
10. Determination of Transmission and Hybrid parameters
11. Measurement of active power for balanced loads
12. Measurement of reactive power for balanced loads

Any Ten experiments should be conducted