

ACADEMIC REGULATIONS

(R-22)

For the

Bachelor of Technology

(B. Tech)



With effect from the Academic year 2022-23

VIDYA JYOTHI INSTITUTE OF TECHNOLOGY

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Definitions of Key Words

Academic Year: An academic year is referred to as the period consisting of two consecutive semesters, each with 16 -18 weeks (90 instructional days) followed by semester examinations.

Course: A plan of study of a particular subject leading to an examination. All the courses need not carry the same weightage. A course may be designed to comprise of lectures/ tutorials/ laboratory work/ field work/ outreach activities/ project work/viva/ seminars/ assignments/ presentations etc. or a combination of some of these.

Choice Based Credit System (CBCS): Choice Based Credit System (CBCS) is the programme in which the students have a choice to choose from the prescribed courses and the entire assessment is graded based on a credit system.

Letter Grade: It is an index of the performance of students in a said course. Grades are denoted by letters O, A+, A, B+, B, C and F.

Grade Point: It is a numerical weight allotted to each letter Grade on a 10-point scale.

Credit: A unit of measurement of course work. It determines the number of hours of instructions required per week. One credit is equivalent to one hour of teaching (lecture or tutorial) or two hours of practical work/ field work per week.

Credit Point: It is the product of Grade Point and Number of Credits for a course.

Semester Grade Point Average (SGPA): SGPA stands for Semester Grade Point Average, a score awarded to students based on how well they have fared in their course work at the end of each semester .It is the ratio of total credit points secured by a student in the courses registered in a semester and the total course credits of that semester. It shall be expressed up to 2nd decimal place.

Cumulative Grade Point Average (CGPA): It is a measure of overall cumulative performance of a student in all the semesters. CGPA is the ratio of total credit points secured by a student in various courses in all semesters and the sum of the total credits of all courses in all the semesters. It is expressed up to 2nd decimal place.

Programme: An Educational Programme leading to the award of a Degree.

B. Tech. with Minor degree:

A student will be eligible to get B. Tech. with Minor Degree, if he/she acquires 18 additional credits. These should be acquired through registered courses offered by the Institution or through MOOCs as equivalent to the courses offered by the Institute.

B. Tech(Honors)

The B. Tech. (Honors) programs are proposed to choose for an area of specialization among various emerging technologies in order to be a domain expert. A student will be eligible for B. Tech. (Honors) Degree, if he/she acquires an additional 20 credits. These should be acquired through registered courses as per the respective courses offered by the Department/Institute or through SWAYAM MOOCs as equivalent to the courses offered by the Institute.

Transcript or Grade Card: Based on the grades earned, a grade certificate shall be issued to all the registered students after every semester. The grade card will be displaying the course details (code, title, number of credits, grade secured) along with SGPA of that semester and CGPA earned till that semester.

Types of Courses: The Courses in/under B. Tech Program may be of three kinds' viz., Core, Elective and Mandatory.

a) Core Course: -

There may be a Core Course in every semester and are to be compulsorily studied by a student and is essential requirement for a given Programme.

b) Elective Course: -

Elective Course is a course which can be chosen by the students from a pool of Courses. In general, the elective course is:

- Supportive to the discipline of study
- Provides an expanded scope of the core courses
- Nurtures student's proficiency/skill

In case an elective is "Discipline centric" and is offered by the student's department itself, the elective is called **Professional elective**. On the contrary, if the elective is offered by other departments wherein the students are at liberty to choose a course of their interest, the elective is called an "**Open Elective.**"

c) Mandatory Courses (Non-Credit Courses)

AICTE considers that the Course work pertaining to certain subjects is essential and a **pass** in those subjects is compulsory as such for the award of B.Tech degree. Such types of courses are referred to as **Mandatory courses**. As AICTE feels that familiarity with the subject content of these courses is sufficient, but essential, only a pass in each of these courses is required. Therefore, these subjects are included in the curriculum as non-Credit courses.

VIDYA JYOTHI INSTITUTE OF TECHNOLOGY

(An Autonomous Institution)

Aziznagar Gate, C.B. Post, Hyderabad - 500 075, Telangana

ACADEMIC REGULATIONS FOR B.Tech.

WITH EFFECT FROM ACADEMIC YEAR 2022-23 (R22)

- 1 **Under-Graduate Degree Programme in Engineering: Vidya Jyothi Institute of Technology** offers a 4 Year (8 Semesters) **Bachelor of Technology (B.Tech.)** Degree programme, under Choice Based Credit System (CBCS) in the following Branches of Engineering.

S. No	Branch
1.	Civil Engineering
2.	Electrical and Electronics Engineering
3.	Mechanical Engineering
4.	Electronics and Communication Engineering
5.	Computer Science and Engineering
6.	Information technology
7.	Artificial Intelligence
8.	Computer Science and Engineering (Data Science)
9.	Artificial Intelligence & Data Science
10.	Computer Science & Engineering (AI&ML)

**Regulations applicable to any new courses introduced in later years*

2 ELIGIBILITY FOR ADMISSION

Admission to the Under Graduate (UG) program shall be made either on the basis of the merit rank obtained by the candidate in the entrance test conducted by the Telangana State Government (TS EAMCET) and on the basis of any other order of merit approved by the Government from time to time, including admissions under Management/NRI Category.

The Government orders with regard to the admissions in vogue shall prevail.

The candidate should have passed the prescribed qualifying examination before the date of Admission.

The medium of instruction is **English**.

3 B.Tech PROGRAMME STRUCTURE

A student after securing admission shall complete the B.Tech programme in a minimum period of **four** academic years (8 semesters), and a maximum period of **eight** academic years (16 semesters) starting from the date of commencement of First year First semester, failing which the student shall forfeit seat in B.Tech Programme. Each student shall secure

160 credits (with CGPA \geq 5), required for the completion of the Under Graduate Programme and the award of the B.Tech. Degree.

UGC/ AICTE specified definitions/ descriptions are adopted appropriately for various terms and abbreviations used in these academic regulations/ norms are listed below.

Semester Scheme

All Under Graduate Programme is of 4 academic years (8 semesters). Every academic year shall be divided into two regular semesters known as the first semester and the second semester. Every Semester has - 'Continuous Internal Evaluation (CIE)' and 'Semester End Examination (SEE)' under Choice Based Credit System (CBCS) as indicated by UGC, and the Curriculum/ Course structure as suggested by AICTE are followed.

Credit Courses

All Subjects/Courses are to be registered by the student in a semester to earn credits which shall be assigned to each Subject/Course in a L: T: P: C (lecture periods: tutorial periods: practical periods: credits) structure based on the following general pattern.

- One credit for one hour/ week/ semester for one Theory/ Lecture (L) courses or Tutorials (T) and,
- One credit for two hours/ week/ semester for laboratory/ practical (P) Courses.

Courses like Gender Sensitization, Environmental Science, Induction Program are mandatory courses. These courses will not carry any credits.

Subject/ Course Classification

The College has followed almost all the guidelines issued by AICTE/ UGC. All Subjects/Courses offered for the UG Programmes in Engineering (B.Tech.) are broadly

classified as follows.

The groups of the subjects shall be as given in the table here under as suggested by AICTE.

S. No.	Subject Categories
1	Humanities and Social Sciences (HS) Subjects: English, Management and the Courses dealing with Personality Development
2	Basic Sciences (BS) Subjects including Mathematics, Physics and Chemistry
3	Engineering Sciences (ES): Engg. Workshop, Drawing, Fundamentals of Computer Science and Courses dealing with the basics of Electrical/Electronics/Mechanical Engineering
4	Professional Core (PC) Subjects: Includes core courses related to the parent discipline / department / branch of Engineering
5	Professional Elective (PE) Subjects: Includes elective courses related to the parent discipline / department / branch of engineering. Students has an option to select amongst the offered courses
6	Open Elective (OE) Subjects: Elective courses which include inter – disciplinary courses or courses in an area outside the parent discipline /
7	Project Work, Seminar and/or Internship in Industry or elsewhere along with Mini project.
8	Mandatory Courses (MC): Mandatory non-credit courses
9	<p>Minor/ Honors Courses</p> <p>Honors: To facilitate the students to choose additional courses by deep dive into emerging areas in their own discipline. The Honors program shall be offered by the parent department.</p> <p>Minor: Students, who are desirous of pursuing their special interest areas other than their branch of engineering, may opt for additional courses in</p>

B. Tech Year Wise distribution of credits

S.No.	Year	Semester	Regular Curriculum	
			Credits	Total Credits
1	1 st Year	I	20	40
		II	20	
2	2 nd Year	I	20	40
		II	20	
3	3 rd Year	I	20	40
		II	20	
4	4 th Year	I	20	40
		II	20	
Total No. of Credits				160

4 COURSE REGISTRATION/DROPPING

Each student has to compulsorily register for course work at the beginning of each semester as per the schedule mentioned in the academic calendar. It is absolutely necessary for the student to register for Courses in time.

A student would be allowed to register for an Additional Course only if the student satisfies the prerequisites.

Departments will notify at the time of registration about the minimum number of students to be enrolled for a particular Open Elective to be offered.

Any student may be barred from registering for any course on disciplinary grounds.

Open Electives: The students have to choose three Open Electives (OE-I, II& III) from the list of open electives given. However, the student cannot opt for an Open Elective Subject offered by his own (parent) department.

Professional Electives:

Students have to register from the list of professional elective courses as prescribed in the

course structure of the programme.

5 ELECTIVE COURSES TO BE OFFERED

An Elective Course may be offered to the students, only if a minimum of 30 students opt for it.

More than one faculty member may offer the same subject (lab/ practical may be included with the corresponding theory subject in the same semester) in any semester.

6 ATTENDANCE REQUIREMENTS

A student shall maintain a minimum required attendance of 75% in AGGREGATE. He/She is eligible to write the Semester End Examinations only if the student acquires a minimum of 75% of attendance in class work aggregate of all the Subjects/Courses in that Semester.

Shortage of attendance in aggregate up to 10% (65% and above, and below 75%) in each semester may be condoned by the college academic council on genuine medical grounds, based on the student's representation with supporting evidence. Student shall submit the same as and when such requirement arises but not at the end of semester.

A stipulated fee shall be payable towards condonation of attendance shortage.

Shortage of attendance below 65% in aggregate shall in no case be condoned.

Students, whose shortage of attendance is not condoned, are not eligible to appear for Semester End Examinations of that semester. Such students are detained and their registration for the examination stands cancelled.

A student detained due to shortage of attendance in a semester may seek re-admission into that semester, as and when offered, within four weeks from the date of the commencement of class work. Academic regulations applicable to the semester in which re-admission is sought shall be applicable to the re-admitted student. In case if there are any professional electives and/or open electives, the same may also be re-registered if offered. However, if those electives are not offered in the later semesters, then alternate electives may be chosen from the same set of elective courses offered under that category.

7 ACADEMIC REQUIREMENTS

The following academic requirements have to be satisfied, in addition to the attendance requirements mentioned in item no.6

A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course, if a student secures not less than 35% (14 marks out of 40 marks in the **Continuous Internal Evaluation**, not less than 35% (21 marks out of 60 marks) in the semester end examination, and a minimum of 40% (40 marks out of

100 marks) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together; in terms of letter grades, this implies securing ‘C’ grade or above in that subject/ course.

A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to Real-time Research Project (or) Field Based Research Project (or) Industry Oriented Mini Project (or) Internship (or) Seminar, if the student secures not less than 40% of marks in each of them.

The student is deemed to have failed, if he (i) does not submit a report on Industry Oriented Mini Project/Internship, or (ii) not make a presentation of the same before the evaluation committee as per schedule, or (iii) secures less than 40% marks in Real-time Research Project (or) Field Based Research Project (or) Industry Oriented Mini Project (or) Internship evaluations.

A student may reappear once for each of the above evaluations, when they are scheduled again; if the student fails in such ‘one reappearance’ evaluation also, the student has to reappear for the same in the next subsequent semester, as and when it is scheduled.

Promotion rules

S. No.	Promotion	Conditions to be fulfilled
1	First year first semester to First year second semester	Regular course of study of First year first semester and shall satisfy attendance requirements.

2	First year second semester to Second year first semester	<p>(i) Regular course of study of First year second semester and shall satisfy attendance requirements.</p> <p>(ii) Must have secured at least 20 credits out of 40 credits i.e., 50% credits up to First year second semester from all the relevant regular and supplementary examinations, whether the student takes those examinations or not.</p>
3	Second year first semester to Second year second semester	Regular course of study of Second year first semester and shall satisfy attendance requirements.
4	Second year second semester to Third year first semester	<p>(i) Regular course of study of Second year second semester and shall satisfy attendance requirements.</p> <p>(ii) Must have secured at least 48 credits out of 80 credits i.e., 60% credits up to Second year second semester from all the relevant regular and supplementary examinations, whether the student takes those examinations or not.</p>
5	Third year first semester to Third year second semester	Regular course of study of Third year first semester and shall satisfy attendance requirements.
6	Third year second semester to Fourth year first semester	<p>(i) Regular course of study of Third year second semester shall satisfy attendance requirements.</p> <p>(ii) Must have secured at least 72 credits out of 120 credits i.e., 60% credits up to Third year second semester from all the relevant regular and supplementary examinations, whether the student takes those examinations or not.</p>
7	Fourth year first semester to Fourth year second semester	Regular course of study of Fourth year first semester and shall satisfy attendance requirements.

A Student (i) shall register for all Courses/Subjects covering 160Credits as specified and listed in the course structure, (ii) fulfils all the attendance and academic requirements for 160 credits, (iii) earn all 160 credits by securing SGPA ≥ 5.0 (in each semester), and

CGPA (at the end of each successive semester) ≥ 5.0 , (iv) **passes all the mandatory courses**, to successfully complete the under graduate programme. The performance of the student in these 160 credits shall be taken into account for the calculation of ‘the final CGPA (**at the end of under graduate programme**)’, and shall be indicated in the grade card / marks memo of IV-year II-semester.

If a student register for ‘**extra subjects**’ (in the parent department or other department / branches of Engineering) other than those listed subjects totaling to 160 credits as specified in the course structure of his/her department, the performances in those ‘**extra subjects**’ (although evaluated and graded using the same procedure as that of the required 160 credits) will not be considered while calculating the SGPA and CGPA. For such ‘**extra subjects**’ registered, percentage of marks and letter grade alone will be indicated in the grade card as a performance measure, subject to completion of the attendance and academic requirements as stated in regulations Items 6 and 7.1 – 7.4 above.

A student eligible to appear in the Semester End Examination for any subject/ course, but absent for it or failed (thereby failing to secure ‘C’ grade or above) may reappear for that subject/ course in the supplementary examination as and when conducted. In such cases, internal marks (CIE) assessed earlier for that subject/ course will be carried over, and added to the marks obtained in the SEE supplementary examination for evaluating performance in that subject.

A student detained in a semester due to shortage of attendance may be re-admitted in the same semester in the next academic year for fulfillment of academic requirements. The academic regulations under which a student has been readmitted shall be applicable. However, no grade allotments or SGPA/ CGPA calculations will be done for the entire semester in which the student has been detained.

A student detained due to lack of Credits in any year, may be readmitted after fulfillment of the Academic Requirements, with the Academic Regulations of the Batch into which he gets readmitted. If there are any Professional Electives/Open Electives, the same may also be re-registered if offered. However, if those Electives are not offered in later Semesters, then alternate Electives may be chosen from the same set of Elective Subjects offered

under that category.

A student detained due to lack of credits, shall be promoted to the next academic year only after acquiring the required number of academic credits. The academic regulations under which the student has been readmitted shall be applicable to him.

Student, who fails to earn 160 credits as indicated in the course structure within eight academic years from the year of his/her admission, shall forfeit the seat in B. Tech. course and admission stands cancelled.

A student with a final CGPA (at the end of the UGP) < 5.00 will not be eligible for the Award of the Degree.

8 EVALUATION- DISTRIBUTION AND WEIGHTAGE OF MARKS

The performance of a student in every subject/course (including Practicals and Project Stage – I & II) will be evaluated for 100 marks each, with 40 marks allotted for CIE (Continuous Internal Evaluation) and 60 marks for SEE (Semester End-Examination).

In CIE, for theory subjects, during a semester, there shall be two mid-term examinations. Each Mid-Term examination consists of two parts i) Part – A for 10 marks, ii) Part – B for 20 marks with a total duration of 2 hours as follows:

1. Mid Term Examination for 30 marks:
 - a. Part - A : Objective/quiz paper for 10 marks.
 - b. Part – B : Descriptive paper for 20 marks.

The objective/quiz paper is set with multiple choice, fill-in the blanks and match the following type of questions for a total of 10 marks. The descriptive paper shall contain 6 full questions out of which, the student has to answer 4 questions, each carrying 5 marks.

While the first mid-term examination shall be conducted on 50% of the syllabus, the second mid-term examination shall be conducted on the remaining 50% of the syllabus.

The remaining 10 marks of Continuous Internal Assessment (out of 40) are distributed as:

- 1 Assignment for 5 marks. (Average of 2 Assignments each for 5 marks).

Five (5) marks are allocated for assignments (as specified by the subject teacher concerned). The first assignment should be submitted before the conduct of the first mid-term examination, and the second assignment should be submitted before the conduct of the second mid-term examination. The average of the two assignments shall be taken as the final marks for assignment (for 5 marks).

- 2 Subject Viva-Voce/Poster Presentation/Participatory Learning/Group Activities/Case Study on a topic in the concerned subject for 5 marks before II Mid-term Examination.

The Student, in each subject, shall have to earn 35% of marks (i.e. 14 marks out of 40 marks) in CIE 35% of marks (i.e. 21 marks out of 60) in SEE and however overall 40% of marks (i.e.40 marks out of 100 marks) both CIE and SEE marks put together.

The student is eligible to write Semester End Examination of the concerned subject, if the student scores $\geq 35\%$ (14 marks) of 40 Continuous Internal Examination (CIE) marks.

In case, the student appears for Semester End Examination (SEE) of the concerned subject but not scored minimum 35% of CIE marks (14 marks out of 40 internal marks), his performance in that subject in SEE shall stand cancelled inspite of appearing the SEE.

The details of End semester question paper pattern is as follows :

The Semester End Examinations (SEE), for theory subjects, will be conducted for 60 marks consisting of two parts viz. i) Part- A for 10 marks, ii) Part - B for 50 marks.

- Part-A is a compulsory question which consists of ten sub-questions from all units carrying equal marks
- Part-B consists of five questions (numbered from 2 to 6) carrying 10 marks each. Each of these questions is from each unit and may contain sub-questions. For each question there will be an “either” “or” choice, which means that there will be two questions from each unit and the student should answer either of the two questions.
- The duration of Semester End Examination is 3 hours.

For the subject, Computer Aided Engineering Graphics, the Continuous Internal Evaluation (CIE) and Semester End Examinations (SEE) evaluation pattern is same as for other theory subjects.

For practical subjects there shall be a Continuous Internal Evaluation (CIE) during the semester for 40 marks and 60 marks for semester end examination. Out of the 40 marks

for internal evaluation:

1. A write-up on day-to-day experiment in the laboratory (in terms of aim, components/procedure, expected outcome) which shall be evaluated for 10 marks
2. 10 marks for viva-voce (or) tutorial (or) case study (or) application (or) poster presentation of the course concerned.
3. Internal practical examination conducted by the laboratory teacher concerned shall be evaluated for 10 marks.
4. The remaining 10 marks are for laboratory project which consists of the Design (or) Software/Hardware model Presentation (or) App Development or Prototype Presentation submission which shall be evaluated after completion of laboratory course before the semester end Practical examinations.

The Semester End Examination shall be conducted with an external examiner and the laboratory teacher. The external examiner shall be appointed from other colleges which will be decided by the examination branch of the College.

In the Semester End Examination held for 3 hours, total 60 marks are divided and allocated as shown below:

1. 10 marks for write-up
 2. 15 for experiment/program
 3. 15 for evaluation of results
 4. 10 marks for presentation on another experiment/program in the same laboratory course and
 5. 10 marks for viva-voce on concerned laboratory course
- The student, in each subject, shall have to earn 35% of marks (i.e. 14 marks out of 40 marks) in CIE, 35% of marks (i.e. 21 marks out of 60) in SEE and Overall 40% of marks (i.e. 40 marks out of 100 marks) both CIE and SEE marks put together.
 - The student is eligible to write Semester End Examination of the concerned subject, if the student scores $\geq 35\%$ (14 mark out of 40 marks) in Continuous Internal Examination (CIE).

- In case, the student appears for Semester End Examination (SEE) of the concerned subject but not scored minimum 35% of CIE marks (14 marks out of 40 internal marks), his performance in that subject in SEE shall stand cancelled inspite of appearing the SEE.

The evaluation of courses having ONLY internal marks in I Year II Semester and II Year II Semester is as follows:

I Year I Semester course (ex., Elements of CE/ME/EEE/ECE/CSE): The internal evaluation is for 50 marks and it shall take place during I Mid-Term examination and II Mid-Term examination. The average marks of two Mid-Term examinations are the final for 50 marks. Student shall have to earn 40%, i.e 20 marks out of 50 marks from average of the two examinations. There shall be NO external evaluation. The student is deemed to have failed, if he (i) is absent as per schedule, or (ii) secures less than 40% marks in this course.

For CSE/IT and allied branches the Continuous Internal Evaluation (CIE) will be for 50 marks. Each Mid-Term examination consists of two parts i) Part – A for 20 marks, ii) Part – B for 20 marks with a total duration of 2 hours.

Part A: Objective/quiz paper is set with multiple choice, fill-in the blanks and match the following type of questions for a total of 20 marks. **Part B:** Descriptive paper shall contain 6 full questions out of which, the student has to answer 4 questions, each carrying 5 marks.

The remaining 10 marks of Continuous Internal Evaluation are for Assignment (5 marks) and Subject Viva-Voce/PPT/Poster Presentation/ Case Study (5 marks) and the evaluation pattern will remain same as for other theory subjects.

For all other branches, the Continuous Internal Evaluation (CIE) will be for 50 marks. Out of the 50 marks for internal evaluation:

- a) A write-up on day-to-day experiment in the laboratory (in terms of aim, components/procedure, expected outcome) which shall be evaluated for 10 marks
- b) 10 marks for viva-voce (or) tutorial (or) case study (or) application (or) poster presentation of the course concerned.
- c) Internal practical examination conducted by the laboratory teacher concerned shall be evaluated for 15 marks.

d) The remaining 15 marks are for Laboratory Report/Project and Presentation, which consists of the Design (or) Software / Hardware Model Presentation (or) App Development (or) Prototype Presentation submission which shall be evaluated after completion of laboratory course and before semester end practical examination.

II Year II Semester Real-Time (or) Field-based Research Project course: The internal evaluation is for 50 marks and it shall take place during I Mid-Term examination and II Mid-Term examination. The average marks of two Mid-Term examinations is the final for 50 marks. Student shall have to earn 40%, i.e 20 marks out of 50 marks from average of the two examinations. There shall be NO external evaluation.

The student is deemed to have failed, if he (i) does not submit a report on the Project, or does not make a presentation of the same before the internal committee as per schedule, or(ii) secures less than 40% marks in this course

In case, a few students are absent due to health reasons or any other unavoidable circumstances, or if a student wish to improve his/her performance in the internal marks a third mid-term examination will be conducted on payment of fees fixed by the examination branch. The test will be conducted in all the units of the subject.

There shall be an Industry training / Internship / Skill Development Courses / Paper presentation in reputed journal / Industry Oriented Mini Project in collaboration with an industry of their specialization. Students shall register for this immediately after II-Year II Semester Examinations and pursue it during summer vacation/semester break & during III Year without effecting regular course work. Internship at reputed organization / Skill development courses / Paper presentation in reputed journal / Industry Oriented Mini Project shall be submitted in a report form and presented before the committee in III-Year II semester before End Semester Examination. It shall be evaluated for 100 external marks. The committee consists of an External Examiner, Head of the Department, Internal Supervisor and a Senior Faculty Member of the Department. There shall be NO internal marks for Industry Training (or) Internship (or) Mini-Project (or) Skill Development Courses (or) Paper Presentation in reputed journal (or) Industry Oriented Mini Project

Design and Drawing

For the subject having design and/ or drawing, (such as Engineering Graphics, Engineering Drawing, and Machine Drawing), the distribution shall be 40 marks for Internal Evaluation (20 marks for day-to-day work and 20 marks for internal tests) and 60 marks for Semester End Examination. There shall be two internal tests in a semester and average of two examinations shall be considered for the award of marks for internal examination.

The UG project shall be initiated at the end of the IV Year I Semester and the duration of the project work is one semester. The student must present Project Stage – I during IV Year I Semester before II Mid examinations, in consultation with his Supervisor, the title, objective and plan of action of his Project work to the departmental committee for approval before commencement of IV Year II Semester. Only after obtaining the approval of the departmental committee, the student can start his project work.

UG project work shall be carried out in two stages: Project Stage – I for approval of project before Mid-II examinations in IV Year I Semester and Project Stage – II during IV Year II Semester. Student has to submit project work report at the end of IV Year II Semester. The project shall be evaluated for 100 marks before commencement of SEE Theory examinations.

For Project Stage – I, the departmental committee consisting of Head of the Department, project supervisor and a senior faculty member shall approve the project work to begin before II Mid-Term examination of IV Year I Semester. The student is deemed to be not eligible to register for the Project work, if he does not submit a report on Project Stage - I or does not make a presentation of the same before the evaluation committee as per schedule. A student who has failed may reappear once for the above evaluation, when it is scheduled again; if he fails in such ‘one reappearances’ evaluation also, he has to reappear for the same in the next subsequent semester, as and when it is scheduled.

For Project Stage – II, the external examiner shall evaluate the project work for 60 marks and the internal project committee shall evaluate it for 40 marks. Out of 40 internal marks, the departmental committee consisting of Head of the Department, Project Supervisor and a Senior Faculty Member shall evaluate the project work for 20 marks and Project Supervisor shall evaluate for 20 marks. The topics for Industry Oriented Mini Project/ Internship/SDC etc. and the main Project shall be different from the topic already taken. The student is deemed to have failed, if he (i) does not submit a report on the Project, or (ii) does not make a presentation of the same before the External Examiner as per schedule, or (iii) secures less than 40% marks in the sum total of the CIE and SEE taken together.

For conducting viva-voce of project, Principal/Director of the college selects an external examiner from the list of experts in the relevant branch submitted by the Head of the Department.

A student, who has failed, may reappear once for the above evaluation, when it is scheduled again; if student fails in such ‘one reappearance’ evaluation also, he has to reappear for the same in the next subsequent semester, as and when it is scheduled

Seminar Presentation

There shall be a seminar presentation in IV-year II Semester. For the seminar, the student shall collect the information on a specialized topic and prepare a technical report, showing his understanding of the topic, and submit it to the department. It shall be evaluated by the departmental committee consisting of Head of the Department, Seminar Supervisor and a Senior Faculty member. The seminar report shall be evaluated for 100 marks. There shall be no SEE or external examination for the seminar.

If the student fails to present the Seminar as required in the IV-year II Semester He may reappear for the seminar when they are scheduled again (within one month); if he fails in such ‘one reappearance’ evaluation also, he has to reappear for the same in the next subsequent Semester, as and when it is scheduled.

A student shall be given one time chance to re-register for a maximum of two subjects: in a semester

- If the internal marks secured by a student in the **continuous internal evaluation marks for 40** (Sum of average of two mid-term examinations consisting of objective & descriptive parts, average of 2 assignments and subject viva voce/PPT/Poster presentation/Case study on a topic in the concerned subject) are less than 35% and failed in those subjects

A student must re-register for the failed subject(s) for 40 marks within four weeks of commencement of the class work in next academic year.

In the event of the student taking this chance, his Continuous Internal Evaluation marks for 40 and Semester End Examination marks for 60 obtained in the previous attempt stand cancelled.

Distribution of Marks Internal Valuation/External Valuation

Subject	Internal Valuation Marks	External Valuation Marks	Total Marks
Theory/Engineering Drawing	40	60	100
Mini Project	0	100	100
Seminar	100	0	100
Major Project	40	60	100

Candidates shall be permitted to apply for recounting/revaluation of SEE scripts within 2 weeks from the date of release of results, with a payment of prescribed fee.

No marks or letter grades shall be allotted for Mandatory/Non-Credit Courses. Only Pass/Fail shall be indicated in Grade Card.

9 B.Tech with Minor Program

The Institution has introduced the **Bachelor of Technology in a particular specialization with minor program** (*For eg., B. Tech. in Electronics & Communication Engineering with Minor in AI&ML*) from AY. 2021-22.

The Bachelor of Technology (B.Tech.) with minor programs offered by VJIT focuses on the fundamental principle of Engineering, where the development of critical & analytical thinking and the ability to develop a distinctive approach to any given problem statement shall be the driving factor that fuels the pedagogic discourse.

A student will be eligible to get B. Tech. with Minor Degree, if he/she completes an additional 18 credits. These should be acquired through registered courses as per the respective courses offered by the Institute or through MOOCs courses as mentioned Annexure-I, as equivalent to the courses offered by the Institute.

Details are given in the Annexure –I

10 B.Tech (Honors)

The B. Tech. (Honors) programs are proposed to choose for an area of specialization among various emerging technologies in order to be a domain expert. A student will be eligible to get B. Tech. (Honors) Degree, if he/she completes an additional 20 credits. These should be acquired through registered courses as per the respective courses offered by the institution or through SWAYAM MOOCs as equivalent to the courses offered by the Institute.

Details are given in the Annexure -II

11 GRADING PROCEDURE

Grades will be awarded to indicate the performance of students in each theory subject, laboratory/ practical's/Seminar/ Industry Oriented Mini Project/Internship and Project Stage –I &II. Based on the percentage of marks obtained (Continuous Internal Evaluation and Semester End Examination, both taken together) as specified item 7 and 8 of above, a corresponding letter grade shall be given.

As a measure of the student’s performance, a 10-point Absolute Grading System using the following Letter Grades and corresponding percentage of marks shall be followed.

Letter Grade	Performance	Grade Points	% of marks Secured (Class Intervals)
O	Outstanding	10	Greater than or equal to 90%
A+	Excellent	9	80% and less than 90%
A	VeryGood	8	70% and less than 80%
B+	Good	7	60% and less than 70%
B	Average	6	50% and less than 60%
C	Pass	5	40% and less than 50%
F	Fail	0	Below 40%
AB	Absent	0	Absent

A Student who has obtained an ‘F’ grade in any subject shall be considered ‘failed’ and is required to reappear as a ‘supplementary student’ in the semester End Examination as and when conducted. In such cases, Internal Marks in those subjects will remain the same as those obtained earlier.

To a student who has not appeared for a semester end examination in any subject, ‘Ab’ grade will be allocated in that subject, and he is deemed to have ‘failed’. A Student will be required to reappear as a ‘Supplementary Student’ in the Semester End Examination, as and when conducted. In this case also, the internal marks in those subjects will remain the same as those obtained earlier.

A letter grade does not indicate any specific percentage of marks secured by the student, but it indicates only the range of percentage of marks.

In general, a student shall not be permitted to repeat any subject/course(s) only for the sake of ‘Grade Improvement’ or ‘SGPA/CGPA Improvement’. However, he has to repeat all the Subject/Courses pertaining to that Semester when he is detained due to shortage of attendance.

A student earns grade point (GP) in each subject/ course, on the basis of the letter grade secured in that subject/ course. The corresponding ‘credit points’ (CP) are computed by

multiplying the grade point with credits for that particular subject/course.

Credit points (CP) = grade point (GP) x credits For a subject/course

The Grade Point Average (GPA) will be calculated according to the formula:

$$GPA = \frac{\sum C_i G_i}{\sum C_i}$$

Where C_i = Number of credits allotted to the i^{th} Subject and G_i = Grade points allotted for all courses passed in that semester

The student passes the Subject/ Course only when he gets $GP \geq 5$ (P Grade or above).

The Semester Grade Point Average (SGPA) is calculated by dividing the sum of credit points ($\sum CP$) secured from all subjects/ courses registered in a semester, by the total number of credits registered during that semester. SGPA is rounded off to two decimal places. SGPA is thus computed as

$$SGPA = \frac{\sum \text{Course Credits } (C_i) \times \text{Grade Points } (G_i)}{\sum \text{Course Credits } (C_i)}$$

Where C_i = Number of credits allotted to the i^{th} Course

G_i = Grade points allotted to i^{th} Course passed in that semester

$\sum C_i$ = Total number of credits for all courses registered in that semester

And 'i' is the course indicator index (takes into account all courses in a semester).

The Cumulative Grade Point Average (CGPA) is a measure of the overall cumulative performance of a student in all semesters considered for registration. The CGPA is the ratio of the total credit points secured by a student in all registered courses in all semesters, and the total number of credits registered in all the semesters. CGPA is rounded off to two decimal places. CGPA is thus computed from the I year II semester onwards at the end of

each semester as per the formula

$$CGPA = \frac{\sum \text{Course Credits } (C_j) \times \text{Grade Points } (G_j)}{\sum \text{Course Credits } (C_j)}$$

Where

C_j = Number of credits allotted to the j^{th} Course

G_j = Grade points allotted to j^{th} Course passed up to that semester

$\sum C_j$ = Total number of credits for all courses registered until that semester

And 'j' is the subject indicator index (takes into account all subjects until the semester)

After registration and completion of I year I semester, the SGPA of that semester itself may be taken as the CGPA, as there are no cumulative effects.

Illustration of calculation of SGPA:

Course	Credits	Letter Grade	Grade Points	Credit Points
Course 1	4	A	8	4x8=32
Course 2	4	O	10	4 x 10=40
Course 3	4	C	5	4x5=20
Course 4	3	B	6	3x6=18
Course 5	3	A+	9	3x9=27
Course 6	3	C	5	3x5=15
	21			152

$$SGPA = 152/21 = 7.24$$

Illustrative Example:

An illustrative example given in below Table indicates the use of the above two equations in calculating SGPA and CGPA, both of which facilitate the declaration of academic performance of a student, at the end of a semester and at the end of successive semesters respectively. Both of them shall be normally calculated up to the second decimal position, so that the CGPA, in particular, can be made use of in awarding a rank for the student's performance in a class or college. If two students get the same CGPA, the tie should be resolved by considering the number of times a student has obtained higher SGPA; But, if it is not resolved even at this stage, the number of times a student has obtained higher grades like O, A, B etc shall be taken into account in rank ordering of the students in a class.

Example:

Year and Semester	Course No.	Credits	Grade	Grade Points	Credit Points
I Year I Semester	XX101	5	A	8	40
I Year I Semester	XX102	4	F	0	00
I Year I Semester	XX103	3	A+	9	27
I Year I Semester	XX104	4	F	0	00
I Year I Semester	XX105	5	C	5	25
I Year I Semester	XX106	5	A	8	40
Total		26(18*)			132
SGPA = 132/26=5.08 CGPA =5.08					
I Year II Semester	XX107	5	B+	7	35
I Year II Semester	XX108	4	A	8	32
I Year II Semester	XX109	3	C	5	15
I Year II Semester	XX110	5	B	6	30
I Year II Semester	XX111	4	A+	9	36
I Year II Semester	XX112	2	F	0	00
I Year II Semester	Xx113	2	A	8	16
I Year II Semester					
Total		25(23*)			164
SGPA = 164/25=6.56 CGPA = 296/51 =5.80					

*Total No. of credits excluding those with 'F'; this is particularly important to keep track of the number of credits earned by a student up to any semester.

The above illustrated calculation process of CGPA will be followed for each subsequent semester until 8th semester. The CGPA obtained at the end of 8th semester will become the final CGPA secured for entire B.Tech. Programme.

For merit ranking or comparison purposes or any other listing, only the 'rounded off' values of the CGPA will be used.

For calculations listed in regulations 11.7 to 11.10, performance in failed subjects/ courses (securing F grade) will also be taken into account, and the credits of such subjects/ courses will also be included in the multiplications and summations. After passing the failed subject(s) newly secured letter grades will be taken into account for calculation of SGPA and CGPA. However, mandatory courses will not be taken into consideration.

SGPA and CGPA of a semester will be mentioned in the semester Memorandum of Grades if all subjects of that semester are passed in first attempt. Otherwise, the SGPA and CGPA shall be mentioned only on the Memorandum of Grades in which sitting he passed his last exam in that semester.

12 EARNING OF CREDITS

A student shall be considered to have completed a Course successfully and earned the credits if he/she secures an acceptable letter grade in the range 'O' to 'P'. Letter grade 'F' in any Course implies failure of the student in that Course and no credits earned.

13 PASSING STANDARDS

A student shall be declared successful or 'passed' in a semester, if he secures a GP ≥ 5 ('C' grade or above) in every subject/course in that semester (i.e. when the student gets an SGPA ≥ 5.0 at the end of that particular semester); and he shall be declared successful or 'passed' in the entire undergraduate programme, only when gets a CGPA ≥ 5.00 ('C' grade or above) for the award of the degree as required.

After the completion of each semester, a grade card or grade sheet shall be issued to all theregistered students of that semester, indicating the letter grades and credits earned. It will show the details of the courses registered (course code, title, no. of credits, grade earned, etc.)and credits earned. There is NO exemption of credits in any case.

A Student shall be declared successful or 'passed' in any Non-Credit Subject/ Course, if he secures a minimum of P grade.

After the completion of each Semester, a Grade Card or Grade Sheet (or Transcript) shall

be issued to all the registered students of that semester, indicating the Letter Grades and Credits earned. It will show the details of the courses registered (Course Code, Title, No. of Credits, Grade Earned etc.), Credits earned, SGPA, and CGPA.

14 DECLARATION OF RESULTS

Computation of SGPA and CGPA are done using the procedure listed in 11.7 TO 11.10.

For final percentage of marks equivalent to the computed final CGPA, the following formula may be used.

$$\% \text{ of Marks} = (\text{final CGPA} - 0.5) \times 10$$

15 AWARD OF DEGREE

A student who registers for all the specified subjects/ courses as listed in the course structure and secures the required number of 160 credits (with CGPA ≥ 5.0), within 8 academic years from the date of commencement of the first academic year, shall be declared to have 'qualified' for the award of B.Tech. degree in the branch of Engineering selected at the time of admission.

A student who qualifies for the award of the degree as listed in item 15.1 shall be placed in the following classes

A student with final CGPA (at the end of the undergraduate programme) > 8.00 , and fulfilling the following conditions - shall be placed in 'First Class with Distinction'.

However, he/she

- (i) Should have passed all the subjects/courses in 'First Appearance' within the first 4 academic years (or 8 sequential semesters) from the date of commencement of first year first semester.
- (ii) Should not have been detained or prevented from writing the semester end examinations in any semester due to shortage of attendance or any other reason.

A student not fulfilling any of the above conditions with final CGPA > 8 shall be placed in 'First Class'.

Students with final CGPA (at the end of the undergraduate programme) ≥ 7.0 but < 8.00 shall be placed in 'First Class'.

Students with final CGPA (at the end of the undergraduate programme) ≥ 6.00 but < 7.00 , shall be placed in 'Second Class'.

All other students who qualify for the award of the degree (as per item 15.1), with final CGPA (at the end of the undergraduate programme) ≥ 5.00 but < 6 , shall be placed in 'pass class'.

A student with final CGPA (at the end of the undergraduate programme) < 5.00 will not be eligible for the award of the degree.

16 CONSOLIDATED GRADE CARD

A consolidated grade card containing credits & grades obtained by the candidates will be issued after completion of the four years B. Tech Program.

17 WITHHOLDING OF RESULTS

If the student has not paid the fees to the College at any stage, or has dues pending due to any reason whatsoever, or if any case of indiscipline is pending, the result of the student may be withheld, and the student will not be allowed to go into the next higher semester. The award or issue of the degree may also be withheld in such cases and the matter will be referred to College Academic Committee for final decision.

18 TRANSITORY REGULATIONS

Discontinued, detained for attendance, detained for want of credits, or failed students are eligible for readmission as and when the course is offered during the subsequent academic year as per the college admission procedures.

Students on transfer from a non- autonomous or from an autonomous college shall complete all the courses of the concerned programme not covered in the earlier organization. However, he/she should take the remaining courses in the programme along with the other students.

There shall be no branch transfers after the cut-off date of admissions made in the B.Tech.I year.

19 TRANSCRIPTS

After successful completion of the total programme of study, a Transcript containing performance of all academic years will be issued as a final record. Duplicate transcripts will also be issued if required after the payment of requisite fee.

20 SUPPLEMENTARY EXAMINATIONS

In addition to the Regular end semester examinations, Supplementary Examinations for the previous semesters will be conducted along with End Semester Examinations. A student can appear for any number of supplementary examinations till he/she clears all courses which he/she could not clear in the first attempt. However, the maximum stipulated period cannot be relaxed under any circumstances.

21 GRADUATION CEREMONY

The College shall have its own annual Graduation Ceremony for the award of degrees to students completing the prescribed academic requirements in each case, in consultation with the University and by following the provisions in the Statute.

The College shall institute Prizes and Awards to meritorious students, for being given away annually at the Graduation Ceremony.

22 TERMINATION OF THE PROGRAM

The admission of a student to the program may be terminated and the student may be asked to leave the Institute in the following circumstances:

- If the student fails to satisfy the Academic requirements of the program within the maximum period stipulated for that program.
- If the student fails to satisfy the norms of discipline specified by the institute from time to time.

23 NON-CREDIT COURSES (Mandatory Courses)

Requirement of 75% attendance as per the college regulations is compulsory of completing the Mandatory courses.

Specified number of Mandatory Courses among the designated ones is compulsory requirement for all the students for the award of B.Tech. Degree.

Although these courses do not carry any credits, performance in these subjects is evaluated following the procedure adopted for other subjects with the same marks. However, their performance will be indicated in the student's memo of marks as Satisfactory/ Unsatisfactory.

MALPRACTICES RULES

DISCIPLINARY ACTION FOR/IMPROPER CONDUCT IN EXAMINATIONS

	Nature of Malpractices/Improper conduct	Punishment
	If the student:	
1. (a)	Possesses or keeps accessible in examination hall, any paper, note book, programmable calculators, cell phones, pager, palm computers or any other form of material concerned with or related to the subject of the examination (theory or practical) in which student is appearing but has not made use of (material shall include any marks on the body of the student which can be used as an aid in the subject of the examination)	Expulsion from the examination hall and cancellation of the performance in that subject only.
(b)	Gives assistance or guidance or receives it from any other student orally or by any other body language methods or communicates through cell phones with any student or persons in or outside the exam hall in respect of any matter.	Expulsion from the examination hall and cancellation of the performance in that subject Only of all the students involved. In case of an outsider, he will be handed over to the police and a case is registered against him.
2.	Has copied in the examination hall from any paper, book, programmable calculators, palm computers or any other form of material relevant to the subject of the examination (theory or practical) in which the student is Appearing.	Expulsion from the examination hall and cancellation of the performance in that subject only.
3.	Impersonates any other student in Connection with the examination.	The student who has impersonated shall be Expelled from examination hall. The student is also debarred and forfeits the seat. The performance of the original student who has been impersonated, shall be cancelled in all the subjects of

		<p>the examination (including practical's and project work) already appeared and shall not be allowed to appear for examinations of the remaining subjects of that Semester/ year. The student is also debarred for two consecutive semesters from class work and All Examinations. The continuation of the course by the student is subject to the academic regulations in connection with Forfeiture of seat. If the imposter is an outsider, he will be handed over to the police and a case is registered against him.</p>
4.	<p>Smuggles in the answer book or additional sheet or takes out or arranges To send out the question paper during the examination or answer book or additional sheet, during or after the Examination</p>	<p>Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the student has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that Semester/ year. The student is also debarred for two consecutive semesters from class work and All Examinations. The continuation of the course by the student is subject to the academic Regulations in connection with Forfeiture of seat.</p>
5.	<p>Uses objectionable, abusive or offensive language in the answer paper or in letters to the examiners or writes to the examiner requesting him to award pass Marks.</p>	<p>Cancellation of the performance in that subject.</p>
6.	<p>Refuses to obey the orders of the chief superintendent/assistant superintendent / any officer on duty or misbehaves or creates disturbance of any kind in and around the examination hall or organizes a walk out or instigates others to walk out, or threatens the officer-in charge or any person on duty in or outside the examination hall of any injury to his person or to any of his relations whether by words, either spoken or written or by signs orb visible representation, assaults the officer-in- charge, or any person on duty in or outside the examination hall or any of his relations, or indulges in any other act of misconduct or mischief which result in damage to or destruction of property in the examination hall or any part of the college campus or engages in any other act which in the opinion of the officer on duty amounts to use of unfair means or misconduct or has the</p>	<ol style="list-style-type: none"> 1. In case of students of the college, they shall be expelled from examination halls and cancellation of their performance in that subject and all other subjects the student(s) has (have) already appeared and shall not be permitted to appear for the remaining examinations of the subjects of that Semester/ year. 2. The students also are debarred and forfeit their seats. In case of outsiders, they will be handed over to the police and a Police case is registered against them.

	tendency to disrupt the orderly conduct of the examination.	
7.	Leaves the exam hall taking away answer script or intentionally tears off the script or any part there of inside or outside the examination hall.	<p>1. Expulsion from the examination hall and cancellation of performance in that subject and all the other subjects the student has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that Semester/ year.</p> <p>2. The student is also debarred for two consecutive semesters from class work and All University examinations. The continuation of the course by the student is subject to the academic regulations in connection with Forfeiture of seat.</p>
8.	Possesses any lethal weapon or firearm in the examination hall.	<p>1. Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the student has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that Semester/ year.</p> <p>2. The student is also debarred and forfeits the seat. Police case will be registered.</p>
9.	If student of the college, who is not a student for the particular examination or any person not connected with the college indulges in any malpractice or improper conduct mentioned in clause 6 to 8.	<p>Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the student has already appeared including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of that Semester/ year. The student is also debarred and forfeits the seat.</p> <p>Person(s) who do not belong to the college will be handed over to the police and, a police case Will be registered against them.</p>
10.	Comes in a drunken condition to the examination hall.	Expulsion from the examination hall and cancellation of the performance in that subject and all other subjects the student has already appeared for including practical examinations and project work and shall not be permitted for the remaining examinations of the subjects of That semester/ year.
11.	Copying detected on the basis of internal evidence, such as, during Valuation or during special scrutiny.	Cancellation of the performance in that subject and all other subjects the student has appeared for including practical examinations and project work of that semester/year

		Examinations.
12.	If any malpractice is detected which is not covered in the above clauses 1 to 11 shall be reported to the Principal for further action to award a suitable Punishment.	

Malpractices identified by squad or special invigilators: Punishment to the students as per the above guidelines.

24 General

Wherever the words “he”, “him”, “his”, occur in the regulations, they include “she”, “her”, “hers”.

Where the words “Subject” or “Subjects”, occur in these regulations, they also imply “Course” or “Courses”.

The academic regulation should be read as a whole for the purpose of interpretation.

In case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Academic Council is final.

The Institution may change or amend the Academic Regulations, course structure or syllabi at any time, and the changes or amendments made shall be applicable to all students with effect from the date notified by the Institution.

ACADEMIC REGULATIONS FOR B. TECH LATERAL ENTRY STUDENTS

Applicable for the students admitted into II-year B. Tech. (Lateral Entry Scheme) from the Academic Year 2023-24.

1. Eligibility for award of B. Tech. Degree (LES)

The LES candidates shall pursue a course of study for not less than three academic years and not more than six academic years.

The candidate shall register for 120 credits and secure 120 credits by securing a minimum CGPA of ≥ 5 of B.Tech. II to IV year for the award of B.Tech. Degree.

The student(s), who fail to fulfill the requirement for the award of the degree in six Academic years from the year of admission, shall forfeit their seat(s). The attendance regulations of B. Tech. (Regular) shall be applicable to B.Tech. (LES).

2. Promotion Rule

A student shall be promoted from B.Tech., II Year to III Year if he/she gets at least a minimum of 24 out of 40 credits, up to II-year II semester, from all the examinations, whether or not the candidate takes the examinations.

A student shall be promoted from III year to IV year if he/she gets a minimum of 48 out of 80 credits, up to III-year II semester, from all the examinations, whether or not the candidate takes the examinations.

A student shall register and put-up minimum attendance in all 120 credits and earns all 120 credits to be eligible for the award of B. Tec degree.

A student, who fails to earn 120 credits as indicated in the course structure within six academic years, shall forfeit his/her admission in B.Tech. Course.

3. Award of Class

A student, who satisfies all the requirements prescribed for the completion of the B.Tech. program, is eligible for the award of the said degree, in any one of the following four classes:

CGPA	Class Awarded	
≥ 8.00	First Class with Distinction	From the CGPA secured from 120 credits
$\geq 7.00 - < 8.00$	First Class	
$\geq 6.00 - < 7.00$	Second Class	
$\geq 5.00 - < 6.00$	Pass Class	

4. All the other regulations as applicable to B. Tech. 4-year degree course (Regular) will hold good for B.Tech. (Lateral Entry Scheme).

Annexure-I

B. Tech with Minor Program

VJIT has always been emphasizing to orient the students towards the technologies that shall drive the world in the years to come; the Institution has introduced the **Bachelor of Technology in a particular specialization with minor program** (For eg., *B. Tech. in Electronics & Communication Engineering with Minor in AI&ML*) from AY. 2021-22.

The Bachelor of Technology (B.Tech) with minor programs offered by VJIT focuses on the fundamental principle of Engineering, where the development of critical & analytical thinking and the ability to develop a distinctive approach to any given problem statement shall be the driving factor that fuels the pedagogic discourse.

B. Tech. with Minor degree:

A student will be eligible to get B. Tech. with Minor Degree, if he/she completes an additional 18 credits. These should be acquired through registered courses as per the respective courses offered by the Institute or through MOOCs as equivalent to the courses offered by the Institute.

The key features of the B.Tech with Minor program being:

- The student can identify **only one area** of specialization along with his/her basic engineering degree.
- The no. of courses for Minor program is limited to 2 in a semester along with normal courses.
- In addition to the traditional B.Tech. program which is a 4-Year (8 Semester program) offering 160 course credits, additional **18 Credits**(minimum) to be completed as part of the **B.Tech with Minor program** between the **5th** and **8th** semester within the same period of 4-Year B. Tech. program.
- To successfully complete the **B.Tech with Minor program** the student shall need to clear the examinations for the additional 18 Credits. These credits can be acquired adopting advanced subjects in the offered specialization/ interdisciplinary studies, etc.

The following are the recommended areas for Minor programs:

S. No.	Minor Program	Eligible branch of students	@ Offering Department	Award of Degree
1	Construction Engineering & Management	All branches, except B. Tech. Civil Engg.	Civil Engg.	“B. Tech. in <u>branch name</u> with Minor in Construction Engineering & Management
2	Robotics	All branches, except B. Tech. Mech. Engg	Mech. Engg.	“B. Tech. in <u>branch name</u> with Minor in Robotics
3	Electric Vehicles	All branches, except B. Tech. EEE	EEE	“B. Tech. in <u>branch name</u> with Minor in Electric Vehicles
4	Sustainable Energy	All branches, except B. Tech. EEE	EEE	“B. Tech. in <u>branch name</u> with Minor in Sustainable Energy
5	IoT	All branches, except B. Tech. ECE	ECE	“B. Tech. in <u>branch name</u> with Minor in IOT”
6	CSE	All branches, except B. Tech. CSE	CSE	“B. Tech. in <u>branch name</u> with Minor in CSE
7	IT	All branches, except B. Tech. IT	IT	“B. Tech. in <u>branch name</u> with Minor in IT
8	AI&ML	All branches, except B. Tech. AI	AI	“B. Tech. in <u>branch name</u> with Minor in AI&ML
9	Business & Innovation Management (B&IM)	All branches of B.Tech.	MBA	“B. Tech. in <u>branch name</u> with Minor in B&IM

Rules & Regulations for B. Tech. with Minor Degree

1. The duration and all the academic regulations are on par with regular 4-Years B. Tech. program. A student completes/earns all the required credits of a course, if he/ she registers for the course and obtains a passing grade.
2. Only Students having earned all the credits at the end of the third semester (i.e., end of 2ndYear I-semester) without active backlogs are eligible to register for Minor program, in the fifth semester only.
3. For B. Tech with Minor, a student needs to earn additional minimum 18 credits (over and above the required 160 credits for B. Tech degree) as per the Minor program course structure.
4. Course registration fee per course should be met by the students only. The registration fee per credit is Rs.1000/-.
5. After registering for the Minor programme, if a student fails in any registered course and unable to earn all the required 18 credits in a specified duration (twice the duration of the course), he/she shall not be awarded Minor degree. However, if the student earns all the required credits of 160 for B. Tech., he/she will be awarded only B. Tech degree in the

concerned discipline. There is no transfer of credits from Minor program courses to regular B. Tech. degree course & vice versa.

6. These 18 credits (minimum) are to be earned from:
 - Additional Courses offered in the specialization by the concerned department.
 - MOOC courses offered by SWAYAM MOOCs as notified/approved by the university (minimum 3 credits each) from time to time.
 - Any expenses incurred for the MOOCs course to be met by the students only.
7. Online courses registered shall be certified ones with grading/marks/pass. Only Pass-grade/ pass-marks/ pass or above grade/marks shall be considered for inclusion of grades.
8. If the MOOC course is a pass course without any grades, the grade to be assigned as per the main regulations.
9. Prior to registration to MOOC courses, formal approval of the courses, by the University based on the organization of the programme, syllabus coverage, detailed duration of the programme, nature of evaluation etc. is needed.
10. The additional courses (for minimum of 18 credits) may be from the departments offering courses/subjects for the Minor degree. These subjects can be considered as advanced courses in that specialization/interdisciplinary courses etc.
11. However, the choice to opt/ take the Minor program is purely on the choice of the students in a particular engineering stream. Only top 50% of the total class in each specialization, based on their overall percentage of marks without active backlogs up to 3rd semester (II-year I Semester), are eligible to register for Minor program courses/ subjects.

Requirement for the Award of B.Tech with Minor Degree:

- a) A student may opt for B.Tech with Minor degree if she/he has no active backlogs till 3rd semester.
- b) For B. Tech with Minor, a student needs to earn additional 18 credits (minimum over and above the required 160 credits for B. Tech degree) as per his/her registered Minor program.
- c) Student should take permission of registration for the B.Tech with Minor program from Head of the department & faculty/course in-charge before commencement of 3rd Year I Semester or 5th Semester.
- d) To successfully complete the B.Tech with Minor program the student shall need to clear the examinations for the additional 18 Credits. The examinations shall be conducted as per the AICTE as well as University guidelines.
- e) The student shall be given a choice of withdrawing all the courses registered and/or credits earned for Minor courses/degree; and in that case the student will be awarded only B. Tech. degree in the concerned specialization on earning the required credits of 160 in a specified duration.

The following are the course structure of B.Tech Minor programs offered by various departments:

Course Structure of B.Tech Minor Programs offered by Various Departments

Department of Civil Engineering

B.TECH MINOR IN CONSTRUCTION ENGINEERING & MANAGEMENT

S. No.	Year/ Semester	Course	L	T	P	Credits
1	III-I	Principles of Surveying/ MOOCS	3	0	0	3
2	III-I	Surveying Lab	0	0	3	1.5
3	III-II	Essentials of building planning / MOOCS	3	0	0	3
4	III-II	Computer aided Building planning Lab	0	0	3	1.5
5	IV-I	AI applications in construction practices	3	0	0	3
6	IV-I	Construction Management/ MOOCS	3	0	0	3
7	IV-II	Mini Project	0	0	6	3
Total Credits						18

Department of Mechanical Engineering

B.TECH MINOR IN ROBOTICS

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1	III-I	Principles of Robotics	3	0	0	3
2		SCI Lab for Robotics Lab	0	0	3	1.5
3	III-II	Microcontrollers for Robotics	3	0	0	3
4	IV-I	Advanced Robotics (or) SWAYAM course on Introduction to Robotics	4	0	0	4
5		Robotic Simulation Lab	0	0	3	1.5
6	IV-II	Implementation of Robotic Systems	3	0	0	3
7		Mini Project	0	0	4	2
Total Credits						18

Department of Electrical & Electronics Engineering

B.TECH MINOR IN SUSTAINABLE ENERGY

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1.	III-I	Energy and its Resources	4	0	0	4
2.	III-II	Climate Change Understanding and Observations	3	0	0	3
3.	III-II	Energy and its Resources Lab	0	0	3	1.5
4.	IV-I	Energy Storage for Renewable	3	0	0	3
5.	IV-II	Electives 1. Electronics for Renewable 2. Solar Energy Technologies and System Design 3. Solar Energy System Installations and Maintenance	3	0	0	3
6.	IV-II	Energy Systems Lab	0	0	3	1.5
7.	IV-II	Internship/Mini Project	0	0	4	2
Total Credits						18

B.TECH MINOR IN ELECTRIC VEHICLES

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1.	III-I	Electric Vehicle and Energy Systems	4	0	0	4
2.	III-II	Power Electronics and Control of Electric Machines	3	0	0	3
3.	III-II	Simulation Lab	0	0	3	1.5
4.	IV-I	Automotive Transmission and Communication	3	0	0	3
5.	IV-II	Electives 1. Electric Vehicle Dynamics and Testing 2. Battery Charging Technology for EVs 3. Electric Vehicle: Safety and Regulations and Future of EVs	3	0	0	3
6.	IV-II	Electric Mobility Lab	0	0	3	1.5
7.	IV-II	Internship/Mini Project	0	0	4	2
Total Credits						18

Department of Electronics & Communication engineering

B.TECH MINOR IN INTERNET OF THINGS

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1	III-I	Embedded Sensors and IOT Architectures	4	0	0	4
2	III-I	Essentials of Python Programming Laboratory	0	0	3	1.5
3	III-II	IOT Communication Protocols	3	0	0	3
4	III-II	Smart Technologies	3	0	0	3
5	IV - I	Fog & Edge Computing for IoT	3	0	0	3
6	IV - I	IoT Automation with Raspberry-PI Laboratory	0	0	3	1.5
7	IV - II	Mini Project	0	0	4	2
Total Credits						18

Department of Computer Science and Engineering

B.TECH MINOR IN COMPUTER SCIENCE AND ENGINEERING

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1	III-I	Computer System Architecture	3	0	0	3
2	III-I	Data Structures	3	0	0	3
3	III-I	Data Structures Lab	0	0	3	1.5
4	III-II	Data Warehousing and Data Mining	3	0	0	3
5	III-II	Data Warehousing and Data Mining Lab	0	0	3	1.5
6	IV-I	Artificial Intelligence	3	0	0	3
		Linux Programming				
		Software Testing Methodologies				
		E-Commerce				
7	IV-II	Mini Project	0	0	6	3
Total Credits						18

Department of Information Technology

B.TECH MINOR IN INFORMATION TECHNOLOGY

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1	III-I	Fundamentals of Algorithms	3	0	0	3
2	III-I	Algorithms Lab	0	0	3	1.5
3	III-II	Foundations of Cloud Computing	3	0	0	3
4	IV-I	Fundamentals of Database Management Systems	3	0	0	3
5	IV-I	Fundamentals of Database Management Systems Lab	0	0	3	1.5
6	IV-II	Principles of Artificial Intelligence Big Data Analytics Internet of Things Blockchain Technologies (or) Swayam Course on Big Data Computing	3	0	0	3
7	IV-II	Mini Project				3
Total Credits						18

Department of Artificial Intelligence

B.TECH MINOR IN ARTIFICIAL INTELLIGENCE & MACHINE LEARNING

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1	III-I	Foundations of Artificial Intelligence	3	0	0	3
2	III-I	Python Programming Lab	0	1	3	2.5
3	III-II	Foundations of Machine Learning	3	0	0	3
4	III-II	Foundations of Machine Learning Lab	0	0	3	1.5
5	IV-I	Basics of Deep Learning	2	0	0	2
6	IV-I	Basics of Deep Learning Lab	0	0	2	1
7	IV-II	Electives: 1. Principles of Natural Language Processing 2. Introduction to Computer Vision 3. Soft Computing 4. Introduction to Artificial Neural Networks	3	0	0	3
8	IV-II	Mini Project	0	0	0	2
Total Credits						18

Department of Management Studies
B.TECH MINOR IN BUSINESS & INNOVATION MANAGEMENT

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1	III - I	Foundations of Management	4	0	0	4
2	III - II	Innovation & Design Thinking	3	0	0	3
3	III - II	Design thinking and Ideation Laboratory	0	0	3	1.5
4	IV - I	Business Ideation Business Models	3	0	0	3
5	IV - I	Business Plan Development	0	0	3	1.5
6	IV - II	<p>Any ONE of the following subjects:</p> <ol style="list-style-type: none"> 1. Project management 2. Market Research 3. Legal Aspects of Business 4. Technology Management (if not studied in the regular course)	3	0	0	3
7	IV - II	<p style="text-align: center;">Mini Project</p> Field Study Report/ Feasibility report on New ventures/ Pitfalls of entrepreneurship	0	0	4	2
Total Credits						18

Annexure-II

B.Tech (Honors)

The B. Tech. (Honors) programs are proposed to choose for an area of specialization among various emerging technologies in order to be a domain expert. A student will be eligible to get B. Tech. (Honors) Degree, if he/she completes an additional 20 credits. These should be acquired through registered courses as per the respective courses offered by the University or through SWAYAM MOOCs as equivalent to the courses offered by the Institute.

1. Objectives

The key objectives of offering **B. Tech. (Honors)** programs are:

- To expand the domain knowledge of the undergraduate students.
- To increase educational and professional skills required for pursuing higher studies or research in the area of interest.
- To acquire more knowledge as per industry requirement for better employment.

The key features of the **B.Tech. (Honors)** program being:

- The student can identify **only one area** of specialization along with his/her basic engineering degree.
- The no. of courses registered for honors is limited to TWO in a semester along with normal courses.
- In contrast to a traditional B. Tech. program which is a 4 Year (8 Semester program) offering 160 course credits, the **B. Tech. (Honors)** program is a 4 Year program (8 Semester program) offering 180 course credits.
- The additional **20 Credits** (Minimum, and maximum 20 credits) to be completed as part of the **B. Tech. (Honors)** program is to be spaced out between the **5th** and **8th** semester.
- To successfully complete the **B. Tech. (Honors)** program the student shall need to clear the examinations for the additional 20 Credits. These credits can be acquired opting advanced subjects in the offered specialization/ research/ interdisciplinary studies, etc.

2. Proposed Specialization Details

As per AICTE guide lines 2021-22, some of the VJIT proposed B. Tech. (Honors) programs are given below in each major discipline:

S. No.	Honors Program	Eligible branch of students	@ Offering Department	Award of Degree
1.	Structural Engineering	B. Tech. Civil Engg.	Civil Engg.	“B. Tech. (Honors) in <u>branch name</u> with Specialization in Structural Engineering
2.	CAD/ CAM	B. Tech. Mech. Engg.	Mech. Engg.	“B. Tech. (Honors) in <u>branch name</u> with Specialization in CAD/CAM
3.	Power Systems	B. Tech. EEE	EEE	“B. Tech. in <u>branch name</u> with Minor in Power Systems
4.	ECE	B. Tech. ECE	ECE	“B. Tech. (Honors) in <u>branch name</u> with Specialization in ECE
5.	CSE	B. Tech. CSE	CSE	“B. Tech. (Honors) in <u>branch name</u> with Specialization in CSE

3. Rules and Regulations for B. Tech. (Honors) Degree

1. The duration and all the academic regulations are on par with regular 4-Years B. Tech. program. A student will be awarded B. Tech. (Honors) degree, if he/she completes & earns all the required credits of a course for the registered courses and obtains a passing grade.
2. The department concerned shall have at least one M.Tech (Preferably NBA accredited) in the concerned stream, for B.Tech. (Honors) registration.
3. Only Students having earned all the credits with CGPA of **7or above** at the end of the third semester (i.e., end of 2nd Year I Sem.) are eligible to register for B.Tech. (Honors), in the fifth semester only.
4. For B. Tech. (Honors), a student needs to earn additional minimum 20 credits (over and above the required 160 credits for B. Tech degree) relevant to her/his discipline as per the course structure.
5. Course registration fees per course should be met by the students only. The registration fees per credit is Rs.1000/-.

6. After registering for the B.Tech. (Honors) programme, if a student fails in any registered course and unable to earn all the required 20 credits in a specified duration, he/she will not be eligible for obtaining B.Tech. (Honors) degree.
7. There is NO reduction in total no. of credits offered in the concerned regulations and NO credit transfer from normal courses to honors courses and vice versa.
8. These 20 credits are to be earned from:
 - Additional Courses offered in the same specialization by the concerned department. Course registration fee per course should be met by the students only as per the norms of the University.
 - Courses offered by NPTEL/ SWAYAM MOOCs as notified/approved by the university (minimum 3 credits each) from time to time. The duration of courses shall be a minimum of 12-14 weeks. The assessment and certification of the NPTEL/SWAYAM MOOCs courses shall be as per the prescribed norms of the NPTEL and approved by the Institute
 - Any expenses incurred for the NPTEL/SWAYAM MOOCs course should be met by the students only.
9. Online courses registered shall be certified ones with grading or marks or pass/ fail. Only Pass- grade/ pass-marks/ pass or above grade/marks shall be considered for inclusion of grades.
10. If the MOOC course is a pass course without any grades, the grade to be assigned as per the main regulations.
11. The additional courses (for minimum of 20 credits) may be from the same department as the undergraduate major. These subjects can be considered as advanced courses in that specialization/ research/ interdisciplinary courses etc.
12. However, the choice to opt/take the Honors program is purely on the choice of the students in a particular engineering stream. Only top 30% of the total class in each specialization, based on their overall percentage of marks in first attempt (without fail in any subject) up to 3rd semester (II-year I Semester), and are eligible to register for honors program courses/ subjects.

Requirement for the Award of B. Tech. (Honors) Degree:

A student enrolled in a B. Tech. program may also graduate with Honors, provided the student completes all the additional requirements for Honors, as specified by the regulations for the program in which he/she is enrolled. These additional requirements normally should include:

- a) For B. Tech (Honors), a student needs to earn additional 20 credits (minimum, over and above the required 160 credits for B. Tech degree) relevant to the discipline as recommended by the faculty advisor based on the courses offered in course structure for honors degree.
- b) Student should not have received any 'F' grade throughout the program in first attempt (without fail in any subject).
- c) Transfer of credits will not be permitted from regular courses to honors courses and vice versa.

4. Registration

- 1) At the beginning, just before the start of classes, of each semester, a student shall register for the courses he/ she wishes to take in that semester. A student shall normally be allowed to register for a course only if he/she has passed all the necessary pre-requisites for that course.
- 2) Student should take permission of registration for the B.Tech with Honors program from Head of the department & faculty/ course in-charge before commencement of 3rd Year I Semester or 5th Semester.
- 3) Registration is compulsory for all students, and is the sole responsibility of the student and must be completed before the last date of registration with necessary course registration fee per subject.
- 4) No student is allowed to register directly and the registration shall be through institute/ department. The registered students list shall be submitted to the university by the concerned principal.
- 5) The institute/ department shall maintain the record of student registered and pursuing the Honors degree.
- 6) The institute/ department shall prepare the time table for the registered Honors courses without any overlap/ clash on other courses the student registered for.
- 7) Minimum class strength (i.e., 33% of intake) is required for offering in-class Honors course.

The following are the course structure of B.Tech Honors programs offered by various departments:

Course Structure of B.Tech Honors Programs offered by Various Departments

Department of Civil Engineering

B.TECH HONORS IN STRUCTURAL ENGINEERING

S. No	Year/ Semester	Course Title	L	T	P	Credits
1	III-I	Advanced R.C. Design	3	0	0	3
2	III-I	Advanced Concrete Lab	0	0	3	1.5
3	III-II	Structural Dynamics	3	0	0	3
4	III-II	Computer aided structural design Lab	0	0	3	1.5
5	IV-I	Research Methodology	3	0	0	3
6	IV-I	Technical Paper Writing	2	0	0	2
7	IV-II	Cost management of Engineering projects/ one course from MOOCS	3	0	0	3
8	IV-II	Earthquake Resistant Design Of Buildings / one course from MOOCS	3	0	0	3
Total Credits						20

Department of Mechanical Engineering

B.TECH HONORS IN CAD/ CAM

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1	III-I	Industrial Robotics	4	0	0	4
2	III-II	Additive Manufacturing (or) SWAYAM Course on Fundamentals of Additive Manufacturing Technologies	4	0	0	4
3		Robotic Simulation & 3D Printing Lab	0	0	3	1.5
4	IV-I	Advanced Finite Element Method	3	0	0	3
5		Technical Paper Writing	0	0	4	2
6	IV-II	Advanced CAD	4	0	0	4
7		CAD/CAM/CAE Lab	0	0	3	1.5
Total Credits						20

Department of Electrical & Electronics Engineering

B.TECH HONORS IN POWER SYSTEMS

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1	III-I	Honors Elective - 1	3	0	0	3
2	III-II	Research Methodologies	3	0	0	3
3	III-II	Honors Elective - 2	3	0	0	3
4	IV-I	Honors Elective - 3	3	0	0	3
5	IV-I	Honors Elective - 4	3	0	0	3
6	IV-II	Technical Paper Writing	0	0	4	2
7	IV-II	Honors Elective -5	3	0	0	3
Total Credits						20

Honors Electives (E)	Pre-Requisites
Honors Elective - 1	
Waste to Energy Conversion	Power Systems I
Energy and its resources	Power Systems I
Electrical Safety and Quality Management	Power Systems I
Honors Elective - 2	
Advances in Distribution Systems	Power Systems I, Power Systems II
IoT Applications in Electrical Engineering	Basic Electrical Engineering
Energy Storage systems for renewable	Power Systems I
Honors Elective -3	
Smart Cities – Management of Smart Urban	Power Systems I, Power Systems II
Grid Integration of Renewable Energy Systems	Power Systems I, Power Systems II
Grid Integration of Electric Vehicles	Power Systems I, Power Systems II
Honors Elective - 4	
Cyber Security of Smart Grids	Smart Grids Planning and Operation
SCADA and Energy Management Systems	Power Systems I and Power Systems II
Distributed Generation and Micro Grids	Power Systems I and Power Systems II
Honors Elective - 5	
Smart Grid Protection	Smart Grids Planning and Operation
Electrical Safety Management	Basic Electrical Engineering
HVDC Transmission	Power Electronics, Power Systems

Department of Electronics & Communication Engineering

B.TECH HONORS IN ELECTRONICS & COMMUNICATION ENGINEERING

S. No.	Year/ Semester	Course Title	L	T	P	Credits
1	III-I	Honors Elective -1	3	0	0	3
		1. High Speed Electronics				
		2. Nano electronics				
2	III-II	Research Methodologies & Ethics	3	0	0	3
3	III-II	Honors Elective – 2	3	0	0	3
		1. Wireless Sensor Networks				
		2. Error Correcting Codes				
4	IV-I	Honors Elective – 3	3	0	0	3
		1. Mixed Signal Processing				
		2. Adaptive Signal Processing				
5	IV-I	Honors Elective – 4	3	0	0	3
		1. Speech and Audio Signal Processing				
		2. Scientific computing				
6	IV-II	Introduction to IOT	3	0	0	3
7	IV-II	Technical Paper Writing	0	0	4	2
Total Credits						20

Department of Computer Science and Engineering

B.TECH HONORS IN COMPUTER SCIENCE AND ENGINEERING

S. No	Year / Semester	Course to be chosen from/ studied	L	T	P	C
1.	III-I	Honors Elective-1 1. Principles of Programming Languages 2. Software Testing Methodologies 3. Computer Graphics	3	0	0	3
2.	III-II	Research Methodologies	3	0	0	3
3.	III-II	Honors Elective-2 1. Foundations of Machine Learning 2. Information Security 3. Software Project Management	3	0	0	3
4.	IV-I	Honors Elective-3 1. Big Data Analytics 2. Internet of Things 3. R Programming	3	0	0	3
5.	IV-I	Honors Elective-4 1. Advanced Databases 2. Neural Networks and Deep Learning 3. Natural Language Processing	3	0	0	3
6.	IV-II	Technical Paper writing	0	0	0	2
7.	IV-II	Honors Elective-5 1. Introduction to Data Science 2. Image Processing 3. Block Chain Technologies or MOOCS	0	0	0	3
Total Credits						20

I B.Tech. I Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1.	A221001	Mathematics-I(Linear Algebra & Calculus)	3	1	0	4
2.	A221002	Applied Physics	3	1	0	4
3.	A221501	C Programming for Engineers	3	0	0	3
4.	A221003	English for Skill Enhancement	2	0	0	2
5.	A221381	Engineering Workshop	0	1	3	2.5
6.	A221401	Elements of Electronics and Communication Engineering	0	0	2	1
7.	A221081	Applied Physics Laboratory	0	0	3	1.5
8.	A221082	English Language and Communication Skills Laboratory	0	0	2	1
9.	A221581	C Programming for Engineers Laboratory	0	0	2	1
10.		Induction Programme				
		Total	11	3	12	20

I B.Tech. II Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1.	A221005	Mathematics-II (Ordinary Differential Equations and Vector Calculus)	3	1	0	4
2.	A221006	Engineering Chemistry	3	1	0	4
3.	A222303	Engineering Graphics & Modeling	1	0	4	3
4.	A222204	Basic Electrical Engineering	2	0	0	2
5.	A222402	Electronic Devices and Circuits	2	0	0	2
6.	A222583	Python Programming Laboratory	0	2	2	3
7.	A222084	Engineering Chemistry Laboratory	0	0	2	1
8.	A222283	Basic Electrical Engineering Laboratory	0	0	2	1
		Total	11	4	10	20

II B.Tech. I Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1	A223403	Analog Circuits	3	0	0	3
2	A223404	Network analysis and Transmission Lines	3	0	0	3
3	A223405	Digital Logic Design	3	0	0	3
4	A223406	Signals and Systems	3	1	0	4
5	A223407	Probability Theory and Stochastic Processes	3	0	0	3
6	A223481	Digital logic Design Laboratory	0	0	2	1
7	A223482	Basic Simulation Laboratory	0	0	2	1
8	A223483	Electronic Devices and Analog Circuits Laboratory	0	0	2	1
9	A223009	Professional Communication	2	0	0	1
		Total Credits	17	1	6	20

II B.Tech. II Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1	A224010	Complex analysis and Fourier transforms	3	0	0	3
2	A224408	Electromagnetic Waves	3	0	0	3
3	A224409	Analog and Digital Communications	3	0	0	3
4	A224410	Linear IC Applications	2	0	0	2
5	A224411	Electronic Circuit Analysis	3	0	0	3
6	A224484	Analog and Digital Communications Laboratory	0	0	2	1
7	A224485	Linear IC Applications Laboratory	0	0	2	1
8	A224486	Electronic Circuit Analysis Laboratory	0	0	2	1
9	A2244P1	Real Time Project/Field Based Project	0	0	4	2
10	A224011	Quantitative methods & logical reasoning	2	0	0	1
		Total Credits	16	0	10	20

**Department of Electronics and Communication Engineering
(Accredited by NBA)**
III B.Tech. I Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1	A225414	Microprocessors and Microcontrollers	3	1	0	4
2	A225415	Control Systems	3	1	0	4
3	A225416	Antennas and Wave Propagation	3	0	0	3
4	Professional Elective-I		3	0	0	3
	A225417	1.Computer Organization and Operating Systems				
	A225418	2.Data Communications and Computer Networks				
	A225419	3.Information Theory and Coding				
5	Open Elective-I		3	0	0	3
	A225420	1.Introduction to Micro controllers				
	A225421	2.Electronic Measuring Instruments				
	A225422	3.Digital Electronics for Engineering				
6	A225488	Microprocessors and Microcontrollers Laboratory	0	0	2	1
7	A225087	Advanced English Communication Skills Laboratory	0	0	2	1
8	A225489	Antenna Laboratory	0	0	2	1
9	A225016	Environmental Science	2	0	0	0
Total Credits			17	2	6	20

III B.Tech. II Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1	A226423	Digital Signal Processing	3	0	0	3
2	A226018	Business Economics & Financial Analysis	3	0	0	3
3	A226424	CMOS VLSI Design	3	0	0	3
4	Professional Elective-II		3	0	0	3
	A226425	1. Digital Image Processing				
	A226426	2. Mobile Communications and Networks				
	A226427	3. Embedded System Design				
5	Open Elective-II		3	0	0	3
	A226428	1. Fundamentals of Internet of Things				
	A226429	2. Electronics for Health Care				
	A226430	3. Telecommunications for Society				
6	A226490	VLSI Laboratory	0	0	2	1
7	A226491	Embedded system Design Laboratory	0	0	2	1
8	A226492	Digital Signal Processing Laboratory	0	0	2	1
9	A2264P1	Industry Oriented Mini Project/Internship	0	0	4	2
10	A226019	Gender Sensitization	2	0	0	0
Total Credits			17	0	10	20



IV B.Tech. I Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1	A227431	Microwave and Optical Communications	3	1	0	3
2	A227432	Electronic Measurements and Instrumentation	3	0	0	3
		Professional Elective–III				
3	A227433	1. Radar Systems	3	0	0	3
	A227434	2. CMOS Analog IC Design				
	A227435	3. Artificial Neural Networks				
		Professional Elective–IV				
4	A227436	1. Network Security and Cryptography	3	0	0	3
	A227437	2. Satellite Communications				
	A227438	3. Biomedical Instrumentation				
		Open Elective–III				
5	A227439	1. Principles of Signal Processing	3	0	0	3
	A227440	2. Communication Technologies				
	A227441	3. IOT for Real Time Applications				
6	A227493	Microwave and Optical Communications Laboratory	0	0	4	2
7	A2274PS1	Project Stage–I	0	0	6	3
		Total Credits	15	1	10	20

IV B.Tech. II Semester

S.No.	Course Code	Course Title	L	T	P	Credits
1.	A228442	Wireless Sensor Networks	3	0	0	3
		Professional Elective-V				
2.	A228443	1. Artificial Intelligence	3	0	0	3
	A228444	2. 5 G and beyond Communication				
	A228445	3. Machine Learning				
		Professional Elective-VI				
3.	A228446	1. Multimedia Database Management Systems	3	0	0	3
	A228447	2. System On-Chip Architecture				
	A228448	3. Software Defined Radio				
4.	A2284PS2	Project Stage-II including Seminar	0	0	22	11
		Total Credits	9	0	22	20



Department of Electronics and Communication Engineering

ANALOG CIRCUITS

II B.Tech I Semester

Course Code: A223403

L	T	P	C
3	0	0	3

Course Outcomes:

After going through this course the student can

1. Design the amplifiers with various biasing techniques.
2. Design single stage amplifiers using BJT and FET
3. Design multistage amplifiers and understand the concepts of High Frequency Analysis of BJT.
4. Utilize the Concepts of negative feedback to improve the stability of amplifiers.
5. Utilize the Concepts of positive feedback to provide sustained oscillations.

UNIT-I

BJT Biasing: Transistor Biasing and Stabilization - Operating point, DC & AC load lines, Biasing –Fixed Bias, Self-Bias, Bias Stability, Bias Compensation using Diode

BJT Small Signal Model: Transistor Hybrid model, Determination of h-parameters from transistor characteristics, Typical values of h- parameters in CE, CB and CC configurations, Transistor at High Frequency: Hybrid $-\pi$ model of Common Emitter transistor model, f_α , f_β and unity gain bandwidth, Gain-bandwidth product.

UNIT-II

BJT Amplifiers: Transistor amplifying action, Analysis of CE, CC, CB Amplifiers and CE Amplifier with emitter resistance, low frequency response of BJT Amplifiers, effect of coupling and bypass capacitors on CE Amplifier.

FET Amplifiers: Analysis of CS, CD, CG JFET Amplifiers, comparison of performance with BJT Amplifiers, Basic Concepts of MOSFET Amplifier.

UNIT-III

Multistage Amplifiers: Classification of Amplifiers, Distortion in amplifiers, Different coupling schemes used in amplifiers, Frequency response and Analysis of multistage amplifiers, Cascade RC Coupled amplifiers, Cascode amplifier, Darlington pair.

UNIT-IV

Feedback Amplifiers: Concepts of feedback–Classification of feedback amplifiers– General characteristics of Negative feedback amplifiers – Effect of Feedback on Amplifier characteristics –Voltage series, Voltage shunt, Current series and Current shunt Feedback configurations – Simple problems.

Department of Electronics and Communication Engineering

UNIT-V

Oscillators: Condition for Oscillations, RC type Oscillators-RC phase shift and Wien-bridge Oscillators, LC type Oscillators–Generalized analysis of LC Oscillators, Hartley And Colpitts Oscillators, Frequency and amplitude stability of Oscillators, Crystal Oscillator.

TEXTBOOKS:

1. Jacob Millman, Christos CH alkias-Integrated Electronics, McGraw Hill Education.
2. Robert L.Boylestead, Louis Nashelsky –Electronic Devices and Circuits
3. theory, 11th Edition, 2009, Pearson

REFERENCEBOOKS:

1. David A.Bell–Electronic Devices and Circuits, 5th Edition, Oxford.
2. Adel S.Sedra, Kenneth C.Smith-Microelectronic Circuits- Theory and Applications,
3. Oxford. Chinmoy Saha, Arindam Halder, Debaati Ganguly-Basic Electronics-
4. Principles and Applications, 2018, Cambridge.



Department of Electronics and Communication Engineering

NETWORK ANALYSIS AND TRANSMISSION LINES

II B.Tech I Semester

Course Code: A223404

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

After going through this course the student can

1. Recite basic concepts of network parameters, theorems and transmission line theory.
2. Differentiate the changes of transient networks using Laplace transform
3. Compare and contrast the parameters, functions and synthesis of the network
4. Apply the concepts of theorems on networks and transmission line theory to solve impedance matching issues.
5. Solve the transmission lines and matching circuits problems using Smith chart

UNIT-I:

Network Theorems:

Source transformation - Superposition Theorem - Thevenin's theorem - Norton's theorem - Reciprocity theorem - Maximum power transfer theorem

UNIT-II:

Transient Analysis:

Transient response of RL, RC, RLC Circuits (Series and Parallel combinations) for DC excitations, Initial Conditions, Solution using Differential Equations approach and Laplace Transform Method. Transient response for different inputs such as step, ramp, pulse and impulse by using Laplace transforms method.

UNIT – III:

Two Port Networks:

Impedance Parameters, Admittance Parameters, Hybrid Parameters, Transmission (ABCD) Parameters, g parameters, Conversion of one of Parameter to another, Conditions for Reciprocity and Symmetry, Inter Connection of Two Port networks in series, Parallel and Cascaded configurations, Illustration problems.

UNIT – IV:

Transmission Lines - I:

Types, Parameters, Transmission Line Equations, Primary & Secondary Constants, Equivalent Circuit, Characteristic Impedance, Propagation Constant, Phase and Group Velocities, Infinite Line Concepts, Lossless / Low Loss Characterization, Types of Distortion, Condition for Distortion less line, Minimum Attenuation, Loading - Types of Loading.

UNIT- V:

Transmission Lines – II:

Input Impedance Relations, SC and OC Lines, Reflection Coefficient, VSWR. $\lambda/4$, $\lambda/2$, $\lambda/8$ Lines – Impedance Transformations, Smith Chart – Configuration and Applications, Single Stub Matching.

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TEXT BOOKS:

1. Abhijith Chakra barthi , Circuit Analysis and Synthesis, Dhanpat Rai and Company, 2018.
2. Umesh Sinha, Transmission Lines and Networks, Satya prakash Publications, 2010

REFERENCE BOOKS:

1. Van Valken Burg , Network Analysis , Pearson, 2016
2. Ryder J D, Networks, Lines and Fields, Prentice Hall India, 1999.



Department of Electronics and Communication Engineering

DIGITAL LOGIC DESIGN

II B.Tech I Semester

Course Code: A223405

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Demonstrate the basic theorems of Boolean algebra, logic gates, combinational and sequential circuits and memories.
2. Analyze the combinational and sequential circuits and memories.
3. Design of logic circuits
4. Realization of gates using different logic families.
5. Explain the design and operation of different semiconductor memories

UNIT-I:

Number System and minimization techniques:

Number System:

Review of number system and base conversion, complements, signed binary numbers, Floating point number representation, Error detection (parity detection only).

Minimization techniques

Boolean Algebra, postulates, basic logic gates, Canonical and Standard Form, NAND and NOR implementation, Minimization of switching function using theorem, The Karnaugh Map Method-Up to Five Variable Maps, Tabular Method.

UNIT-II:

Combinational Circuits:

Adders & Subtractor, Binary Adder-Subtractor, Decimal Adder, Binary Multiplier, Magnitude Comparators, Multiplexers, De-multiplexers, Decoders, Encoders and Code converters, Hazards and Hazard Free Relations.

UNIT-III:

Sequential circuits-I:

Basic Architectural Distinctions between Combinational and Sequential circuits, Latches, Flip Flops: SR, JK, Race Around Condition in JK, JK Master Slave, D and T Type Flip Flops, Excitation Table of all Flip Flops, Design of a Clocked Flip-Flop, Timing and Triggering Consideration, Clock Skew, Conversion from one type of Flip-Flop to another.

Unit-IV:

Sequential Circuits-II:

Synchronous – Asynchronous – Comparison, Design of Single mode Counter, Ripple Counter, Ring Counter, Shift Register, Shift Register Sequences, Ring Counter Using Shift Register, MOD Counters. Finite state machine-capabilities and limitations, Mealy and Moore models.

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UNIT-V:

Logic Families and Semiconductor Memories:

Logic Families:

DCTL, RTL, DTL, TTL and CML Logic –gate realization - Comparison,

Semiconductor Memories:

Introduction to ROM, PAL, PLA, CPLD, FPGA.

TEXT BOOKS:

1. Morris Mano , Digital Design, Prentice Hall India, 2006
2. A Anand Kumar, Switching Theory and Logic Design, Prentice Hall India, 2013

REFERENCE BOOKS:

1. Jain N P, Modern Digital Electronics, Tata McGraw Hill, 2007.
2. Fredriac J. Hill, Gerald R. Peterson, Introduction to Switching Theory and Logic Design
Wiley, 1981.



Department of Electronics and Communication Engineering

SIGNALS AND SYSTEMS

II B.Tech I Semester

Course Code: A223406

L	T	P	C
3	1	0	4

COURSE OUTCOMES:

After going through this course the student will be able to:

1. Understand the Mathematics, operations and classification of signals and systems
2. Apply the transform on standard and arbitrary signals
3. Infer the signal transmission through linear systems
4. Interpret the concepts of Laplace Transform and Z-Transform in analysis of systems.
5. Understand the process of sampling and the effects of under sampling.

UNIT I:

Signal Analysis:

Analogy between Vectors and Signals, Orthogonal Signal Space, Signal approximation using Orthogonal functions, Mean Square Error, Closed or complete set of Orthogonal functions, Orthogonality in Complex functions, Classification of Signals and systems, Exponential and Sinusoidal signals, Concepts of Impulse function, Unit Step function, Signum function.

UNIT II:

Fourier series & Fourier Transforms:

Representation of Fourier series, Continuous time periodic signals - Dirichlet's conditions, Trigonometric Fourier Series and Exponential Fourier Series, Complex Fourier spectrum. Deriving Fourier Transform from Fourier series, Fourier Transform of arbitrary signal - standard signals - Periodic Signals – Properties - Introduction to Hilbert Transform.

UNIT III:

Signal Transmission through Linear Systems:

Linear System, Impulse response, Response of a Linear System, Linear Time Invariant(LTI) System, Linear Time Variant (LTV) System, Transfer function of a LTI System, Filter characteristic of Linear System, Distortion less transmission through a system, Signal bandwidth, System Bandwidth, Ideal LPF, HPF, and BPF characteristics, Causality and Paley-Wiener criterion for physical realization, Relationship between Bandwidth and rise time.

UNIT IV:

Laplace Transforms:

Laplace Transforms (L.T), Inverse Laplace Transform, Concepts of Region of Convergence (ROC) for Laplace Transforms, Properties of L.T, Relation between L.T and F.T of a signal, Laplace Transform of certain signals using waveform synthesis.

UNIT V:

Sampling Theorem and Z-Transforms:

Graphical and analytical proof for Band Limited Signals, Impulse Sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling – Aliasing, Introduction to Band Pass Sampling.

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Concept of Z-Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms, Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

TEXT BOOKS:

1. Lathi B P, Signals, Systems & Communications, B.S. Publications, 2003.
2. Alan V. Oppenheim, Alan S. Willsky, Syed Hamid Nawab , Signals and Systems, PrenticeHall India, 1997

REFERENCE BOOKS:

1. Simon S. Haykin, Barry Van Veen , Signals and Systems, Wiley, 2003
2. Rama Krishna Rao A, Signals and Systems, 2008, Tata McGraw Hill, 2008.



Department of Electronics and Communication Engineering

PROBABILITY THEORY AND STOCHASTIC PROCESSES

II B.Tech I Semester

Course Code: A223407

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

After going through this course the student will be able to

1. Demonstrate knowledge in Probability theory, Single and multiple random variables and Random processes and their characteristics
2. Analyze operations on single and multiplier and om variables and processes
3. Compute Simple probabilities using an appropriate sample space, Expectations from probability density functions, Least-square & maximum likelihood estimators for engineering problems Mean and Covariance functions for simple random processes.
4. Designsolutionsforcomplexengineeringproblemsinvolvingrandomprocesses.
5. Understand how random variables and stochastic processes can be described and analyzed

UNIT-I:

Probability & Random variables:

Probability:

Probability introduced through Sets and Relative Frequency: Experiments and Sample Spaces, Discrete and Continuous Sample Spaces, Events, Probability Definitions and Axioms, Joint Probability, Conditional Probability, Total Probability, Bay's Theorem, Independent Events.

Random Variable:

Definition, Conditions for a Function to be a Random Variable, Discrete, Continuous and Mixed Random Variable, Distribution and Density functions, Properties, Binomial, Poisson, Uniform, Gaussian, Exponential, Rayleigh, Methods of defining Conditioning Event, Conditional Distribution, Conditional Density and their Properties.

UNIT-II:

Operations on single & multiple random variables– expectations:

Expected Value of a Random Variable, Function of a Random Variable, Moments about the Origin, Central Moments, Variance and Skew, Chebychev's Inequality, Characteristic Function, Moment Generating Function, Joint Distribution Function and its Properties, Marginal Distribution Functions, Conditional Distribution and Density – Point Conditioning, Conditional Distribution and Density – Interval conditioning, Statistical Independence, Sum of Two and more Random Variables, Central Limit Theorem, Equal and Unequal Distribution. Expected Value of a Function of Random Variables- Joint Moments about the Origin, Joint Central Moments, Joint Characteristic Functions

UNIT-III:

Random processes – Temporal characteristics:

The Random Process Concept, Classification of Processes, Deterministic and Non deterministic Processes, Distribution and Density Functions, concept of Stationarity and Statistical Independence. First-Order Stationary Processes, Second- Order and Wide- Sense Stationarity, (N-Order) and Strict-Sense Stationarity, Time Averages and Ergodicity, Mean-

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Ergodic Processes, Correlation-Ergodic Processes, Autocorrelation Function and Its Properties, Cross-Correlation function and Its Properties, Random Signal Response of Linear Systems: System Response–Mean and Mean-squared Value of System Response, autocorrelation Function of Response, Cross-Correlation Functions of Input and Output.

UNIT-IV:

Random processes – Spectral characteristics:

The Power Spectrum: Properties, Relationship between Power Spectrum and Autocorrelation Function, the Cross-Power Density Spectrum, Properties, Relationship between Cross–Power Spectrum and Cross-Correlation Function. Spectral Characteristics of System Response: Power Density Spectrum of Response, Cross-Power Density Spectrums of Input and Output.

UNIT-V:

Noise sources:

Resistive/Thermal Noise Source, Arbitrary Noise Sources, Effective Noise Temperature, Noise equivalent bandwidth, Average Noise Figures, Average Noise Figure of cascaded networks, Narrow Band noise, Quadrature representation of narrow band noise & its properties.

TEXT BOOKS:

1. Peyton Z. Peebles , Probability, Random Variables & Random Signal Principles,Tata McGraw Hill, 2001.
2. Taub and Schilling , Principles of Communication systems , Tata McGraw Hill,2008

REFERENCE BOOKS:

1. Athanasios Papoulis and S. Unnikrishna Pillai, Probability, Random Variables and Stochastic Processes, Prentice Hall India, 2002.
2. Murugesan K, Guruswamy P, Probability, Statistics & Random Processes, Anuradha Publications, 2003.



Department of Electronics and Communication Engineering

DIGITAL LOGIC DESIGN LABORATORY

II B.Tech I Semester

Course Code: A223481

L	T	P	C
0	0	2	1

Course Outcomes:

Upon completing this course, the students will be able to

1. Design of all logic gates by using universal gates.
2. Design of adder and subtractor by using logic gates.
3. Design of MSI combinational circuits by using logic gates.
4. Design and analyze counters by using flip-flops.
5. Analyze the universal shift register.

List of Experiments

Note: Minimum 10 Experiments have to be conducted

1. Design of all logic gates using NAND/NOR gates and verify the truth tables.
2. Realization of Boolean Expressions using Logic Gates.
3. Design full adder & full subtractor using NAND/NOR gates and verify the truth table.
4. Verification of 4-bit Parallel adder /subtractor circuit.
5. Design of 4-bit gray to binary code converter.
6. Design any 4 variable functions using 8:1 Multiplexer and verify.
7. Design and realization 2-bit comparator.
8. Verification of 4-bit Magnitude comparator
9. Design full adder using 3*8 Decoder and verify.
10. Design T & D flip flops using JK flip flop and verify the truth table.
11. Verification of 4-bit Decade counter
12. Verification of Universal Shift Register



Department of Electronics and Communication Engineering

BASIC SIMULATION LABORATORY

II B.Tech I Semester

Course Code: A223482

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

After going through this course the student will be able to:

1. Evaluate the operation on signals and systems using arithmetic operations and transforms
2. Application of correlation and transforms on noise removal and signal extraction
3. Compute various statistical properties of a random noise and verify whether it is stationary
4. Determine the correlation & Convolution between Signals and sequences.
5. Validate the properties and waveform synthesis of various transforms

Minimum 10 experiments to be Simulated Using MATLAB:

1. Generation of various signals and sequences (Periodic and A periodic), such as Unit Impulse, Unit step, square, saw tooth, Triangular, Sinusoidal, Ramp, Sync.
2. Operations on Signals and Sequences such as Addition, multiplication, scaling, Shifting, Folding, computation of Energy and average power.
3. Finding the Even and Odd parts of Signal/sequence and Real and imaginary parts of signal.
4. Convolution between signals and sequences.
5. Auto correlation and cross correlation between signals and sequences.
6. Verification of Linearity and Time Invariance Properties of a given continuous/Discrete system.
7. Gibbs Phenomenon.
8. Finding the Fourier Transform of a given signal and plotting its magnitude and phase spectrum.
9. Waveform synthesis using Laplace Transform.
10. Locating the Zeros and Poles and plotting the Pole-Zero maps in S plane and Z-plane for the given transfer function.
11. Generation of Guassian noise (Real and complex), Computation of its mean, M.S. value and its Skew, Kurtosis, and PSD, probability distribution function.
12. Sampling Theorem Verification.



Department of Electronics and Communication Engineering

ELECTRONIC DEVICES AND ANALOG CIRCUITS LABORATORY

II B.Tech I Semester

Course Code: A223483

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

After going through this course the student will be able to:

1. Outline the characteristics of different semiconductor devices.
2. Interpret the ripple factor, regulations of rectifiers.
3. Sketch the frequency response of small signal amplifiers.
4. Understand the concepts of UJT and observe its characteristics.
5. Design and analyze oscillator circuits.

Note: Minimum of 10 experiments:

1. Forward & Reverse Bias Characteristics of PN Junction Diode.
2. Zener diode characteristics.
3. Half Wave Rectifier with & without filters.
4. Full Wave Rectifier with & without filters.
5. Input & Output Characteristics of Transistor in CE Configuration
6. FET characteristics.
7. Clippers Clampers
8. UJT Characteristics
9. Frequency Response of CE Amplifiers
10. Frequency Response of Common Source FET amplifier.
11. Two Stage RC Coupled amplifiers
12. RC Phase shift Oscillator



PROFESSIONAL COMMUNICATION

II B.Tech I Semester

Course Code: A223009

L	T	P	C
2	0	0	1

COURSE OUTCOMES:

After going through this course the student can

1. Acquire enhanced personality
2. Demonstrate appropriate professional etiquette
3. Practice team building with strong communication skills
4. Develop problem solving skills and decision-making
5. Exhibit effective communication on digital platforms

Unit: I: Introduction to Soft Skills

Soft Skills for personal and professional development
Self Introduction in various situations
SWOC Analysis
Goal setting

Unit: II: Professional Etiquette

Etiquette-Mobile Etiquette- Netiquette
Non-Verbal Communication
Presentations – Individual & Team
Time Management

Unit: III: Team Essentials

Leadership Skills
Team Building
Negotiation Skills
Group Discussion-Functional Aspects

Unit: IV: Decision Making & Problem Solving

Logical Thinking
Decision Making
Problem Solving
Critical Thinking

Unit: V: Digital Communication

Role of Multimedia in Communication
E-Mail
Social Networking: Importance and Effects.
Communication in Corporate World

References Books:

1. Ashrif Rizvi, Effective Technical Communication, Tata Mac Graw Hill, 2018.
2. Barun, K Mitra, Personality Development and Soft Skills, Oxford University Press, 2nd Edition, 2017.



Department of Electronics and Communication Engineering

COMPLEX ANALYSIS AND FOURIER TRANSFORMS

II B.Tech II Semester

Course Code: A224010

L	T	P	C
3	0	0	3

Course Outcomes: After learning the contents of this course the students must be able to:

1. Work with the functions of complex variables and evaluation of complex differentiation.
2. Acquire the knowledge of complex power series and integration.
3. Apply the knowledge of contour integration to evaluate real integrals in engineering problems and acquire the knowledge of evaluating of conformal mapping and bilinear transformations.
4. Studying of Fourier series and defining it for various types of functions.
5. Apply Fourier sine and cosine integral theorems for a given function $f(x)$ evaluate Fourier transforms, sine and cosine transforms.

UNIT-I:

Functions of Complex Variables:

Introduction, Complex functions - limits and Continuity-Differentiability, Analytic functions and Properties, Cauchy-Riemann Equations (Cartesian and Polar), Harmonic functions, Construction of analytic functions.

UNIT-II:

Complex Integration:

Introduction, Complex integration-Line integral, Cauchy's integral theorem, Cauchy's integral formula, Generalized Cauchy's integral formula, Power series: Taylor's series, Laurent series, Singular points, Types of Singularities, Residue, Cauchy's Residue theorem.

UNIT-III:

Evaluation of Integrals & Conformal Mapping:

Introduction,-Evaluation of improper real integrals of the type (a) $\int_c^\infty f(x)dx$ (b) $\int_c^{c+2\pi} f(\cos \theta, \sin \theta)d\theta$ -

Conformal Mapping,-Critical Points-Bilinear transformation – fixed point – cross ratio - properties - invariance of circles.

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UNIT-IV:

Fourier series:

Introduction- Periodic functions- Fourier series of periodic function- Dirichlet's conditions- Even and odd functions- Change of interval- Half-range sine and cosine series.

UNIT-V:

Fourier Transforms:

Introduction- Fourier integral theorem (without proof)- Fourier integrals in complex form- Standard results- Fourier sine and cosine integrals- Fourier Transforms- Infinite and finite Fourier Transforms- Properties- Fourier sine and cosine transforms- inverse transforms, Finite Fourier transforms.

Textbooks:

1. Higher Engineering Mathematics by Dr. B. S. Grewal, Khanna Publishers.
2. A text book of Engineering Mathematics by N.P.Bali, Manesh Goyal Laxmi Publications

Reference Books:

1. Advanced Engineering Mathematics : Kreyszig, John Wiley & sons
2. Fundamentals of Complex Analysis by Saff, E. B. and A. D. Snider, Pearson.
3. Functions of Complex Variables by J.N.Sharma, Krishna Prakashan Media.



Department of Electronics and Communication Engineering

ELECTROMAGNETIC WAVES

II B.Tech II Semester

Course Code: A224408

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Understand the electric field due to different charge distributions.
2. Demonstrate the EM Field Characteristics divergence and curl of fields.
3. Interpret the Maxwell's equations for static Electric and Magnetic fields and dynamic Electromagnetic fields.
4. Analyze the behavior of EM waves in different media.
5. Solving Engineering problems on EM Wave Propagation.

UNIT I: Electrostatics-I:

Introduction to Vectors and Coordinate system- Coulomb 's Law- Electric Field Intensity – Fields due to Different Charge Distributions, Electric Flux Density, Gauss Law and Applications, Electric Potential, Relation between E and V, Maxwell's Two Equations for Electrostatic Fields, Energy Density in Electrostatics.

Unit-II: Electrostatics-II:

Convection and Conduction Currents, Dielectric Constant, Dielectric polarization, Linear, Isotropic and Homogeneous Dielectrics, Continuity Equation, Relaxation Time, Poisson 's and Laplace 's Equations, Capacitors – Parallel Plate, Coaxial, Spherical, boundary conditions in static electric field, Uniqueness theorem.

UNIT III: Magneto statics:

Biot-Savart's Law-Magnetic Field Intensity due to finite length current carrying conductor, Ampere's Circuit Law and Applications, Magnetic Flux Density, Maxwell's two Equations for Magneto static Fields, Magnetic Scalar and Vector Potentials, Forces due to Magnetic Fields, Ampere's Force Law, Magnetostatic boundary conditions.

UNIT IV: EM Wave Characteristics -I (Time-Varying Maxwell's Equations):

Faraday's Law and Transformer EMF, Inconsistency of Ampere's Law and Displacement Current Density, Maxwell's Equations in Different Forms, Time-varying Boundary Conditions at a Surface -Dielectric-Dielectric and Dielectric-Conductor Interfaces, Lorentz condition for Time-varying potentials.

UNIT V: EM Wave Characteristics –II:

Wave Equations for Conducting and Perfect Dielectric Media, Uniform Plane Waves – Definitions, Relation between E & H, Sinusoidal Variations, Wave Propagation in Conductor & Dielectric media, Polarization. Reflection and Refraction of Plane Waves – Normal and Oblique Incidences for both Perfect Conductor and Perfect Dielectrics, Brewster Angle, Critical Angle and Total Internal Reflection, Surface Impedance, Poynting Vector and Poynting Theorem.

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TEXT BOOKS:

1. Matthew N.O. Sadiku and S.V. Kulkarni, Elements of Electromagnetic, Tata McGraw Hill, 2020.
2. E.C. Jordan and K.G. Balman, Electromagnetic Waves and Radiating Systems, PHI, 2000.

REFERENCE BOOKS:

1. Engineering Electromagnetics – William H. Hayt Jr. and John A. Buck, McGrawHill, 2014.
2. Engineering Electromagnetics – Nathan Ida, Springer (India) Pvt. Ltd., New Delhi, 2005.



Department of Electronics and Communication Engineering

ANALOG AND DIGITAL COMMUNICATIONS

II B.Tech II Semester

Course Code: A224409

L	T	P	C
3	0	0	3

COURSEOUTCOMES:

After going through this course the student can

1. Demonstrate fundamental knowledge in elements of Analog and Digital Communication systems.
2. Analyze different types of analog and digital modulation systems and calculate total power & bandwidth.
3. Design an efficient Transmitter and Receiver based on SNR, bandwidth and equipment complexities.
4. Formulate and solve engineering problems in the core area of analog and digital communications in developing information transmitting systems and telemetry system.
5. Illustrate the impact of noise in analog communication systems and computation of Probability of error in digital modulation techniques.

UNIT-I:

Amplitude Modulation - I:

Review of signals and systems, Amplitude Modulation: Time and Frequency domain representations–Power and Bandwidth, AM Generators: Square law modulator-Switching modulator, AM Detectors: Square law detector – Envelope detector, DSBS Modulation: Time domain and frequency domain representations, DSB-SC Generators: Balanced Modulators-Ring Modulator, DSB-SC Detectors: Coherent detector-COSTAS Loop, Output SNR & Noise figure in AM system.

UNIT-II:

Amplitude Modulation - II:

SSB Modulation: Time and Frequency domain representations-SSB Generators and Detectors, VSB Modulation: Time and Frequency domain representation- Envelope detection of a VSB wave, Comparison and Applications of different AM Systems, Output SNR & Noise figure in DSBSC & SSB systems, Frequency Division Multiplexing.

UNIT-III:

Frequency Modulation:

Angle Modulation: Time domain representation – Single tone FM wave- NBFM and WBFM- Spectral analysis of single tone FM Wave – Power and Bandwidth, FM Generators: Indirect FM and Direct FM Generators, FM Detectors: Balanced Frequency discriminator-Ratio detector, Pre-emphasis & De-emphasis, Threshold effect, Comparison of FM between AM, Output SNR & Noise figure in FM system..

UNIT-IV:

Pulse Analog and Digital Modulations:

Pulse Analog Modulation:

Sampling process, Pulse Amplitude Modulation and Demodulation, Pulse Width Modulation and demodulation, Pulse Position Modulation and Demodulation, Time Division Multiplexing.

Pulse Digital Modulation:

Quantization process, Pulse Code Modulation (PCM), Differential PCM (DPCM), Delta Modulation (DM), Output SNR in PCM and DM system, Comparison of PCM and DM Systems.

UNIT-V:**Pass band Digital Modulations:**

Carrier Digital Modulation Schemes: ASK–PSK–DPSK–FSK–QAM, Probability of error, Error Probability in Optimal Coherent detection of PSK and FSK Modulations.

TEXTBOOKS:

1. Haykin S, Communications Systems, Wiley, 2001.
2. Sam Shanmugam, Digital and Analog Communication Systems, Wiley, 2006.

REFERENCEBOOKS:

1. Proakis JG, Digital Communications, McGraw Hill, 2000.
2. Kennedy–Davis, Electronics and Communication Systems, McGraw Hill 4th Edition, 2006.



Department of Electronics and Communication Engineering

LINEAR IC APPLICATION

II B.Tech II Semester

Course Code: A224410

L	T	P	C
2	0	0	2

COURSE OUTCOMES:

Upon completing this course, the student will be able to

1. Understand the overview of IC classifications and comparison of analog and digital IC's.
2. Understand the operating principle of operational amplifiers and working of its IC version.
3. Acquire the knowledge of active filter basics and applications of op-amps.
4. Attain the knowledge of functional diagrams and applications of IC555 and IC565.
5. Acquire the knowledge about the Data converters.

UNIT-I

Integrated Circuits: Classification, chip size and circuit complexity, basic information of Op-amp, ideal and practical Op-amp, internal circuits, Op-amp characteristics, DC and AC Characteristics, 741 op-amp and its features, modes of operation-inverting, non- inverting, differential.

UNIT-II

Op-amp and Applications: Basic information of Op-amp, instrumentation amplifier, ac amplifier, V to I and I to V converters, Sample & hold circuits, multipliers and dividers, differentiators and integrators, comparators, Schmitt trigger, Multivibrators, introduction to voltage regulators, features of 723.

UNIT-III

Active Filters & Oscillators: Introduction, 1st order LPF, HPF filters, Band pass, Band reject and all-pass filters. Oscillator types and principle of operation-RC, Wien and quadrature type, waveform generators-triangular, saw tooth, square wave and VCO.

UNIT-IV

Timers & Phase Locked Loops: Introduction to 555 timer, functional diagram, monostable and astable operations and applications, Schmitt Trigger. PLL - introduction, block schematic, principles and description of individual blocks of 565.

UNIT-V

D-A and A-D Converters: Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, and IC 1408 DAC, Different types of ADCs - Parallel comparator type ADC, counter type ADC, successive approximation ADC dualSlope integration type ADC, DAC and ADC specifications.

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TEXT BOOKS:

1. Roy Choudhury D, Shail B. Jain, Linear Integrated Circuit, New Age International, 2012
2. Ramakant A. Gayakwad, OP-AMP and Linear Integrated Circuits, Prentice Hall India, 2012.

REFERENCE BOOKS:

1. Tony Chan Carusone, David A. Johns, Kenneth W. Martin, Analog Integrated Circuit Design, John Wiley & Sons Ltd, 2011.
2. Joseph Carr, Linear IC Applications: A Designer's Handbook, Reed Educational and Publishing Pvt.Ltd, 1996
3. S. Salivahanan, V.S. Kanchana Bhaskaran, Linear IC Applications, McGra Hill, 2020



Department of Electronics and Communication Engineering

ELECTRONIC CIRCUIT ANALYSIS

II B.Tech II Semester

Course Code: A224411

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

Upon completing this course, the student will be able to

1. Design the power amplifiers
2. Design the tuned amplifiers and analyse its frequency response
3. Design Multivibrators and sweep circuits for various applications.
4. Design and understanding of time-base generators.
5. Utilize the concepts of synchronization, frequency division and sampling gates.

UNIT-I

Linear Wave Shaping: High pass, low pass RC circuits, their response for sinusoidal, step, pulse, square, and ramp inputs. High pass RC circuit as differentiator and Low pass RC circuit as integrator, attenuators.

Sampling Gates: Basic operating principles of Sampling Gates, Unidirectional and Bi-directional Sampling Gates, Four Diode Sampling Gate, Reduction of pedestal in Gate Circuits

UNIT-II

Large Signal Amplifiers: Class A Power Amplifier- Series fed and Transformer coupled, Conversion Efficiency, Class-B Power Amplifier- Push Pull and Complimentary Symmetry configurations, Conversion Efficiency, Principle of operation of Class-AB and Class-C and D Amplifiers.

UNIT-III

Tuned Amplifiers: Introduction, single Tuned Amplifiers – Q-factor, frequency response, Double Tuned Amplifiers–Q-factor, frequency response, Concept of stagger tuning and synchronous tuning

UNIT-IV

Multivibrators: Analysis and Design of Bistable, Monostable, Astable Multivibrators and Schmit trigger using Transistors.

UNIT-V

Time Base Generators: General features of a Time base Signal, Methods of Generating Time Base Waveform, concepts of Transistor Miller and Bootstrap Time Base Generator, Methods of Linearity improvement.

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TEXTBOOKS:

1. Jacob Millman, Christos C Halkias-Integrated Electronics, McGraw Hill Education.
2. J.Millman, H.Taub and Mothiki S.Prakash Rao- Pulse, Digital and Switching Waveforms–2nd Ed.,TMH,2008,

REFERENCEBOOKS:

1. David A.Bell-Electronic Devices and Circuits,5th Ed.,Oxford.
2. Robert L.Boylestead, Louis Nashelsky Electronic Devices and Circuits theory, 11thEd.,Pearson,2009
3. Ronald J. Tocci-Fundamentals of Pulse and Digital Circuits, 3rd Ed., 2008.
4. David A. Bell-Pulse, Switching and Digital Circuits, 5th Ed., Oxford, 2015.



Department of Electronics and Communication Engineering

ANALOG AND DIGITAL COMMUNICATIONS LABORATORY

II B.Tech II Semester

Course Code: A224484

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Demonstrate knowledge in different Analog and Digital Communication Systems.
2. Compare the characteristics of various Analog and Digital modulation schemes and analyze the performances.
3. Develop various analog and digital modulation and demodulation systems
4. Explain how Pulse code modulation is applied to transform an analog signal into a digital one and transmitted through the digital communication network.
5. Design the shift keying based digital modulation techniques for the transmission of digital information

Note: Any 10 experiments to be conducted

1. Amplitude modulation and demodulation
2. DSBSC modulation and demodulation
3. SSB modulation and demodulation
4. Frequency modulation and demodulation
5. Pulse Amplitude Modulation and demodulation
6. Pre-emphasis and De-emphasis
7. Verification of Sampling Theorem
8. Pulse code modulation and demodulation
9. Delta modulation and demodulation
10. PSK Modulation and demodulation
11. FSK Modulation and demodulation
12. DPSK and QPSK Modulation and demodulation



Department of Electronics and Communication Engineering

LINEAR IC APPLICATIONS LABORATORY

II B.Tech II Semester

Course Code: A224485

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Design and analyze filter circuits using op-amp.
2. Design and analyze the various linear application of op-amp.
3. Design and analyze the various application of 555 timer.
4. Design and analyze the performance of oscillators.
5. Design and analyze IC 565, IC 723 and converters.

Note: Minimum 10 Experiments have to be conducted

1. Design of 2-input adder and subtractor using IC 741.
2. Design of Integrator and Differentiator circuit using IC 741.
3. Design of first order LPF and HPF using IC 741.
4. Design of Astable multivibrator using IC 741.
5. Design of triangular wave generator using IC 741.
6. Design of RC phase shift oscillator using IC 741.
7. Design of Wein-bridge oscillator using IC 741.
8. Analyze the operation of Weighted resistor type DAC and R-2R Ladder type DAC
9. Design Astable Multivibrator Circuit using IC 555 Timer.
10. Design Monostable Multivibrator Circuit using IC 555 Timer.
11. Calculation of Capture Range & Lock Range Using IC 565 PLL
12. Design a Voltage Regulator using IC 723.



Department of Electronics and Communication Engineering

ELECTRONICS CIRCUIT ANALYSIS LABORATORY

II B.Tech II Semester

Course Code: A224486

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

Upon completing this course, the students will be able to

1. Design power amplifiers and find its efficiency
2. Design tuned amplifiers and find its Q-factor
3. Design various multivibrators and sweep circuits.
4. Understand the Schmitt trigger response for various gain
5. Design the unidirectional bidirectional Sampling gates

Note: Minimum 10 Experiments have to be conducted

1. Design transformer coupled class A power amplifier and draw the input and output waveforms find its efficiency
2. Design class B power amplifier and draw the input and output waveforms, and find its efficiency.
3. Design the complementary symmetry push-pull amplifier and draw the input and output waveforms.
4. Design class C power amplifier and draw the input and output waveforms
5. Design a single tuned amplifier and determine the Q of its tuned circuit practically.
6. Design a Bistable Multivibrator and draw the wave forms at base and collector of transistors.
7. Design an Astable Multivibrator and draw the wave forms at base and collector of transistors.
8. Design a Monostable Multivibrator and draw the input and output waveforms
9. Design and Analyze the operation of Schmitt trigger circuit.
10. Design a Bootstrap sweep circuit using BJT and draw its output time base waveform
11. Design a Miller sweep circuit using BJT and draw its output time base waveform.
12. Design unidirectional and bidirectional sampling gates



QUANTITATIVE METHODS & LOGICAL REASONING

II B.Tech II Semester

Course Code: A224011

L	T	P	C
2	0	0	1

Course Outcomes: At the end of the completion of the course a student is expected –

1. To perform well in various competitive exams and placement drives.
2. To solve basic and complex mathematical problems in short time.
3. To become strong in Quantitative Aptitude and Reasoning which can be applied for GRE, GATE, GMAT or CAT exam also.
4. To develop problem solving skills and analytical abilities, which play a great role in corporate and industry set up.

Quantitative Aptitude and Reasoning:

Unit – I :

Number System: Speed Maths, Numbers, Factors, Prime & Co Primes, LCM & HCF, Divisibility Rules, Finding Unit Place Digit and Last Two Digits of an Expression

Ratio, Proportion and Variations: Definition of Ratio, Ratio of Proportion, Comparison of Ratios, Compound ratio, Direct and Indirect Proportion

Percentages: Converting Fractions and Decimal into Percentages, Successive Percentage, Populations, Expenditure and Savings

Profit and loss: Relation between Cost Price and Selling Price, Discount and Marked Price, Gain or Loss Percentages on Selling Price

Simple and Compound Interest: Problems on Interest (**I**), Amount (**A**), Principal (**P**) and Rate of Interest (**R**) difference between the Simple Interest and Compound Interest for 2 and 3 years.

Unit – II :

Partnership: Relation between Partners, Period of Investment and Shares

Averages, Ages and Allegation : Average of Different Groups, Change in Averages by Adding, Deleting and Replacement of Objects, Problems on ages, Allegation Rule, Mean Value of the Mixture, Replacement of Equal Amount of Quantity.

Time and Work: Men and Days, Work and Wages, Pipes and Cisterns, Hours and Work, Alternate Days Concept,

Time and Distance: Difference between the Average and Relative Speeds, Reaching the Destination Late and Early, Stoppage Time Per Hour, Time and Distance between Two Moving Bodies : Train Crossing Man - same and opposite directions, Speed of Boat and Stream,

Unit – III :

Progressions and Quadratic Equations : Arithmetic, Geometric and Harmonic Progressions, Arithmetic Mean, Geometric Mean and Harmonic Mean and their Relations. General form of Quadratic Equation, Finding the Roots of Quadratic Equation, Nature of the Roots.

Permutation and Combination: Fundamental Rules, Problems on Permutations & combinations.

Probability : Definition of probability, Notations and Formulae, Problems on Probability.

Data Interpretation and Data Sufficiency: Tabular and Pie-charts, Bar and Line Graphs, Introduction to Data Sufficiency, Problems on Data Sufficiency.

Unit – IV:

Deductions: Statements and conclusions using Venn diagram and Syllogism Method

Series completion: Number series, Alphabet series, Letter Series.

Coding and Decoding: Letter coding, Number coding, Number to letter coding, Matrix Coding, Substitution, Mixed Letter Coding, Mixed Number Coding, Deciphering Individual Letter Codes by Analysis.

Analytical Reasoning Puzzles:

Problems on Linear, Double line-up and Circular Arrangements, Selections and Comparisons.

Blood Relations:

Defining the various Relations among the Members of a Family, Solving Blood Relation Puzzles by using Symbols and Notations. Problems on Coded Relations.

Unit – V:

Direction sense Test: Sort of directions in puzzles distance between two points, problems on shadows, Application of triangular triplets.

Clocks: Relation between Minute-Hour Hands, Angle vs Time, Exceptional Cases in Clocks

Calendars: Definition of a Leap Year, Finding the Odd days, Finding the Day of any Random Calendar Date, repetition of Calendar Years.

Cubes and Dices: Finding the Minimum and Maximum Number of Identical Pieces and Cuts, Painting of Cubes and cuts, Problems on Dice.

Venn Diagrams: Circular Representation of given words, Geometrical Representation of Certain class, Set theory based Problems.

Text Books:

1. Verbal Reasoning, GL Barrons, Pinterest, Latest Edition 2019
2. A Modern Approach to Logical Reasoning & Quantitative Aptitude, R S Agarwal, S. Chand, Publications, Revised edition, 2019

Reference Books:

1. Quantitative Aptitude, G.L Barrons, Pinrest 2019
2. Quantitative Aptitude, Abhijit Guha, Mc Graw Hills, Edition 2019
3. Quantitative Aptitude, U. Mohan Rao SCITECH

MICRO PROCESSORS AND MICRO CONTROLLERS

III B.Tech I Semester

Course Code: A225414

L	T	P	C
3	1	0	4

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Acquire knowledge about Microprocessors, Microcontroller and its need.
2. Ability to identify basic architecture of different Microprocessors & Microcontroller
3. Develop systems for interfacing of different peripheral devices microprocessor & Microcontrollers
4. Write a program to interface microprocessor and microcontroller for different applications.
5. Develop microcontroller application for different domain

UNIT-I:

8086 Architecture:

8086 Architecture-Functional diagram, Register Organization, Memory Banks, Memory Segmentation, Programming Model, memory addresses, Physical Memory Organization, Signal descriptions of 8086, Minimum & Maximum Mode configuration of 8086, Timing diagrams.

UNIT-II:

Instruction Set and Assembly Language Programming of 8086:

Instruction formats, Addressing modes, Instruction Set, Assembler Directives, Macros, Simple Programs involving Logical, Branch and Call Instructions, sorting, Evaluating Arithmetic Expressions, String Manipulations.

UNIT-III:

Interfacing RAM with 8086:

8255 PPI – Modes – Interfacing with 8086, 8251 – Modes - Interfacing with 8086, 8259 Interfacing with 8086, 8257 DMA – Modes, Interfacing 8086 with Stepper Motor and DAC 0800.

UNIT-IV:

Introduction to Microcontrollers:

Overview of 8051 Microcontroller, Architecture, I/O Ports, Memory Organization, Addressing Modes and instruction set of 8051

UNIT-V:

8051 Real Time Control:

Timers/Counters, Serial Communication and Interrupts, Programming 8051 Timers and Counters, Programming the Serial Communication interrupts, Programming Timer Interrupts, Programming external Hardware Interrupts.

TEXT BOOKS:

1. Ray A K and Bhurchandani K M, Advanced Microprocessors and Peripherals , Tata McGraw-Hill, 2006.
2. Kenneth. J. Ayala, The 8051 Microcontroller ,Cengage Learning, 2005

REFERENCE BOOKS:

1. D. V. Hall, Microprocessors and Interfacing, Tata McGraw-Hill, 2006
2. Uma Rao K., Andhe Pallavi , The 8051 Microcontrollers, Architecture and Programming and Applications, Pearson, 2009



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CONTROL SYSTEMS

III B.Tech I semester

Course Code: A225415

L	T	P	C
3	1	0	4

COURSE OUTCOMES:

At the end of the Course, the student will be able to:

1. Understand and analyzing different linear-time-invariant systems using transfer function.
2. Analyze system response in time domain for first and second order systems and evaluate static error.
3. Understand the concept of stability and its assessment for linear-time invariant systems.
4. Analyze system response in frequency domain and understanding compensation networks.
5. Realize the concept of state variable, state space and analyze the stability of linear Time discrete systems.

UNIT I:

Introduction to control Systems:

Concepts of control systems, open & closed loop control systems-examples, Industrial Control systems examples, Mathematical models of physical systems.

Transfer Function Representation: Block diagram representation of systems considering electrical systems as examples- Block diagram algebra – Representation by signal flow graph- Reduction using Manson's gain formula

UNIT II:

Time Response Analysis: Standard test signals – Time response of first order systems- Characteristic Equation of feedback controls systems, Transient response of second order systems – Time domain specifications - Steady state response – Steady state errors & error constants – Effects of proportional derivative, proportional integral systems and PID controllers, Application of Proportional, Integral and Derivative Controllers.

UNIT III:

Stability analysis in S- Domain:

The concept of stability – Routh's stability criterion – qualitative stability & conditional stability – limitations of Routh's stability

Root Locus technique: The root locus concept – construction of root loci-effects of adding poles and zeros to $G(s)H(s)$ on the root loci.



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UNIT IV:

Frequency-response analysis:

Relationship between time and frequency response, Polar plots, Bode plots, Nyquist stability criterion. Relative stability using Nyquist criterion – gain and phase margin. Closed-loop frequency response, Introduction to compensation techniques

UNIT V:

State space analysis:

Concepts of state, state variables and state model, derivation of state models from block diagrams, Diagonalization-Solving the Time invariant state Equation- State Transition Matrix and it's Properties-Concepts of Controllability and Observability.

TEXT BOOKS:

1. Nagoor kani A, Control Systems Engineering, CBS PUB & DIST,2020
2. Nagrath I J & Gopal M, Control Systems Engineering, New Age International, 2009.

REFERENCE BOOKS:

1. A.Anand Kumar, Control Systems, PHI Publications, Second Edition,2014.
2. Jagan N.C, Control Systems, BS Publications, 2014.

ANTENNA AND WAVE PROPAGATION

III B.Tech I semester

Course Code: A225416

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Understand different antennas, field analysis and their applications to antenna elements.
2. Distinguish the mechanism of radiation, different antenna characteristics, mathematical relations their estimates in practical cases.
3. Analyze and design the working of different antenna's and to interpret the radiation pattern of planar arrays from the knowledge of linear arrays.
4. Obtain the capability to differentiate and report the electromagnetic radiation levels in the Atmosphere and any radio transmissions.
5. Design Microwave antenna Systems from specification

UNIT I:

Antenna Basics:

Basic Antenna Parameters – Patterns, Beam Area, Radiation Intensity, Beam Efficiency, Directivity-Gain-Resolution, Antenna Apertures, Effective Height. Fields from Oscillating Dipole, Field Zones, Front - to-back Ratio, Antenna Theorems, Radiation, Retarded Potentials – Helmholtz Theorem.

Thin Linear Wire Antennas:

Radiation from Small Electric Dipole, Quarter Wave Monopole and Half Wave Dipole – Current Distributions, Field Components, Radiated Power, Radiation Resistance, Beam Width, Directivity, Effective Area and Effective Height, Natural Current Distributions, Far Fields and Patterns of Thin Linear Centre-fed Antennas of Different Lengths. Loop Antennas - Small Loop, Comparison of Far Fields of Small Loop and Short Dipole, Radiation Resistances and Directivities of Small Loops (Qualitative Treatment).

UNIT II:

Antenna Arrays:

Point Sources – Definition, Patterns, arrays of 2 Isotropic Sources - Different Cases, Principle of Pattern Multiplication, Uniform Linear Arrays – Broadside Arrays, End fire Arrays.

Antenna Measurements:

Introduction, Concepts - Reciprocity, Near and Far Fields, Coordinate System, Sources of Errors. Patterns to be Measured, Directivity Measurement, Gain Measurements (by Comparison, Absolute and 3-Antenna Methods).

UNIT III:

VHF, UHF and Microwave Antennas - I:

Arrays with Parasitic Elements, Yagi-Uda Array, Folded Dipoles and their Characteristics, Helical Antennas – Helical Geometry, Helix Modes, Horn Antennas – Types, Fermat’s Principle, Optimum Horns.

VHF, UHF and Microwave Antennas – II:

Microstrip Antennas – Introduction, Features, Advantages and Limitations, Rectangular Patch Antennas – Geometry and Parameters, Characteristics of Microstrip Antennas. Reflector Antennas – Introduction, Flat Sheet and Corner Reflectors, Paraboloidal Reflectors – Geometry, Pattern Characteristics, Feed Methods, Reflector Types – Related Features.

UNIT IV:

Wave Propagation:

Definitions, Categorizations and General Classifications, Different Modes of Wave Propagation, Ray/Mode Concepts.

Ground Wave Propagation:

Plane Earth Reflections, Space and Surface Waves, Wave Tilt, Curved Earth Reflections.

Space Wave Propagation:

Field Strength Variation with Distance and Height, Effect of Earth’s Curvature, Absorption, Super Refraction, M-Curves and Duct Propagation, Scattering Phenomena, Troposphere Propagation.

UNIT V:

Sky Wave Propagation:

Structure of Ionosphere, Refraction and Reflection of Sky Waves by Ionosphere, Ray Path, Critical Frequency, MUF, LUF, OF, Virtual Height and Skip Distance, Relation between MUF and Skip Distance, Multi-hop Propagation.

TEXT BOOKS:

1. Kraus J D, Marhefka R J and Ahmad S. Khan , Antennas and Wave Propagation, Tata McGraw-Hill, 2010.
2. Jordan E C and Balmain K G, Electromagnetic Waves and Radiating Systems, Prentice Hall India, 2000.

REFERENCE BOOKS:

1. Balanis C A, Antenna Theory, John Wiley, 2005.
2. K.D. Prasad & Satya Prakashan, Antennas and Wave Propagation, Tech India Publications, 2001.

COMPUTER ORGANIZATION & OPERATING SYSTEMS
(Professional Elective-I)

III B.Tech. I Semester

Course Code: A225417

L	T	P	C
3	0	0	3

Course Outcomes:

After completion of this course the student will able to

1. Visualize the organization of different blocks in a computer.
2. Utilize the micro-level operations to control different units in a computer.
3. Understand the memory system and different ways of communicating with IO devices and standard IO interfaces.
4. Understand the design of memory unit, learn the overview of operating system.
5. Learn about major activities of an OS with regard to file management and the operating system functions.

UNIT-I

Basic Structure of Computers: Computer Types, Functional Units, Basic Operation Concepts, Bus Structures, Software, Performance, Multiprocessors and Multi Computers, Data Representation, Fixed Point Representation, Floating Point Representation.

Register Transfer Language and Micro Operations: Register Transfer Language, Register Transfer, Bus and Memory Transfer, Arithmetic Micro Operations, Logic Micro Operations, Shift Micro Operations, Arithmetic Logic Shift Unit.

UNIT-II:

Basic Computer Organization and Design:

Instruction Codes, Computer Registers, Computer Instructions, Instruction Cycle, Timing and Control, Memory Reference Instructions, Input, Output and Interrupt.

Central Processing Unit:

Stack Organization, Instruction Formats, Addressing Modes, Data Transfer and Manipulation, Program Control, Reduced Instruction Set Computer (RISC).

UNIT-III:

The Memory System: Basic Concepts, Semiconductor RAM, Types Of Read Only Memory (ROM), Cache Memory, Performance Considerations, Secondary Storage RAID, Direct Memory Access (DMA).

Input-Output Organization: Peripheral Devices, Input-Output Interface, Asynchronous Data Transfer Modes, Priority Interrupt, Input-Output Processor (IOP), Introduction to Peripheral Components, Interconnect (PCI) Bus, Introduction to Standard Serial Communication Protocols like RS232, USB, IEEE1394.

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UNIT - IV:

Operating Systems Overview: Overview of Computer Operating Systems- Functions, Protection and Security, Distributed Systems, Special Purpose Systems, Operating Systems Structures Operating System Services and Systems Calls, System Programs, Operating System Generation.

Memory Management: Swapping, Contiguous Memory Allocation, Paging, Structure of Paging Table, Segmentation, Virtual Memory, Demand Paging, Page-Replacement Algorithms, Allocation of Frames, Deadlock characterization.

UNIT - V:

File System Interface: The Concept of a File, Access Methods, Directory Structure, File System Mounting, File Sharing, Protection.

File System Implementation: File System Structure, File system Implementation, Directory Implementation, Allocation Methods, Free-Space Management.

TEXT BOOKS:

1. Carl Hamacher, ZvonksVranesic, SafeaZaky - Computer Organization, 5th Edition, McGraw Hill.
2. M. Moris Mano -Computer Systems Architecture, 3rd Edition, Pearson
3. Abraham Silberchatz, Peter B. Galvin, Greg Gagne -Operating System Concepts, 8th Edition, John Wiley.

REFERENCE BOOKS:

1. William Stallings- Computer Organization and Architecture , 6thEdition, Pearson
2. Stallings - Operating Systems – Internals and Design Principles, 6thEdition, Pearson Education, 2009.
3. Principles of Operating Systems, B.L. Stuart, Cengage Learning, India Edition.

DATA COMMUNICATIONS AND COMPUTER NETWORKS
(Professional Elective–I)

III B.Tech. I Semester

Course Code: A225418

L	T	P	C
3	0	0	3

Course Outcomes: Upon completing this course, the student will be able to

1. Know the Categories and functions of various Data communication Networks
2. Design and analyze various error detection techniques.
3. Demonstrate the mechanism of routing the data in network layer
4. Know the significance of various Flow control and Congestion control Mechanisms
5. Know the Functioning of various Application layer Protocols.

UNIT – I: INTRODUCTION TO DATA COMMUNICATIONS:

Components, Data Representation, Data Flow, Networks- Distributed Processing, Network Criteria, Physical Structures, Network Models, Categories of Networks Interconnection of Networks, The Internet - A Brief History, The Internet Today, Protocol and Standards - Protocols, Standards, Standards Organizations, Internet Standards. Network Models, Layered Tasks, OSI model, Layers in OSI model, TCP/IP Protocol Suite.

UNIT – II: DATA LINK LAYER:

Links, Access Networks, and LANs- Introduction to the Link Layer, The Services Provided by the Link Layer, Types of errors, Redundancy, Detection vs Correction, Forward error correction Versus Retransmission Error-Detection and Correction Techniques, Parity Checks, Check summing Methods, Cyclic Redundancy Check (CRC), Framing, Flow Control and Error Control protocols , HDLC, Multiple Access Protocols, Random Access, ALOHA, Controlled access, MAC Protocol.

UNIT – III: THE NETWORK LAYER:

Introduction, Forwarding and Routing, Network Service Models, Virtual Circuit and Datagram Networks-Virtual-Circuit Networks, Datagram Networks, Origins of VC and Datagram Networks, Inside a Router-Input Processing, Switching, Output Processing, Queuing, The Routing Control Plane, The Internet Protocol (IP): Forwarding and Addressing in the Internet-Datagram format, Ipv4 Addressing, Internet Control Message Protocol (ICMP), IPv6

UNIT – IV: TRANSPORT LAYER:

Introduction and Transport Layer Services : Relationship Between Transport and Network Layers, Overview of the Transport Layer in the Internet, Multiplexing and Demultiplexing, Connectionless Transport: UDP -UDP Segment Structure, UDP Checksum, Principles of Reliable Data Transfer-Building a Reliable Data Transfer Protocol, Pipelined Reliable Data Transfer Protocols, Go-Back-N(GBN), Selective Repeat(SR), Connection Oriented Transport: TCP - The TCP Connection, TCP Segment Structure, Congestion Control.

UNIT – V: APPLICATION LAYER:

Principles of Networking Applications – Network Application Architectures, Processes Communicating, Transport Services Available to Applications, Transport Services Provided by the File Transfer: FTP - FTP Commands and Replies, Electronic Mail in the Internet- STMP, Comparison with HTTP, DNS-The Internet’s Directory Service – Service Provided by DNS, Overview of How DNS Works, DNS Records and messages.

TEXTBOOKS:

1. Kurose James F, Keith W- Computer Networking A Top-Down Approach,6th Edition, Pearson.
2. Behrouz A. Forouzan - Data Communications and Networking, 4th Edition, McGraw-Hill Education

REFERENCES:

1. Bhusan Trivedi - Data communication and Networks, Oxford university press, 2016
2. Andrew S Tanenbaum - Computer Networks,4th Edition, Pearson Education
3. W. A. Shay - Understanding Communications and Networks, 3rd Edition, Cengage Learning.

INFORMATION THEORY AND CODING
(Professional Elective-I)

III B.Tech. I Semester

Course Code: A225419

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course, students will be able to:

1. Understand the concept of information theory, coding techniques and errors related to it.
2. Compare the different coding techniques.
3. Formulate codes using different coding techniques
4. Apply different coding techniques to develop an error free communication system.
5. Inspect error detection and correction in various coding technique.

UNIT-I:

Basics of Information Theory:

Entropy, Entropy for discrete ensembles, Information rate, source coding: Shannon's noiseless coding theorem, Shannon's noisy coding theorem, Mutual Information, Shannon- Hartley law

UNIT-II:

Source Coding:

Encoding of the Source Output, Shannon's Encoding Algorithm. Shannon Fano Encoding Algorithm, Huffman codes, Extended Huffman coding, Arithmetic Coding, Lempel – Ziv Algorithm, Calculations of Channel capacity and bounds for Discrete Channel, Applications to continuous channels

UNIT-III:

Information Channels:

Communication Channels, Channel Models, Channel Matrix, Joint probability Matrix, Binary Symmetric Channel, System Entropies, Mutual Information, Channel Capacity, Channel Capacity of : Binary Symmetric Channel, Binary Erasure Channel, Morgan's Theorem, Continuous Channels

UNIT-IV:

Error Control Coding:

Examples of Error control coding, methods of Controlling Errors, Types of Errors, Linear Block Codes: matrix description of Linear Block Codes, Error Detection and Error Correction Capabilities of Linear Block Codes, Single Error Correcting hamming Codes, Binary Cyclic Codes: Algebraic Structure of Cyclic Codes, Encoding using an (n-k) Bit Shift register, Syndrome Calculation, Error Detection and Correction

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UNIT-V:

Convolution Arithmetic Codes:

Convolution Encoder, Time domain approach, Transform domain approach, Code Tree, Trellis and State Diagram, Viterbi Algorithm

TEXT BOOKS:

1. Abramson N, Information and Coding, McGraw Hill, 1963.
2. Mansurpur M, Introduction to Information Theory, McGraw Hill, 1987.

REFERENCE BOOKS:

1. Ash R B, Information Theory, Prentice Hall, 1970.
2. Chitode J S, Information Theory and coding, Technical publication, 2009.

Department of Electronics and Communication Engineering
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INTRODUCTION TO MICROCONTROLLERS
(Open Elective – 1)

III B.Tech I SEMESTER

Course Code: A225420

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Interpret the internal organization of 8051 with its unique features.
2. Understand the basic concept of various addressing modes, instruction set and simple Assembly programs
3. Provide Knowledge about special function registers in 8051.
4. Understand the fundamental concept of interfacing.
5. Study the basic concepts of Advanced ARM processors

UNIT-I:

8051 Microcontrollers:

Introduction-Microprocessors & microcontrollers- Comparison - RISC, CISC, Harvard and Von Neumann architectures-Architecture- Pin Description – I/O Ports- PC, DPTR, PSW register, Memory organization

UNIT-II:

8051 Addressing modes: Introduction – addressing modes in 8051 – Immediate, Register, Direct, Register indirect, indexed and implied addressing modes.

Basic assembly language programming concepts: Assembly language program format- Instruction set of 8051- Simple programs.

UNIT-III:

Special Function Registers: Timer and Counters in 8051 – Modes of operation – TCON, TMOD, - Serial communication in 8051 – SCON, SBUF, PCON registers, Interrupt in 8051, Interrupt enable register, Interrupt priority registers - Simple programs.

UNIT-IV:

Interfacing the 8051: Interfacing LCD, Matrix keyboard, 7 Segment display, Analog to Digital Converter, Digital to analog converter, Stepper motor with 8051

UNIT-V:

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ARM Architecture: Introduction to ARM-Features of ARM-ARM Processor family-ARM Processor categories- Application of ARM-ARM Architecture – Register & Special registers-PSR, APSR, IPSR, EPSR, PRIMASK and control register- ARM instruction set.

TEXT BOOKS:

1. The 8051 Microcontroller, Kenneth. J. Ayala, Cengage Learning, 3rd Ed, 2004.
2. Raj Kamal , Microcontrollers Architecture, Programming, Interfacing and System Design,Pearson, 2005.

REFERENCE BOOKS:

1. Mazidi and Mazidi, The 8051 Microcontroller and Embedded Systems, Prentice Hall India, 2000.
2. ARM System Developers guide, Andrew N SLOSS, Dominic SYMES, Chris WRIGHT, Elsevier,2012



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Department of Electronics and Communication Engineering

ELECTRONIC MEASURING INSTRUMENTS
(Open Elective-I)

III B.Tech I Semester

Course Code: A225421

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Acquire knowledge in Characteristics of Instruments, measurement of electrical quantities
2. Measure and analyze the signals using CRO.
3. Study the performance electronic systems using signal generators and wave analyzers
4. Acquire knowledge on Transducers and understanding their applications in real time world
5. Understanding the acquisition and storing data of non-electrical and electrical quantities

UNIT-I: Measurements and measuring systems:

Functional Diagram of Instrumentation System, Static characteristics: Accuracy - Precision – Resolution – Sensitivity – measurement Errors, Dynamic Characteristics: Speed of response – fidelity – Lag - Dynamic error - Statistical Analysis, Basic meter movement, DC Ammeters: Multi-range, DC voltmeters: Multi-range, AC voltmeters: Rectifier type - Thermocouple Type, Ohmmeters: Series type and Shunt type, Multimeter

UNIT-II: Oscilloscopes:

Oscilloscope block diagram, Cathode Ray Tube, Vertical Deflection System, Horizontal Deflection System, Dual Beam CRO, Dual Trace CRO, Measurements with CRO: Amplitude – Time period - Frequency – Phase, Lissajous patterns, Sampling Oscilloscope, Analog Storage Oscilloscope, Digital Storage Oscilloscope.

UNIT-III: Signal Generators and Signal analyzers:

Signal Generators:

Function generator, RF signal generators, Sweep frequency generator.

Signal analyzers:

Wave analyzers: Frequency Selective Wave Analyzer - Heterodyne Wave Analyzer, Total Harmonic Distortion Analyzer, Spectrum Analyzer.

UNIT-IV: Transducers:

Classification of Transducers, Strain gauges, Capacitive Transducers, Linear Variable Differential Transducer (LVDT), Piezoelectric Transducer, Thermocouple, Thermistor, Measurement of Displacement, Velocity, Acceleration, Vibration, Force, Pressure, Fluid Flow, Liquid Level and Temperature.



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UNIT-V: Bridges and Recorders:

Bridges:

Wheat Stone Bridge, Kelvin Bridge, Maxwell Bridge, Schering Bridge and Wien Bridge

Recorders:

Magnetic disk and tape Recorders, LED, LCD, Dot matrix display, Strip Chart Recorder, X-Y Recorder

TEXT BOOKS:

1. Albert D. Helfrick, Cooper William D, Modern Electronic Instrumentation and Measurement Techniques, Prentice-Hall of India, 1997.
2. Kalsi H S, Electronic instrumentation, Tata Mcgraw Hill, 2015.

REFERENCE BOOKS:

1. David A. Bell, Electronic Instrumentation & Measurements, Prentice-Hall, 2003.
2. Sawhney A K, PuneetSawhney , A Course in Electrical and Electronic Measurements and Instrumentation, Dhanpat Rai & Company, 2016

DIGITAL ELECTRONICS FOR ENGINEERING
(Open Elective–I)

III B.Tech I Semester

Course Code: A225422

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Demonstrate the basic theorems of Boolean algebra, logic gates, combinational and sequential circuits and memories.
2. Analyze the combinational and sequential circuits and memories.
3. Design of logic circuits
4. Understanding the concepts of sequential circuits
5. Demonstrate the basic counter circuits

UNIT-I:

Number System and minimization techniques:

Number System:

Review of number system and base conversion, complements, signed binary numbers, Floating point number representation, Error detection & Correction (Hamming Code), Boolean Algebra, postulates

UNIT-II:

Minimization techniques

Basic logic gates, Canonical and Standard Form, Minimization of switching function using theorem, The Karnaugh Map Method-Up to Four Variable Maps.

UNIT-III:

Combinational Circuits:

Adders & Subtractor, Binary Adder-Subtractor, Decimal Adder, Binary Multiplier, Magnitude Comparators, Multiplexers, De-multiplexers, Decoders, Encoders Hazards and Hazard Free Relations.

UNIT-IV:

Sequential circuits-I:

Basic Architectural Distinctions between Combinational and Sequential circuits, Latches, Flip Flops: SR, JK, Race Around Condition in JK, JK Master Slave, D and T Type Flip Flops, Excitation Table of all Flip Flops, Design of a Clocked Flip-Flop, Timing and Triggering Consideration, Clock Skew, Conversion from one type of Flip-Flop to another.

Unit-V:

Sequential Circuits-II:

Synchronous – Asynchronous – Comparison, Design of Single mode Counter, Ripple Counter, Ring Counter, Shift Register, Shift Register Sequences, Ring Counter Using Shift Register, MOD Counters

TEXT BOOKS:

1. Morris Mano , Digital Design, Prentice Hall India, 2006
2. A Anand Kumar, Switching Theory and Logic Design, Prentice Hall India, 2013

REFERENCE BOOKS:

1. Jain N P, Modern Digital Electronics, Tata McGraw Hill, 2007.
2. Fredriac J. Hill, Gerald R. Peterson, Introduction to Switching Theory and Logic Design
Wiley, 1981

MICRO PROCESSORS AND MICRO CONTROLLERS LABORATORY

III B.Tech I Semester

Course Code: A225488

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

After going through this course the student will be able to:

1. Apply the fundamentals of assembly level programming of microprocessors and microcontrollers.
2. Build a program on a microprocessor using instruction set of 8086 and 8051.
3. Evaluate Assembly language program for 8086 and 8051 microcontroller to interface peripheral devices for simple applications
4. Develop assembly language programs for various applications using 8051 microcontroller
5. Understand the development of prototype using combination of hardware and software

Note: Minimum 12 Experiments have to be conducted

1. Introduction to MASM.
2. Programs for 16 bit Arithmetic Operations for 8086.
3. Program for sorting an array for 8086.
4. Program for searching a number or character in a string for 8086.
5. Programs for String Manipulations for 8086.
6. Interfacing to 8086 and programming to control Stepper Motor.
7. Interfacing ADC to 8086.
8. Interfacing DAC to 8086.
9. Serial Communication between Two Microprocessors using 8255.
10. Programming using Arithmetic, Logical and Bit Manipulation Instructions of 8051.
11. Program and verify timer/counter in 8051
12. Program and verify interrupt handling in 8051
13. UART operation in 8051
14. Interfacing LCD to 8051.
15. Data transfer from peripheral to memory through DMA Controller 8237/8257

ADVANCED ENGLISH COMMUNICATION SKILLS LABORATORY

III B.Tech. I Semester

Course Code: A225087

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

At the end of the course a student is expected to:

1. Enhance reading and active listening techniques for a faster and better comprehension.
2. Exhibit strong writing skills to exhibit ideas effectively in social and professional situations.
3. Demonstrate effective presentation skills.
4. Develop critical thinking, problem-solving, decision-making and communication skills.
5. Display confidence during job interviews.

SYLLABUS:

1. Activities on Listening and Reading Comprehension: Active Listening – Development of Listening Skills Through Audio clips - Benefits of Reading– Methods and Techniques of Reading – Basic Steps to Effective Reading – Common Obstacles – Discourse Markers or Linkers - Sub- skills of reading –Reading for facts, negative facts and Specific Details- Guessing Meanings from Context, Inferring Meaning-Critical Reading—Reading Comprehension—Exercises for Practice.

2. Activities on Writing Skills: Vocabulary for Competitive Examinations – Planning for Writing – Improving Writing Skills-Structure and presentation of different types of writing– Free Writing and Structured Writing- Letter Writing –Writing a Letter of Application –Resume vs. Curriculum Vitae–Writing a Résumé–Styles of Résumé-e- Correspondence–Emails–Blog Writing- (N)etiquette– Report Writing – Importance of Reports – Types and Formats of Reports– Technical Report Writing– Exercises for Practice.

3. Activities on Presentation Skills - Starting a conversation – responding appropriately and relevantly – using the right language and body language–Role Play in different situations including Seeking Clarification, Making a Request, Asking for and Refusing Permission, Participating in a Small Talk–Oral presentations (individual and group) through JAM sessions- PPTs–Importance of Presentation Skills– Planning, Preparing, Rehearsing and Making a Presentation – Dealing with Glossophobia or Stage Fear – Understanding Nuances of Delivery - Presentations through Posters/Projects/Reports – Checklist for Making a Presentation and Rubrics of Evaluation.

4. **Activities on Group Discussion (GD):** Types of GD and GD as a part of a Selection Procedure- Dynamics of Group Discussion- Myths of GD - Intervention, Summarizing - Modulation of Voice, Body Language, Relevance, Fluency and Organization of Ideas– Do's and Don'ts-GD Strategies – Exercises for Practice.

5. **Interview Skills: Concept and Process** - Interview Preparation Techniques - Types of Interview Questions–Pre-interview Planning, Opening Strategies, Answering Strategies- Interview through Tele-conference & Video-conference - Mock Interviews.

Minimum Requirement:

The Advanced English Communication Skills (AECS) Laboratory shall have the following infrastructural facilities to accommodate at least 35 students in the lab:

- Spacious room with appropriate acoustics
- Round Tables with movable chairs
- Audio-visual aids
- LCD Projector
- Public Address system
- One PC with latest configuration for the teacher
- T.V, a digital stereo & Camcorder
- Headphones of High quality

Suggested Software:

1. TOEFL&GRE (BARRONS, USA, Cracking GRE by CLIFFS)
2. Oxford Advanced Learner's Dictionary, 10 Edition
3. Cambridge Advanced Learner's Dictionary

Text books:

1. Rizvi, M. Ashraf (2018). Effective Technical Communication. (2ed.). McGraw Hill Education (India) Pvt. Ltd.
2. Bailey, Stephen. (2018). Academic Writing: A Handbook for International Students. (5 Edition). Routledge.

References Books:

1. Raman, Meenakshi & Sharma, Sangeeta. (2022). Technical Communication, Principles and Practice. (4th Edition) Oxford University Press.
2. Anderson, Paul V (2007). Technical Communication. Cengage Learning Pvt.Ltd. New Delhi.

ANTENNA LABORATORY

III B.Tech. I Semester

Course Code: A225489

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Illustrate the different ways of measuring antenna parameters.
2. Differentiate the different Radiation pattern of the antennas
3. Study the characteristics of Antennas
4. Articulate the performance of Antennas based on the measurements
5. Formulate an antenna design using Antenna technologies.

LIST OF THE EXPERIMENTS: (Perform any 10 experiments)

1. Measurement of parameters of Half wave Dipole Antenna
2. Measurement of parameters of monopole Antenna
3. Measurement of parameters of large loop Antennas
4. Measurement of parameters of small loop Antennas
5. Measurement of parameters of Helical Antenna
6. Measurement of parameters of 2- element Antenna Array
7. Measurement of parameters of Yagi-Uda Antenna
8. Measurement of parameters of Folded Dipole Antenna
9. Performance analysis of N-element linear array
10. Design and simulation of Crossed Yagi Antenna
11. Study of performance of 2x2 Helical Antenna Array
12. Design and analysis Planar reflector antenna
13. Design and study of 3x3 dipole Antenna Array

ENVIRONMENTAL SCIENCE

III B.Tech. I Semester

Course Code: A225016

L	T	P	C
2	0	0	0

COURSE OUTCOMES

Students will be able to:

- Define and explain the structure and functions of ecosystem, value of biodiversity, threats and conservation of biodiversity.
- Explain the limitations of the resources and impacts of over utilization of all natural resources.
- Explain the sources and effects of environmental pollutions and list the available techniques to control the pollution.
- Explain the global environmental issues like climate change, ozone hole and can explain the scope of EIA, Environmental Management Plan, environmental audit and list the EIA methods.
- Mention the salient features of environmental acts and rules, define the sustainable goals along with measures required for the sustainability.

UNIT I:

Ecosystem: Definition, Scope and Importance of ecosystem, Structure and Functions of ecosystem: Food chains, Food Web and Ecological Pyramids, Flow of energy; Bio-magnification.

Biodiversity and Biotic Resources: Introduction, Definition, levels of Biodiversity, Value of biodiversity, Hot spots of biodiversity, Threats to biodiversity, conservation of biodiversity: In-Situ and Ex-situ conservation.

UNIT II:

Natural Resources: Classification of Resources, **Water resources:** use and over utilization of surface and ground water, Dams: benefits and problems, Rain water harvesting; **Energy resources:** growing energy needs, Renewable and Non Renewable Energy resources. **Land resources:** land degradation – Landslide and Soil Erosion; **Forest Resources** – Uses and Exploitation.

UNIT III:

Environmental Pollution And Control: Types of Pollution, Sources, Effects and Control measures of Air Pollution, Water Pollution, Soil Pollution and Noise Pollution.

UNIT IV:

Global Environmental Problems and Global Efforts: Greenhouse effect, Global Warming, climate change and their impacts on human environment; Ozone depletion and Ozone depleting substances (ODS); Acid Rains.

Environmental Impact Assessment (EIA): Scope of EIA and EIA methods, scope of Environmental audit and Environmental Management Plan.

UNIT V:

Environmental Policy, Legislation, Rules And Regulations: Salient features of Environmental Protection act, Air (Prevention and Control of pollution) Act- 1981, Water (Prevention and Control of pollution) Act-1974, Forest Conservation Act, Municipal solid waste, Hazardous waste, E-waste, Bio-medical waste, Radioactive waste Rules.

Towards Sustainable Future: Concept of Sustainable Development, Sustainable goals defined by UN, Threats to Sustainability, Environmental Education, Role of IT in Environment, Smart Cities, Concept of Green Building, Low Carbon Lifestyle, Life cycle assessment and Ecological Foot Print.

TEXT BOOKS:

1. Text Book of Environmental Studies by Anubha Kaushik (4th Edition), New age International Publishers.
2. Environmental studies by Erach Bharucha 2005, University Grants Commission, University Press.

REFERENCE BOOKS:

1. Textbook of Environmental Science and Technology by M. Anji Reddy, 2007.
2. Text Book of Environmental Studies by Anubha Kaushik (3rdEdition), New age International Publishers.
3. Environmental Science: Towards a Sustainable Future by Richard T. Wright, 2008 PHL Learning Private Ltd, New Delhi.

DIGITAL SIGNAL PROCESSING

III B.Tech. II Semester

Course Code: A226423

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Define the concepts of Fourier transforms, digital filters with their effect of errors.
2. Illustrate speed and memory requirements of Fourier transforms on signals.
3. Relate the effects of finite word length on systems.
4. Formulate frequency filtering, impulse response filters with its structure.
5. Ability to understand various applications of DSP such as multi rate signal processing, telecommunication

UNIT-I:

Introduction to DSP- applications-advantages

Discrete Fourier Transform:

DTFT, DFT-Complexity calculation- Properties of DFT- linear convolution- Circular convolution- Sectioned convolution- Relation between DTFT, DFS, DFT and Z-Transform.

UNIT-II:

Fast Fourier Transform:

Fast Fourier Transform (FFT), Radix-2 decimation-in-time and decimation-in-frequency FFT Algorithms, Inverse FFT- Convolution of sequences using FFT

UNIT-III:

IIR Digital Filters:

Analog filter approximations –Butterworth and Chebyshev- Design of IIR digital filters from analog filters- Impulse invariant technique – warping effect- bilinear transformation method - Spectral transformations, realization of IIR filters- direct, canonic, cascade and parallel forms.

UNIT-IV:

FIR Digital Filters:

Characteristics of FIR Digital filters - frequency response – Gibbs Phenomenon- Design of FIR filters - window techniques – Frequency Sampling - Comparison of IIR and FIR filters, realization of FIR filters- direct& cascade forms

UNIT-V:

Finite Word Length Effects:

Quantization- Quantization error- Types- Limit cycles- Overflow oscillations –Scaling

Multirate Signal Processing: Introduction - down sampling- Decimation – up sampling – Interpolation -Sampling Rate Conversion

TEXT BOOK:

1. John G. Proakis, Digital Signal Processing, Principles, Algorithms, and Applications, Pearson, 2007.
2. Tarun Kumar Rawat , Digital Signal Processing, Oxford Publications, 2015

REFERENCES BOOKS:

1. Andreas Antoniou, Digital Signal Processing, TATA McGraw Hill , 2006
2. Ashok Ambardar , Digital Signal Processing, Cenage Learning, 2007.

BUSINESS ECONOMICS AND FINANCIAL ANALYSIS

III B.Tech. II Semester

Course Code: A226018

L	T	P	C
3	0	0	3

Course Outcomes:

At the end of the course the students are expected to

- CO1:** Understand the nature and scope of business economics.
- CO2:** Analyze the Demand, Supply Functions and to forecast the demand.
- CO3:** Understand the concept of production and its relationship with business operations.
- CO4:** Analyze the Financial Statements of a Company.
- CO5:** Compare and interpret the Financial Statements of a Company using ratios.

UNIT-I Introduction to Business and Economics: Types of Business Entities, Theory of Firm, Capital and Sources of Capital for a Company, Economics - Significance of Economics, Micro and Macro Economic Concepts, Concepts and Importance of National Income, Inflation, Types of Inflation, Business Cycle, Features and Phases of Business Cycle. Nature and Scope of Business Economics, Role of Business Economist, Multidisciplinary nature of Business Economics.

UNIT-II Demand and Supply Analysis: Elasticity of Demand: Elasticity, Types of Elasticity, Law of Demand, Measurement and Significance of Elasticity of Demand, Factors affecting Elasticity of Demand, Elasticity of Demand in decision making, Demand Forecasting: Characteristics of Good Demand Forecasting, Steps in Demand Forecasting, Methods of Demand Forecasting. Supply Analysis: Determinants of Supply, Supply Function & Law of Supply.

UNIT-III Production, Cost, Market Structures & Pricing: Production Analysis: Factors of Production, Production Function, Production Function with one variable input, two variable inputs, Returns to Scale. Cost analysis: Types of Costs. Market Structures: Nature of Competition, Features of Perfect competition, Monopoly, Oligopoly, and Monopolistic Competition. Pricing: Types of Pricing, Product Life Cycle based Pricing, Break Even Analysis, and Cost Volume Profit Analysis.

UNIT-IV Financial Accounting: Accounting concepts and Conventions, Accounting Equation, Double-Entry system of Accounting, Rules for maintaining Books of Accounts, Journal, Posting to Ledger, Preparation of Trial Balance, and Preparation of Final Accounts.

UNIT-V Financial Analysis through Ratios: Concept of Ratio Analysis, Liquidity Ratios, Turnover Ratios, Profitability Ratios, Proprietary Ratios, Solvency, Leverage Ratios (simple problems).

Text Books:

1. D. D. Chaturvedi, S. L. Gupta, Business Economics - Theory and Applications, International Book House Pvt. Ltd. 2013.
2. Dhanesh K Khatri, Financial Accounting, Tata McGraw Hill, 2011.
3. Geethika Ghosh, Piyali Gosh, Purba Roy Choudhury, Managerial Economics, 2e, Tata McGraw Hill Education Pvt. Ltd. 2012.

Reference Books:

1. Paresh Shah, Financial Accounting for Management 2e, Oxford Press, 2015.
2. S. N. Maheshwari, Sunil K Maheshwari, Sharad K Maheshwari, Financial Accounting, 5e, Vikas Publications, 2013.

CMOS VLSI DESIGN

III B.Tech. II Semester

Course Code: A226424

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Enumerate different steps involved in Integrated Circuits technology for MOS transistor and explain the primary and secondary effects of MOSFET and BICMOS.
2. Summarize the fabrication process involved in VLSI circuits
3. Outline the design process involved in VLSI design flow for design of MOS transistors.
4. Understand and apply the concepts of memories in design.
5. Design digital circuits using Verilog HDL.

UNIT-I:

Introduction:

Introduction to IC Technology, Fabrication process of NMOS, PMOS CMOS and BiCMOS

Basic Electrical Properties:

I_{ds} - V_{ds} relationships, MOS transistor Threshold Voltage- V_t , Transconductance- g_m , g_{ds} , figure of merit- ω_0 , Pass transistor, NMOS Inverter, Various pull ups, Ratioed Circuits CMOS Inverter analysis and design, Bi-CMOS Inverters.

UNIT-II:

VLSI Circuit Design Processes:

VLSI Design flow, MOS Layers - Stick Diagrams - Lambda(λ)-based Design rules - wires – Contacts – Transistors- Layout Diagrams – NMOS – PMOS - CMOS Inverters – Gates - Scaling of MOS circuits.

UNIT-III:

Gate Level Design:

Logic Gates – Pass transistors, Transmission gate- Switch logic - Alternate gate circuits, Latches- Time delays - Driving large capacitive loads - Wiring capacitance, Fan – in, Fan – out, Choice of layers.

Programmable Logic Devices:

PROM – PLA - PAL-Design Approach - CPLDs – FPGA -Parameters influencing low power design.

UNIT-IV:

Introduction to Verilog HDL:

Overview of Digital Design with Verilog HDL, typical HDL-flow, Concurrency, Simulation and Synthesis, Functional verification;

Gate Level Modeling:

Introduction, Modeling using basic Verilog gate primitives, description of AND, OR, NOT type gates, Design of Flip – Flops with Gate Primitives, Delays

UNIT-V:

Dataflow Modeling:

Continuous assignments, delay specification, expressions, operators, operands, operator types;

Behavioral Modeling:

Structured procedures, initial and always, blocking and non-blocking statements, The Case Statement, for Loop, While Loop, Design of Flip flop, Shift register

TEXT BOOKS:

1. Kamran Eshraghian, Douglas A. Pucknell, SholehEshraghian, Essentials of VLSI Circuits and Systems, Prentice, 2005.
2. T.R. Padmanabhan, Digital Design Through Verilog HDL, Wiley, 2003.

REFERENCE BOOKS:

1. John P. Uyemura, CMOS logic circuit Design, Springer, 1999.
2. Lal Kishore K, Prabhakar V S V, VLSI Design, I.K International, 2010.

DIGITAL IMAGE PROCESSING
(Professional Elective-II)

III B.Tech. II Semester

Course Code: A226425

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. State the Digital Image Fundamentals and operation associated with various stages of image processing.
2. Illustrate the mathematics involved in various stages of image processing.
3. Demonstrate the operations various stages of image processing.
4. Contrast the different types of operation and its impact on images.
5. Understand the anatomy of image compression in Image Transmission

UNIT-I:

Fundamentals of Image Processing:

Elements of Digital Image Processing Systems – Image sensing and Acquisition- Elements of Visual Perception – structure of human eye – light- luminance- brightness and contrast- image formation- Basic steps of image processing- Sampling -Quantization and Digital Image representation - Basic relationships between pixels – neighborhood, adjacency, connectivity, distance measures..

UNIT-II:

Image Enhancement in Spatial & frequency domain:

Image Enhancement in Spatial domain:

Introduction-Point Processing-Histogram processing- Arithmetic and logical operations- Fundamentals of Spatial filtering-masking-Spatial filters for Smoothing - Spatial filters for Sharpening.

Image Enhancement in Frequency domain:

Need for transform-Basics of filtering in frequency domain-Image smoothing in frequency domain-Image sharpening in frequency domain

UNIT-III:

Image Restoration:

Introduction- Degradation model –Noise models-Spatial domain filtering for restoration- Mean Filters – Order Statistics filters – Adaptive filters –frequency domain filtering for noise removal - Band reject Filters – Band pass Filters – Notch Filters –Degradation function estimation– Inverse filtering – Wiener filter.

UNIT-IV:

Image Segmentation and Morphological processing:

Image Segmentation:

Segmentation concepts - Point - Line - Edge Detection-Thresholding based segmentation- Local-Global and Adaptive Thresholding- Region based segmentation-Region growing-Region splitting and merging.

Morphological processing:

Introduction- structuring element – erosion – dilation – Opening - closing.

UNIT-V:

Image Compression:

Introduction-Redundancy in images-Fidelity Criteria-Image compression model-Lossless compression-Huffman coding -Lossless Predictive coding- Lossy compression– lossy predictive coding- Transform coding –Image compression standards- JPEG and JPEG 2000.

TEXT BOOKS:

1. Rafael C. Gonzales, Richard E. Woods, Digital Image Processing, Pearson, 2007.
2. Anil K. Jain, Fundamentals of Digital Image Processing, Pearson, 2005.

REFERENCE BOOKS:

1. Rafael C. Gonzalez, Richard E.. Woods, Richard Eugene Woods, Steven L. Eddins, Digital Image processing using MATLAB, Tata McGraw Hill, 2010.
2. William K Pratt, Digital Image Processing, John Wiley & Sons, 2002.

MOBILE COMMUNICATIONS AND NETWORKS
(Professional Elective-II)

III B.Tech. II Semester

Course Code: A226426

L	T	P	C
3	0	0	3

Course Outcomes: Upon completing this course, the student will be able to:

1. Known the evolution of cellular and mobile communication system.
2. Explore the Co-Channel and Non-Co-Channel interferences.
3. Known how to overcome the different fading effects?
4. Familiar with cell coverage for signal and traffic, diversity, techniques, frequency management, Channel assignment and types of handoff.
5. Demonstrate the difference between cellular and Adhoc Networks and design goals of MAC Layer protocol.

UNIT - I

Introduction to Cellular Mobile Radio Systems: Limitations of Conventional Mobile Telephone Systems. Basic Cellular Mobile System, First, Second, Third and Fourth Generation Cellular Wireless Systems. Uniqueness of Mobile Radio Environment-Fading-Tie Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time.

Fundamentals of Cellular Radio System Design: Concept of Frequency Reuse, Co-Channel Interference, Co-Channel Interference Reduction Factor, Desired C/I from a Normal Case in a Omni Directional Antenna System, System Capacity Improving Coverage and Capacity in Cellular Systems-Cell Splitting, Sectoring, Microcell Zone Concept.

UNIT – II

Co-Channel Interference: Measurement of Real Time Co-Channel Interference, Design of Antenna System, Antenna Parameters and their effects, diversity techniques-space diversity, polarization diversity, frequency diversity, time diversity.

Non Co-Channel Interference: Adjacent Channel Interference, Near end far end interference, cross talk, effects on coverage and interference by power decrease, antenna height decrease, effects of cell site components.

UNIT – III

Cell Coverage for Signal and Traffic: Signal Reflections in flat and Hilly Terrain, effects of Human Made Structures, phase difference between direct and reflected paths, constant standard deviation, straight line path loss slope, general formula for mobile propagation over water and flat open area, near and long-distance propagation, path loss from a point to point prediction model in different conditions, merits of lee model.

Frequency Management and Channel Assignment: Numbering and Grouping, Setup Access and Paging Channels, Channel Assignments to Cell Sites and Mobile Units.

UNIT - IV

Handoffs and Dropped Calls: Handoff Initiation, types of Handoff, Delaying Handoff, advantages of Handoff, Power Difference Handoff, Forced Handoff, Mobile Assisted and Soft Handoff, Intersystem handoff, Introduction to Dropped Call Rates and their Evaluation.

UNIT - V

Ad Hoc Wireless Networks: Introduction, Cellular and Ad Hoc wireless Networks, Applications and Ad Hoc Wireless Networks, Issues in Ad Hoc Wireless Networks, Ad Hoc Wireless Internet, MAC Protocols for Ad Hoc Wireless, Introduction, issues in designing AMAC Protocol for Ad Hoc wireless Networks, Design Goals of AMAC protocol for Ad Hoc Wireless Networks, Classification of MAC Protocols.

TEXT BOOKS:

1. W.C.Y. Lee - Mobile Cellular Telecommunications, 2nd edition, Mc Graw Hill, 1989.
2. Theodore. S. Rapport - Wireless Communications, 2nd editon, Pearson Education, 2002.

REFERENCE BOOKS:

1. C. Siva ram Murthy and B.S. Manoj - Ad Hoc Wireless Networks: Architectures and Protocols, PHI, 2004.
2. Simon Haykin, Michael Moher - Modern Wireless Communications, Pearson Education, 2005.
3. Vijay Garg - Wireless Communications and Networking, Elsevier Publications, 2007.
4. Andrea Goldsmith -Wireless Communications-, Cambridge University Press, 2005.



Department of Electronics and Communication Engineering

(Accredited by NBA)

EMBEDDED SYSTEM DESIGN

(Professional Elective-II)

III B.Tech. II Semester

Course Code: A226427

L	T	P	C
3	0	0	3

Course Outcomes:

At the end of the course the student should be able to

1. Expected to understand the selection procedure of Processors in the embedded domain.
2. Design Procedure for Embedded Firmware.
3. Expected to visualize the role of Real time Operating Systems in Embedded Systems
4. Expected to evaluate the Correlation between task synchronization and latency issues
5. To Enumerate the need for Task Communications in a Multiprocessor Environment

UNIT I:

Introduction to Embedded Systems:

Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

UNIT II:

Typical Embedded System:

Core of the Embedded System: General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

UNIT III:

Embedded Firmware:

Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

UNIT IV:

RTOS Based Embedded System Design:

Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

UNIT V:

Task Communication:

Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication/Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

TEXT BOOKS:

1. Shibu K V, Introduction to Embedded Systems, McGraw-Hill Education, 2009.
2. Lyla B. Das, Embedded Systems an Integrated Approach, Pearson Education, 2013.

REFERENCE BOOKS:

1. Raj Kamal , Embedded systems architecture, programming and design, McGraw-Hill Education, 2003
2. Frank Vahid, Tony Givargis , Embedded System Design A Unified Hardware/Software Introduction, John Wiley, 2003

FUNDAMENTALS OF INTERNET OF THINGS
(Open Elective-II)

B.Tech. ECE III Year II Semester

Course Code: A226428

L	T	P	C
3	0	0	3

Course Outcomes: Upon completing this course, the students will be able to

- Know basic protocols in sensor networks.
- Program and configure Arduino boards for various designs.
- Python programming and interfacing for Raspberry Pi.
- Design IoT applications in different domains.

UNIT – I Introduction to Internet of Things: Characteristics of IoT, Physical design of IoT, Functional blocks of IoT, Sensing, Actuation, Basics of Networking, Communication Protocols, Sensor Networks.

UNIT - II Machine-to-Machine Communications: Difference between IoT and M2M, Interoperability in IoT, Introduction to Arduino Programming, Integration of Sensors and Actuators with Arduino.

UNIT – III Introduction to Python programming: Introduction to Raspberry Pi, Interfacing Raspberry Pi with basic peripherals, Implementation of IoT with Raspberry Pi.

UNIT - IV Implementation of IoT with Raspberry Pi: Introduction to Software defined Network (SDN), SDN for IoT, Data Handling and Analytics.

UNIT - V Cloud Computing: Sensor-Cloud, Smart Cities and Smart Homes, Connected Vehicles, Smart Grid, Industrial IoT.

Case Study: Agriculture, Healthcare, Activity Monitoring

TEXT BOOKS:

1. "The Internet of Things: Enabling Technologies, Platforms, and Use Cases", by Pethuru Raj and Anupama C. Raman (CRC Press)
2. "Make sensors": Terokarvinen, kemo, karvinen and villeyvaltokari, 1st edition, maker media, 2014.
3. "Internet of Things: A Hands-on Approach", by Arshdeep Bahga and Vijay Madisetti

REFERENCE BOOKS:

1. Vijay Madisetti, Arshdeep Bahga, "Internet of Things: A Hands-On Approach"
2. WalteneagusDargie,ChristianPoellabauer, "Fundamentals of Wireless Sensor Networks: Theory and Practice"

ELECTRONICS FOR HEALTH CARE
(Open Elective–II)

III B.Tech. II Semester

Course

Code: A226429

L	T	P	C
3	0	0	3

Course Outcomes: Upon completion of this course, the students will be able to

1. Know about health care data and its conversion to information and to knowledge.
2. Acquire knowledge on (Electronic Health Records) EHRs and their Implementation.
3. Understand the working of electronic devices used for the patient monitoring.
4. Know the concepts of Telemedicine and therapeutic devices used inside the human body
5. Understand the importance of Therapeutic devices

UNIT I:

Health care data, Information and Knowledge: Definitions and Concepts, Converting Data to Information to Knowledge, Clinical Data Warehouses, What makes Health Informatics Difficult, Why Health IT fails Sometimes, Terminology of Analytics, Challenges to Data Analytics, Research and application of analytics, Role of Informatics in analytics.

UNIT II:

Electronic Health Records: Introduction, Need for Electronic Health Records, Institute of Medicine's Vision for EHRs, Electronic Health Record Key Component, Electronic Prescribing, Electronic Health Record Adoption, Electronic Health Record Adoption and Meaningful use Challenges, Electronic Health Record Examples, Logical Steps to Selecting and Implementing an EHR

UNIT III:

Patient Monitoring Systems: System Concepts, Cardiac Monitor, Bedside Patient Monitoring Systems, Central Monitors, Measurement of Heart Rate, Measurement of Pulse Rate, Blood Pressure Measurement, Measurement of Temperature, Measurement of Respiration Rate, Catheterization Laboratory Instrumentation.

UNITIV:

Biomedical Telemetry and Telemedicine: Wireless Telemetry, Single Channel Telemetry Systems, Multi-channel Wireless Telemetry Systems, Multi-patient Telemetry, Implantable Telemetry Systems, Transmission of Analog Physiological Signals, Over Telephone, Telemedicine.

UNIT V:

Therapeutic devices: Need for Cardiac Pacemaker, Implantable Pacemakers, DC Defibrillator, and Electronics in the Anaesthetic Machine.

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TEXT BOOKS:

1. Robert E. Hoyt MD FACP “Health Informatics” sixth edition 2007.
2. R. S. Kandpur “Biomedical Instrumentation Technology and Applications” second edition Tata McGraw-Hill.

REFERENCES:

1. Edward H. Shortliffe, James J. Cimino “Biomedical Informatics, Computer applications in Health care and Biomedicine” third edition Springer.
2. G.V.R.K. Acharyulu, Bhimaraya Metri, L. Kalyan Viswanath REDDY “Health care and Hospital Management Contemporary Issues and Strategies”

TELECOMMUNICATIONS FOR SOCIETY
(Open Elective–II)

III B.Tech. II Semester

Course Code: A226430

L	T	P	C
3	0	0	3

Course Outcomes: Upon completion of this course, the students will be able to

1. Understand the concepts of simplex, half duplex, and full duplex of one-way and two-way circuits.
2. Get knowledge on subscriber loop design and VF repeaters of voice telephony.
3. Get brief overview of video transmission and its broadcasting standards in television transmission.
4. Know different of modes of television transmission.
5. Understand the concept of CATV.

UNIT I:

INTRODUCTORY TOPICS IN TELECOMMUNICATIONS:

End-Users, Nodes, and Connectivities, Telephone Numbering and Routing, Use Of Tandem Switches in a Local Area Connectivity, Introduction to the Busy Hour and Grade Of Service, Simplex, Half-Duplex, and Full Duplex, One-Way and Two-Way Circuits, Network Topologies, Variations in Traffic Flow, Quality Of Service,

UNIT II:

QUALITY OF SERVICE AND TELECOMMUNICATION IMPAIRMENTS:

Objective, Quality of Service: Voice, Data, and Image, Signal-to-Noise Ratio, Voice Transmission, Data Circuits, Video (Television), The Three Basic Impairments and How They Affect the End-User, Amplitude Distortion, Phase Distortion, Noise Level, Typical Levels, Echo and Singing.

UNIT III:

TRANSMISSION ASPECTS OF VOICE TELEPHONY:

Definition of the Voice Channel, Operation of the Telephone Subset, Subscriber Loop Design, Design of Local Area Wire-Pair Trunks (Junctions), VF Repeater (Amplifiers).

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UNIT IV:

TELEVISION TRANSMISSION:

Background and Objectives, An Appreciation of Video Transmission, Critical Video Parameters, Video Transmission Standards (Criteria for Broadcasters), Methods of Program Channel Transmission, The Transmission of Video Over LOS Microwave, TV Transmission by Satellite Relay, Digital Television, Conference Television, Brief Overview of Frame Transport for Video Conferencing.

UNIT V:

COMMUNITY ANTENNA TELEVISION (CABLE TELEVISION):

Objective and Scope, The Evolution of CATV, System Impairments and Performance Measures, Hybrid Fiber-Coax (HFC) Systems, Digital Transmission of CATV Signals, Two-Way CATV Systems, Two-Way Voice and Data over CATV Systems Based on the DOCSIS 2.0 Specification,.

TEXT BOOKS:

1. Roger L. Freeman “Fundamentals of Telecommunications” 2nd Edition, John Wiley & Sons Publications 2005.
2. Annabel Z. Dodd “The Essential Guide to Telecommunications” 5th Edition, Prentice Hall 2012.

REFERENCES:

1. JYRKI T. J. PENTTINEN “THE TELECOMMUNICATIONS HANDBOOK” John Wiley & Sons Publications 2015.
2. Prof. Dr. Muhammad EL-SABA “Telecommunications systems and data networks” 3rd Edition 2015.

VLSI LABORATORY

III B.Tech. II Semester

Course Code: A226490

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

At the end of the course the student should be able

1. To learn Hardware Descriptive Language (Verilog).
2. To learn the fundamental principles of Digital System Design using HDL and FPGA.
3. To learn the fundamental principles of VLSI circuit design in digital domain
4. To analyze various combinational and sequential circuits using Verilog HDL
5. To provide hands on design experience with EDA platforms.

Perform any 10 Experiments:

1. Verification of Logic Gates
2. Verification of Demorgan's Law
3. Design of 8 to 1 multiplexer
4. Design of 1 to 8 Demultiplexer
5. Design of 2 to 4 Encoder
6. Design of 4-bit comparator
7. Design of 4 bit binary to gray converter
8. Design of full adder using 3 modeling styles
9. Design of flip flops SR, D, JK, and T
10. Design Ripple Counter
11. Design Modulo Counter
12. Design Shift Register
13. Design Inverter using PMOS / NMOS
14. Design of full adder using decoder and multiplexer
15. Design System using finite state Machine

EMBEDDED SYSTEM DESIGN LABORATORY

III B.Tech. II Semester

Course Code: A226491

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Code the ARM cortex M0+ processor instruction set.
2. Articulate the concept of interfacing I/O devices with FRDM kit.
3. Simulate I/O Devices with Arduino using Tinkercad
4. Simulate sensors with Arduino using Tinkercad
5. Formulate a system design using Embedded technologies

Perform any 10 Experiments:

Note: perform below experiments using FRDM Boards/Tinkercad Simulation.

1. Study of FRDM Development Board
2. Blinking of LED using FRDM Kit
3. Breath out 2 LEDs using FRDM Kit
4. Color Circle using FRDM Kit
5. Interface to Accelerometer sensor using FRDM kit
6. LED intensity control using touch sensor using FRDM kit
7. Simulate Potentiometer with Arduino using Tinkercad.
8. Simulate servomotor with Arduino using Tinkercad.
9. Simulate Ultrasonic sensor with Arduino using Tinkercad.
10. Simulate LDR with Arduino using Tinkercad.
11. Simulate 2-wire LCD with Arduino using Tinkercad.
12. Simulate SPI LCD with Arduino using Tinkercad.

DIGITAL SIGNAL PROCESSING LABORATORY

III B.Tech. II semester

Course Code: A226492

L	T	P	C
0	0	2	1

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Formulate programs for performing time & frequency operation on signals and systems.
2. Design and implement impulse response filters and Multirate system for a given sequence
3. Analyze and Observe Magnitude and phase characteristics (Frequency response Characteristics) of digital IIR-Butterworth, Chebyshev filters
4. Analyze and Observe Magnitude and phase characteristics (Frequency response Characteristics) of digital FIR filters using window techniques
5. Develop various DSP Algorithms using MATLAB Software package

Note: Minimum 10 Experiments have to be conducted

1. Generation of Sinusoidal Waveform / Signal based on Recursive Difference Equations.
2. To find DFT / IDFT of given DT Signal.
3. To find Frequency Response of a System given in Transfer Function/ Differential equation form.
4. Implementation of FFT of given Sequence.
5. Determination of Power Spectrum of a given Signal(s).
6. Implementation of LP FIR Filter for a given Sequence/Signal.
7. Implementation of HP IIR Filter for a given Sequence/Signal.
8. Generation of Narrow Band Signal through Filtering.
9. Generation of DTMF Signals.
10. Implementation of Decimation Process.
11. Implementation of Interpolation Process.
12. Implementation of I/D Sampling Rate Converters.
13. Step and Ramp Response of First order and Second Order Systems.

GENDER SENSITIZATION

III B.Tech. II Semester

Course Code: A226019

L	T	P	C
2	0	0	0

Course Outcomes

At the end of the course the student should be able to

1. Students will have developed a better understanding of important issues related to gender in contemporary India.
2. Students will attain a finer grasp of how gender discrimination works in our society and how to counter it.
3. Students will acquire insight into the gendered division of labour and its relation to politics and economics.
4. Men and women, students and professionals will be better equipped to work and live together as equals.
5. Students will develop a sense of appreciation of women in all walks of life through social media and literature.

Unit-I: UNDERSTANDING GENDER

Introduction: Definition of Gender-Basic Gender Concepts and Terminology-Exploring Attitudes towards Gender-Construction of Gender-Socialization: Making Women, Making Men-Preparing for Womanhood. Growing up Male. First lessons in Caste.

Unit – II: GENDER ROLES AND RELATIONS

Two or Many? -Struggles with Discrimination-Gender Roles and Relations-Types of Gender Roles- Gender Roles and Relationships Matrix-Missing Women-Sex Selection and Its Consequences- Declining Sex Ratio. Demographic Consequences-Gender Spectrum: Beyond the Binary

Unit – III: GENDER AND LABOUR

Division and Valuation of Labour-Housework: The Invisible Labor- “My Mother doesn’t Work.” “Share the Load.”-Work: Its Politics and Economics -Fact and Fiction. Unrecognized and unaccounted work.-Gender Development Issues-Gender, Governance and Sustainable Development-Gender and Human Rights-Gender and Mainstreaming

Unit – IV: GENDER - BASED VIOLENCE

The Concept of Violence- Types of Gender-based Violence-Gender-based Violence from a Human Rights Perspective-Sexual Harassment: Say No!-Sexual Harassment, not Eve-teasing- Coping with Everyday Harassment- Further Reading: “Chupulu”.

Domestic Violence: Speaking Out Is Home a Safe Place? -When Women Unite [Film]. Rebuilding Lives. Thinking about Sexual Violence Blaming the Victim-“I Fought for my Life....”

Unit – V: GENDER AND CULTURE

Gender and Film-Gender and Electronic Media-Gender and Advertisement-Gender and Popular Literature- Gender Development Issues-Gender Issues- Gender Sensitive Language-Gender and Popular Literature - Just Relationships: Being Together as Equals Mary Kom and Onler. Love and Acid just do not Mix. Love Letters. Mothers and Fathers. Rosa Parks-The Brave Heart.

TEXTBOOK

“Towards a World of Equals: A Bilingual Textbook on Gender” written by A. Suneetha, Uma Bhrugubanda, Duggirala Vasanta, Rama Melkote, Vasudha Nagaraj, Asma Rasheed, Gogu Shyamala, Deepa Sreenivas and Susie Tharu and published by **Telugu Akademi, Hyderabad, Telangana State** in the year **2015**.

REFERENCE BOOKS:

1. Menon, Nivedita. Seeing like a Feminist. New Delhi: Zubaan-Penguin Books, 2012
2. Abdulali Sohaila. *“I Fought For My Life...and Won.”* Available online at: <http://www.thealternative.in/lifestyle/i-fought-for-my-lifeand-won-sohaila-abdulal/>

MICROWAVE AND OPTICAL COMMUNICATIONS

IV B.Tech. I Semester

Course Code: A227431

L	T	P	C
3	1	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Understands the basics of waveguides in the EM wave transmission and waveguide components for microwave frequencies.
2. Classify different types of Microwave Tubes and their functionality
3. Understand a Microwave test bench setup for parameters measurements and study the Microwave Solid State Devices
4. Analyze the concepts of LED, LASER, PIN, and APD's used in optical fibers.
5. Analyze fiber alignment, connectors and splicing methods of optical fibers Evaluate power coupling from transmitter to fibers and design of fiber optic link

UNIT I: WAVEGUIDES AND WAVEGUIDE COMPONENTS:

Microwave Spectrum and Band designation, Rectangular Waveguides - TE/TM mode analysis, Cut-off Frequencies, Dominant and Degenerate Modes, Impossibility of TEM Mode.

Waveguide Components: Attenuators-Resistive Card, Rotary Vane types, scattering matrix parameters: Properties, S- parameters of E-Plane, H-Plane, Magic TEE Junctions and Directional coupler, Ferrite Components - Faraday Rotation Law, Isolator, Circulator.

UNIT II: MICROWAVE TUBES:

Limitations and Losses of conventional Tubes at Microwave Frequencies, Re-entrant Cavities,

O-TYPE TUBES : 2 Cavity Klystrons – Velocity Modulation and Bunching process, Reflex Klystrons – Structure, principle of working.

HELIX TWTS: Significance, Types and Characteristics of Slow Wave Structures; Structure and operation of TWT.

M-TYPE TUBES

Introduction, Magnetrons – 8-Cavity Cylindrical Travelling Wave Magnetron.

UNIT III: MICROWAVE SOLID STATE DEVICES& MEASUREMENTS

MICROWAVE MEASUREMENTS: Description Of Microwave Bench- Different Blocks, Microwave Power Measurement-. Measurement of Attenuation, VSWR & Impedance

MICROWAVE SOLID STATE DEVICES -TEDs – Introduction, Gunn Diodes –RWH Theory, Characteristics, Modes of Operation.

UNIT IV: BASICS OF OPTICAL FIBER COMMUNICATION

Block diagram of Optical Communication, Total Internal Reflection, Numerical Aperture, Cut off wavelength, Optical Fiber types- Step index fiber, Graded index fiber, Losses in Optical Fiber cables, Optical Sources- LED, LASERS, Optical Detectors- Avalanche photodiode, PIN photodiode. (Qualitative treatment).

UNIT V: OPTICAL FIBER CONNECTORS & SYSTEM DESIGN

OPTICAL FIBER CONNECTORS-Connector types, Single mode fiber connectors, Connector Return loss, Fiber Splicing- Splicing techniques, Splicing single mode fibers, Multimode fiber Joints, single mode fiber joints.

OPTICAL SYSTEM DESIGN - Point to point links –Link power budget, Line coding in Optical links, WDM, Eye pattern.

TEXT BOOK:

1. Samuel Y. Liao, Microwave Devices and Circuits, Pearson, 2003.
2. Kulkarni M , Microwave and Radar Engineering , UMESH Publications, 2003
3. Optical Fiber Communication Principle & Practice, John M Senior, Pearson Publications, 3rd edition.

REFERENCES BOOKS:

1. David M. Pozar, Microwave Engineering, John Wiley, 2011.
2. Raghuvanshi G S, Microwave Engineering, Cengage Learning, 2012.
3. Optical Fiber Communication- Gerd Keiser, TMH, 4th Ed., 2008.

ELECTRONIC MEASUREMENTS AND INSTRUMENTATION

IV B.Tech. I Semester

Course Code: A227432

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Acquire knowledge in Characteristics of Instruments, measurement of electrical quantities
2. Measure and analyze the signals using CRO.
3. Study the performance electronic systems using signal generators and wave analyzers
4. Acquire knowledge on Transducers and understanding their applications in real time world
5. Understanding the storing data of non-electrical and electrical quantities

UNIT-I: MEASUREMENTS AND MEASURING SYSTEMS:

Functional Diagram of Instrumentation System, Static characteristics: Accuracy - Precision – Resolution – Sensitivity – measurement Errors, Dynamic Characteristics: Speed of response – fidelity – Lag - Dynamic error - Statistical Analysis, Basic meter movement, DC Ammeters: Multi-range, DC voltmeters: Multi-range, AC voltmeters: Rectifier type - Thermocouple Type, Ohmmeters: Series type and Shunt type, Multimeter

UNIT-II: OSCILLOSCOPES:

Oscilloscope block diagram, Cathode Ray Tube, Vertical Deflection System, Delay Line, Horizontal Deflection System, Dual Beam CRO, Dual Trace CRO, Measurements with CRO: Amplitude – Time period - Frequency – Phase, Lissajous patterns, Sampling Oscilloscope, Analog Storage Oscilloscope, Digital Storage Oscilloscope.

UNIT-III: SIGNAL GENERATORS AND SIGNAL ANALYZERS:

Signal Generators:

AF Sine & Square wave generator, Function generators, RF signal generators, Sweep frequency generator.

Signal analyzers:

Wave analyzers: Frequency Selective Wave Analyzer - Heterodyne Wave Analyzer - Application of Wave Analyzers, Total Harmonic Distortion, Spectrum Analyzer.

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UNIT-IV: TRANSDUCERS:

Classification of Transducers, Strain gauges, Capacitive Transducers, Linear Variable Differential Transducer (LVDT), Piezoelectric Transducer, Thermocouple, Thermistor, Measurement of Displacement, Velocity, Acceleration, Vibration, Force, Pressure, Fluid Flow, Liquid Level and Temperature.

UNIT-V: BRIDGES AND RECORDERS:

Bridges:

Wheat Stone Bridge, Kelvin Bridge, Maxwell Bride, Schering Bridge and Wien Bridge

Recorders:

Magnetic disk and tape Recorders, LED, LCD, Dot matrix display, Strip Chart Recorder, X-Y Recorder

TEXT BOOKS:

1. Albert D. Helfrick, Cooper William D, Modern Electronic Instrumentation and Measurement Techniques, Prentice-Hall of India, 1997.
2. Kalsi H S, Electronic instrumentation, Tata Mcgraw Hill, 2015.

REFERENCE BOOKS:

1. David A. Bell, Electronic Instrumentation & Measurements, Prentice-Hall, 2003.
2. Sawhney A K, PuneetSawhney , A Course in Electrical and Electronic Measurements and Instrumentation, Dhanpat Rai & Company, 2016

Department of Electronics and Communication Engineering
(Accredited by NBA)
RADAR SYSTEMS
(Professional Elective-III)

IV B.Tech. I Semester

Course Code: A227433

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Understand the concepts of radar fundamentals, noise analysis and evaluation of radar.
2. Differentiate various types of radar transmitters and receivers.
3. Relate the different types of radar transmitter and receiver.
4. Categorize the type of radar system and noise analysis based on applications.
5. Correlate the different methods of Radar Reception and Receivers

UNIT – I:

Basics of Radar:

Introduction- Maximum Unambiguous Range- Radar Waveforms-- Radar Block Diagram and Operation- Radar Frequencies and Applications. Prediction of Range Performance-Minimum Detectable Signal- Receiver Noise.

Radar Equation:

Simple and Modified form of Radar Range Equation with Illustrative Problems - SNR- Envelop Detector-False Alarm Time and Probability- Integration of Radar Pulses- Radar Cross Section of Targets (simple targets - sphere- cone-sphere)- Transmitter Power- PRF and Range Ambiguities- System Losses (qualitative treatment)- Illustrative Problems.

UNIT – II:

CW and Frequency Modulated Radar:

Doppler Effect- CW Radar – Block Diagram- Isolation between Transmitter and Receiver- Non-zero IF Receiver- Receiver Bandwidth Requirements- Applications of CW radar- Illustrative Problems.

FM-CW Radar:

Range and Doppler Measurement- Block Diagram and Characteristics- FM-CW altimeter- Measurement Errors- Multiple Frequency CW Radar

UNIT – III:

MTI and Pulse Doppler Radar:

Introduction- Principle- MTI Radar with Power Amplifier Transmitter and Power Oscillator Transmitter- Delay Line Cancellers – Filter Characteristics- Blind Speeds- Double Cancellation- Staggered PRFs- Range Gated Doppler Filters- MTI Radar Parameters- Limitations to MTI Performance- MTI versus Pulse Doppler Radar.

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UNIT – IV:

Tracking Radar:

Tracking With Radar- Sequential Lobing- Conical scan-Mono pulse Tracking Radar-Amplitude Comparison Mono pulse (One-And Two-Coordinates)-Phase Comparison Monopulse- Tracking In Range- Acquisition and Scanning Patterns- Comparison Of Trackers.

UNIT – V:

Detection of Radar Signals in Noise:

Introduction- Matched Filter Receiver-Response Characteristics and Derivation- Correlation Function and Cross-Correlation Receiver- Efficiency of Non-Matched Filters- Matched Filter with Non-White Noise.

Radar Receivers:

Noise Figure and Noise Temperature- Display-Types- Duplexers-Branch types And Balanced type- Circulators as Duplexers. Introduction to Phased Array Antennas-Basic concepts- Radiation Pattern- Beam Steering and Beam Width changes- Advantages and Limitations- Applications.

TEXT BOOKS:

1. Merrill I. Skolnik , Introduction to radar systems, Tata McGraw Hill special Indian edition, 2007.
2. Kulkarni M , Microwave and Radar Engineering , UMESH Publications, 2003

REFERENCE BOOKS:

1. Byron Edde, Radar: Principles, Technology, Applications, Pearson, 2004.
2. Peyton Z. Peebles, Radar Principles, Wiley, 1998.

CMOS ANALOG IC DESIGN
(Professional Elective-III)

IV B.Tech. I Semester

Course Code: A227434

L	T	P	C
3	0	0	3

Course Outcomes

After studying the course, each student is expected to be able to

1. Design basic building blocks of CMOS Analog ICs.
2. Carryout the design of single and two stage operational amplifiers and voltage references.
3. Determine the device dimensions of each MOSFETs involved.
4. Design various amplifiers like differential, current and operational amplifiers.
5. Understand the Characterization of Comparator.

UNIT - I

MOS Devices and Modeling

The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.

UNIT - II

Analog CMOS Sub-Circuits

MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascode current Mirror and Wilson Current Mirror, Current and Voltage References, Bandgap Reference.

UNIT- III

CMOS Amplifiers

Inverters, Differential Amplifiers, Cascode Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.

UNIT-IV

CMOS Operational Amplifiers

Design of CMOS Op-Amps, Compensation of Op-Amps, Design of Two-Stage Op-Amps, Power-Supply, Rejection Ratio of Two-Stage Op-Amps, Cascode Op-Amps, Measurement Techniques of OP-Amp.

UNIT - V

Comparators

Characterization of Comparator, Two-Stage, Open-Loop Comparators, Other Open-Loop Comparators, Improving the Performance of Open-Loop Comparators, Discrete-Time Comparators.

TEXT BOOKS:

1. Philip E. Allen and Douglas R. Holberg – CMOS Analog Circuit Design, Oxford University Press, International Second Edition/Indian Edition, 2010.
2. Paul R. Gray, Paul J. Hurst, S. Lewis and R.G. Meyer - Analysis and Design of Analog Integrated Circuits, 5th edition, Wiley India, 2010.

REFERENCE BOOKS:

1. David A. Johns, Ken Martin - Analog Integrated Circuit Design, Wiley Student Edition, 2013.
2. Behzad Razavi – Design of Analog CMOS Integrated Circuits, TMH.
3. Baker, Liand Boyce – CMOS Circuit Design, Layout and Simulation, PHI.

ARTIFICIAL NEURAL NETWORKS
(Professional Elective-III)

IV B.Tech. I Semester

Course

Code: A227435

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

Upon completing this course, the student will be able to

1. Explore the similarity of Biological networks and Neural networks
2. Perform the training of neural networks using various learning rules.
3. Demonstrate the concepts of forward and backward propagations.
4. Construct the Hopfield models.
5. Demonstrate essence of Neuro Dynamics and Hopfield Models.

UNIT-I

Introduction: A Neural Network, Human Brain, Models of a Neuron, Neural Networks viewed as Directed Graphs, Network Architectures, Knowledge Representation, Artificial Intelligence and Neural Networks

Learning Process: Error Correction Learning, Memory Based Learning, Hebbian Learning, Competitive, Boltzmann Learning, Credit Assignment Problem, Memory, Adaption, Statistical Nature of the Learning Process

UNIT-II

Single Layer Perceptrons: Adaptive Filtering Problem, Unconstrained Organization Techniques, Linear Least Square Filters, Least Mean Square Algorithm, Learning Curves, Learning Rate Annealing Techniques, Perceptron –Convergence Theorem, Relation Between Perceptron and Bayes Classifier for a Gaussian Environment

Multilayer Perceptron: Back Propagation Algorithm XOR Problem, Heuristics, Output Representation and Decision Rule, Computer Experiment, Feature Detection

UNIT-III

Back Propagation: Back Propagation and Differentiation, Hessian Matrix, Generalization, Cross Validation, Network Pruning Techniques, Virtues and Limitations of Back Propagation

UNIT-IV

Self-Organization Maps (SOM): Two Basic Feature Mapping Models, Self-Organization Map, SOM Algorithm, Properties of Feature Map, Computer Simulations, Learning Vector Quantization, Adaptive Patter Classification

UNIT-V

Neuro Dynamics: Dynamical Systems, Stability of Equilibrium States, Attractors, Neuro Dynamical Models, Manipulation of Attractors as a Recurrent Network Paradigm

Hopfield Models – Hopfield Models, restricted Boltzmann machine.

TEXT BOOKS:

1. Simon S Haykin - Neural Networks a Comprehensive Foundations, PHI
2. Jacek M. Zurada - Introduction to Artificial Neural Systems, JAICO Publishing House, 2006.

REFERENCE BOOKS:

1. Li Min Fu - Neural Networks in Computer Intelligence, TMH 2003
2. James A Freeman David M S Kapura - Neural Networks, Pearson , 2004.
3. B. Vegnanarayana -Artificial Neural Networks, Prentice Hall of India P Ltd, 2005

NETWORK SECURITY AND CRYPTOGRAPHY
(Professional Elective-IV)

IV B.Tech. I Semester

Course Code: A227436

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Describe network security fundamental concepts and principles
2. Encrypt and decrypt messages using block ciphers and network security technology and protocols
3. Analyze key agreement algorithms to identify their weaknesses
4. Identify and assess different types of threats, malware, spyware, viruses, vulnerabilities
5. Analyze IP Security, firewall and viruses.

UNIT- I:

Introduction: Security Services, Mechanisms and Attacks, A Model for Internetwork security, Classical Techniques: Conventional Encryption model, Steganography, Classical Encryption Techniques.

Modern Techniques: Simplified DES, Block Cipher Principles, Data Encryption standard, Strength of DES, Block Cipher Design Principles.

UNIT- II:

Encryption: Triple DES, International Data Encryption algorithm, Blowfish, RC5, Characteristics of Advanced Symmetric block Ciphers. Placement of Encryption function, Traffic confidentiality, Key distribution, Random Number Generation

UNIT – III

Public Key Cryptography: Principles, RSA Algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Cryptography

Number Theory: Prime and Relatively prime numbers, Modular arithmetic, Fermat's and Euler's theorems, Testing for primality, Euclid's Algorithm, the Chinese remainder theorem, Discrete logarithms.

UNIT- IV

Message Authentication and Hash Functions: Authentication requirements and functions, Message Authentication, Hash functions, Security of Hash functions and MACs.

Hash and Mac Algorithms: MD-5, Message digest Algorithm, Secure Hash Algorithm.

Digital signatures and Authentication protocols: Digital signatures, Authentication Protocols, Digital signature standards.

Authentication Applications: Kerberos, Electronic Mail Security: Pretty Good Privacy, SIME/MIME.

UNIT – V:

IP Security: Overview, Architecture, Authentication, Encapsulating Security Payload, Key Management. **Web Security:** Web Security requirements, Secure sockets layer and Transport layer security, Secure Electronic Transaction.

Intruders, Viruses and Worms: Intruders, Viruses and Related threats.

Fire Walls: Fire wall Design Principles, Trusted systems.

TEXT BOOKS:

1. Cryptography and Network Security: Principles and Practice - William Stallings, Pearson Education.
2. Network Security: The complete reference, Robert Bragg, Mark Rhodes, TMH,2004.

REFERENCE BOOKS:

1. Network Security Essentials (Applications and Standards) by William Stallings Pearson Education.
2. Fundamentals of Network Security by Eric Maiwald (Dreamtech press)
3. Principles of Information Security, Whitman, Thomson.
4. Introduction to Cryptography, Buchmann, Springer.

SATELLITE COMMUNICATIONS
(Professional Elective-IV)

IV B.Tech. I Semester

Course Code: A227437

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Demonstrate the historical background, basic concepts and frequency allocations for satellite communications.
2. Compare and contrast between various multiple accesses systems for satellite communication system.
3. Understand the propagation effects of signal in Satellite transmission
4. Design of satellite links for specified CNR.
5. Visualize satellite subsystems like telemetry, tracking, command and monitor power systems etc.

UNIT-I:

Introduction to Satellite Communication:

Principles and architecture of satellite Communication, Brief history of Satellite systems, advantages, limitations, applications and frequency bands used for satellite communication.

Orbital Mechanics:

Orbital equations, Kepler's laws, Apogee and Perigee for an elliptical orbit, evaluation of velocity, orbital period, angular velocity etc. of a satellite, concepts of Solar day and Sidereal day. Placement of Satellite in a Geo-stationary orbit

UNIT-II:

Satellite sub systems:

Attitude and orbit control system, TT& C subsystem, control subsystem, power systems, communication subsystems, satellite antenna equipment

Satellite link:

Basic Transmission Theory, System noise temperature and G/T ratio, Basic link analysis, Interference analysis, Design of satellite links for a specified C/N (with and without frequency reuse), Link budget

UNIT – III:

Propagation effects:

Introduction, Atmospheric Absorption, Cloud attenuation, Tropospheric and Ionospheric scintillation and low angle fading, Rain induced attenuation, Rain induced cross polarization interference.

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Multiple Access:

Frequency division multiple access(FDMA), inter modulation, calculation of C/N, Time division multiple access(TDMA) – frame structure, Burst structure, Satellite switched TDMA, on-board processing, Demand Assignment multiple Access (DAMA), CDMA spread spectrum transmission and reception.

UNIT – IV:

Earth station Technology:

Transmitters, Receivers, Antenna, Tracking systems, Terrestrial interface, Power test methods, Lower orbit considerations. Satellite Navigation and Global Positioning systems: radio and satellite navigation, GPS position location principles, GPS receivers

UNIT – V:

Typical Phenomena in Satellite Communication:

Solar Eclipse on satellite and its effects, Remedies for Eclipse, Sun Transit Outage phenomena, its effects and remedies, Doppler frequency shift phenomena and expression for Doppler shift.

TEXT BOOKS:

1. Timothy pratt, Charles Bostian, Jeremy Allnutt , Satellite communications, John Wiley, 2003
2. Pritchard, Satellite communications engineering, Pearson, 1993.

REFERENCE BOOKS:

1. Madhavendra Richharia, Satellite communications: Design principles, Macmillan,2017
2. Tri T. Ha , Digital satellite communications, McGraw-Hill, 1990

BIOMEDICAL INSTRUMENTATION
(Professional Elective-IV)

IV B.Tech. I Semester

Course Code: A227438

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Summarize the requirement of biomedical instrumentation and adversity involved in human measurement.
2. Understand the concept of Bio Potentials in a Human Body
3. Utilize the concept of electrode and its responses used in real time.
4. Outline the divergent responses involved in cardiovascular and respiratory system.
5. Compare the various processes involved in bio telemetry.

UNIT-I:

Introduction:

The age of Biomedical Engineering, Development of Biomedical Instrumentation, Man–Instrumentation system, Components, Physiological system of the body, Problem encountered in measuring a living system. Transducers & Electrodes: The Transducers & Transduction principles, Active transducers, Passive Transducers, Transducer for Biomedical Applications.

UNIT-II:

Sources of Bioelectric potentials:

Resting & Action potentials, propagation of active potential, The Bioelectric potentials-ECG, EEG, EMG, and Invoked responses Electrodes: Electrode theory, Biopotential Electrodes–Microelectrodes Body surface electrodes, Needle Electrodes, Biochemical Transducers, Reference electrodes, PH electrodes, Blood Gas electrodes.

UNIT-III:

Cardiovascular Measurements:

Electrocardiography – ECG amplifiers, Electrodes & leads, ECG recorders - Three channel, Vector Cardiographs, ECG system for stress testing, Continuous ECG recording (Holterrecording), Blood pressure measurement, Blood flow measurement, Heart sound measurements. Patient Care & Monitoring- Elements of Intensive Care monitoring, patient monitoring displays, Diagnosis, Calibration & Reparability of patient monitoring equipment, pacemakers & Defibrillators

UNIT-IV:

Measurements in Respiratory system:

Physiology of respiratory system Measurement of breathing mechanics- Spiro meter, Respiratory Therapy equipment's: Inhalators ventilators & Respirators, Humidifiers, Nebulizers & Aspirators. Diagnostic Techniques: Ultrasonic Diagnosis Echocardiography, Echo Encephalography, Ophthalmic scans, X-Ray & Radio-isotope Instrumentation, Computerized Axial Tomography Scanners

UNIT-V:

Bio Telemetry:

The components of Biotelemetry system Implantable units, Telemetry for ECG measurements during exercise, for Emergency patient monitoring. Physiological Effects of Electric Current Safety of Medical Electronic Equipments, Shock hazards from Electrical equipment and prevention against them

TEXT BOOKS:

1. LeslieCormwell, Biomedical Instrumentation and Measurements, Prentice Hall India,1980
2. Arumugam M, Biomedical Instrumentation, Anuradha Publications, 1994

REFERENCE BOOKS:

1. Khandpur R.S, Biomedical Instrumentation, Tata McGraw-Hill, 2003 .
2. Willis J Tompkins, Biomedical DSP: C Language Examples and Laboratory Experiments for the IBM PC, Prentice Hall India, 2006.

PRINCIPLES OF SIGNAL PROCESSING
(Open Elective–III)

IV B.Tech. I Semester

Course Code: A227439

L	T	P	C
3	0	0	3

Course Outcomes Upon completing this course, the student will be able to

1. Differentiate various signal functions.
2. Understand the characteristics of linear time invariant systems.
3. Understand the concepts of sampling theorem and signal to noise ratios.
4. Understanding the Concept of Z-Transform of a Discrete Sequence and systems
5. Demonstrate the effect of Noise on signals.

UNIT I: Signal Analysis:

Introduction to Signals and Systems, Classification of Signals and systems, Exponential and Sinusoidal signals, Concepts of Impulse function, Unit Step function, Signum function, Operation on signals.

UNIT II: Signal Transmission through Linear Systems Linear System:

Impulse response, Response of a Linear System, Linear Time Invariant (LTI) System, Linear Time Variant (LTV) System, Transfer function of a LTI System, Signal bandwidth, System Bandwidth, Ideal LPF, HPF, and BPF characteristics, Convolution and Correlation of Signals, Concept of convolution in Time domain and Frequency domain,

UNIT III: Sampling theorem:

Sampling theorem for band limited signals, Impulse Sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling – Aliasing, Introduction to Band Pass Sampling.

UNIT IV: Z-Transforms:

Concept of Z-Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms,

Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

Department of Electronics and Communication Engineering
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UNIT V: Noise:

Introduction to Noise, Resistive/Thermal Noise Source, Shot Noise Sources, Effective Noise Temperature, Noise equivalent bandwidth, Average Noise Figures, Average Noise Figure of cascaded networks.

TEXT BOOKS:

1. Signals, Systems & Communications - B.P. Lathi, B.S. Publications, 2013.
2. Probability, Random Variables & Random Signal Principles - Peyton Z. Peebles, TMH, 4 th Ed., 2001.

REFERENCES:

1. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawabi, 2 Ed.
2. Statistical Theory of Communication – S.P Eugene Xavier, New Age Publications, 2003



COMMUNICATION TECHNOLOGIES
(Open Elective–III)

IV B.Tech. I Semester

Course Code: A227440

L	T	P	C
3	0	0	3

Course Outcomes: Upon completing this course, the student will be able to

1. Understand the information theory and its coding styles.
2. Acquire knowledge on satellite communication and broadcasting services.
3. Know GSM, LTE and 5G mobile networks.
4. Know about network security through encryption and decryption.
5. Understand the principles of Encryption and Authentication.

UNIT I:

INFORMATION THEORY: Shannon Capacity, Multimedia Data, Data Processing, Boolean Logics, Information Content, Entropy, Source Coding, Channel Coding, Modulation Schemes, Internet.

UNIT II:

WIRELESS COMMUNICATION TECHNOLOGIES: WLAN, Wifi, Bluetooth, Other Wireless PAN And WAN Technologies, Satellite Communications, Broadcast Services.

UNIT III:

CELLULAR MOBILE NETWORKS: GSM (2G), UMTS (3G), LTE (4G), 5G Mobile Networks, Mobile Network Planning Aspects.

UNIT IV:

FREE SPACE OPTICAL COMMUNICATIONS: Optical fiber, FTTC, FTTH, FTTBS, Free Space Optical Link, Channel Model with Different Factors, Deep Space Optical Communications.

UNIT V:

NETWORK SECURITY AND MANAGEMENT: Symmetrical Encryption, Asymmetrical Encryption, Authentication, Hash-Value, Integrity Check, Telecommunications Management Network, SNMP, Functionalities of Network Management, Trends and Future Development.



TEXT BOOKS:

1. Shun-Ping Chen, “Fundamentals of Information and Communication Technologies” 2020
2. B.P. Lathi, “Communication systems”- BS Publications, 2006.

REFERENCES:

1. Simon Haykin, John Wiley “Digital Communications” 2005.
2. Herbert Taub, Donald L Schilling Gautham Saha “Principles of Communication systems”
3rd edition McGraw-Hill 2008.

IOT FOR REAL TIME APPLICATIONS
(Open Elective–III)

IV B.Tech. I Semester

Course Code: A227441

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Design remote data sensing and aggregation system for health care
2. Develop energy efficient IoT systems for smart cities
3. Design Precision agricultural systems
4. Develop smart automation system for Industries
5. Investigate various IoT based innovative systems for societal benefits

UNIT I:

IoT FOR HEALTHCARE:

Architecture of IoT for Healthcare, IoT based Health Monitoring System using Arduino, Smart continuous glucose monitoring (CGM) and insulin pens, Remote Patient Monitoring- IoT Heart Rate Monitoring, remote monitoring of physiological parameters, ECG, EEG, Diabetics and BP.

UNIT II:

IoT ENABLED SMARTCITIES:

Energy Consumption Monitoring, Smart Energy meters to minimize power consumptions, Smart home powered by IoT, Smart Grid and Solar Energy Harvesting, Intelligent Parking System

UNIT III:

IoT FOR SMARTAGRICULTURE:

Animal Intrusion detection in farms, soil moisture detection and Irrigation system, Pest monitoring and control, Livestock monitoring system, IoT based Greenhouse Environment Monitoring and controlling.

UNIT IV:

IoT BASED INDUSTRIAL AUTOMATION:

IoT based gas leakage monitoring system, Temperature and liquid level monitoring in boilers, Fire detection system, wireless video surveillance robot, Automatic Solar Tracker

UNIT V:

IoT FOR SOCIETY:

Medical Waste Management, Weather update system with IoT Women security system, GPS Smart Sole, wearable glove to enables into speech conversation, IoT based air pollution meter

TEXT BOOKS:

1. Fadi Al-Turjman, Intelligence in IoT- enabled Smart Cities, 2019, 1st edition, CRC Press, ISBN-10:1138316849
2. Giacomo Veneri, and Antonio Capasso, Hands-on Industrial Internet of Things: Create a powerful industrial IoT infrastructure using Industry 4.0, 2018, Packt Publishing.

REFERENCES BOOKS:

1. Krishna P. Venkata, Sasikumar Gurumoorthy, Mohammad S. Obaidat, Internet of Things and Personalized Healthcare Systems, Springer-2019.
2. Pattnaik, Prasant Kumar, Mohanty, Suneeta, Mohanty, Satarupa, Smart Health care Analytics in IoT Enabled Environment, Springer-2020.

MICROWAVE AND OPTICAL COMMUNICATIONS LABORATORY

IV B.Tech. I Semester

Course Code: A227493

L	T	P	C
0	0	4	2

COURSE OBJECTIVES

At the end of the Course, the students able to

1. Understand the behavioral aspects of various microwave sources
2. Analyze the measurement procedures of important parameters in microwave engineering.
3. Evaluate the S-Parameters of various three and four port devices
4. Understand the characteristics of various optical sources.
5. Measure the losses, intensity modulation for the analog optical fiber and data rate for digital Optical fiber.

Note: Any **12** of the following experiments

LIST OF EXPERIMENTS:

1. Reflex Klystron Characteristics
2. Gunn Diode Characteristics
3. Directional Coupler Characteristics
4. VSWR Measurement
5. Measurement of Waveguide Parameters
6. Measurement of Scattering parameters of a Magic Tee
7. Measurement of Scattering parameters of a Circulator
8. Attenuation Measurement
9. Characterization of LED.
10. Characterization of Laser Diode.
11. Intensity modulation of Laser output through an optical fiber.
12. Measurement of Data rate for Digital Optical link.
13. Measurement of Numerical Aperture of fiber cable.
14. Measurement of losses for Optical link



WIRELESS SENSOR NETWORKS

IV B.Tech. II Semester

Course Code: A228442

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

Upon completion of the course, the student will be able to:

- CO1:** Apply fundamental concepts of wireless sensor networks to identify suitable architectures and applications.
- CO2:** Analyze constraints, challenges, and enabling technologies of WSNs and MANETs for different deployment scenarios.
- CO3:** Apply routing and MAC protocols (S-MAC, B-MAC, IEEE 802.15.4, ZigBee) to support efficient communication in WSNs.
- CO4:** Analyze data dissemination and fusion in WSNs with respect to QoS, real-time, and security needs.
- CO5:** Apply WSN design principles to analyze node architecture, gateway concepts, and WSN–Internet integration

UNIT -1:

Introduction to Sensor Networks, unique constraints and challenges, Advantage of Sensor Networks, Applications of Sensor Networks, Types of wireless sensor networks

UNIT - II:

Mobile Ad-hoc Networks (MANETs) and Wireless Sensor Networks, Enabling technologies for Wireless Sensor Networks. Issues and challenges in wireless sensor networks

UNIT - III:

Routing protocols, MAC protocols: Classification of MAC Protocols, S-MAC Protocol, B-MAC protocol, IEEE 802.15.4 standard and ZigBee

UNIT -IV:

Dissemination protocol for large sensor network. Data dissemination, data gathering, and data fusion; Quality of a sensor network; Real-time traffic support and security protocols.

UNIT -V:

Design Principles for WSNs, Gateway Concepts Need for gateway, WSN to Internet Communication, and Internet to WSN Communication. Single-node architecture, Hardware components & design constraints, Operating systems and execution environments, introduction to TinyOS and nesC.



TEXT BOOKS:

1. Ad-Hoc Wireless Sensor Networks- C. Siva Ram Murthy, B. S. Manoj, Pearson
2. Principles of Wireless Networks – Kaveh Pahlavan and P. Krishna Murthy, 2002, PE

REFERENCE BOOKS:

1. Wireless Digital Communications - Kamilo Feher, 1999, PHI.
2. Wireless Communications-Andrea Goldsmith, 2005 Cambridge University Press.
3. Mobile Cellular Communication - Gottapu Sasibhushana Rao, Pearson Education, 2012.
4. Wireless Communication and Networking - William Stallings, 2003, PHI.



ARTIFICIAL INTELLIGENCE
(Professional Elective-V)

IV B.Tech. II Semester

Course Code: A228443

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of course, the student will be able to

1. Understand the basics of the theory and practice of Artificial Intelligence as a discipline and about intelligent agents.
2. Explain search techniques and gaming theory.
3. Learn to apply knowledge representation techniques and problem solving Strategies to common AI applications.
4. Understand the basic ideas of software agents utilized in Artificial Intelligence.
5. Illustrate the basic applications of Artificial Intelligence.

UNIT I:

Introduction- Definition - History & overview of Artificial Intelligence, Applications - Future of Artificial Intelligence - Characteristics of Intelligent Agents-Typical Intelligent Agents - Problem Solving Approach to Typical AI problems.

UNIT II:

Problem solving Methods - Search Strategies- Uninformed and Informed - Heuristics - Uninformed Search: depth first search, breadth first search - Heuristic Search Strategies: Greedy Best First Search, A* Search - Beyond Classical Search: Hill-climbing search, Simulated annealing search.

UNIT III:

Knowledge Representation First Order Predicate Logic - Unification - Forward Chaining- Backward Chaining - Resolution - Knowledge Representation - Ontological Engineering- Categories and Objects - Events - Mental Events and Mental Objects - Reasoning Systems for Categories -Reasoning with Default Information

UNIT IV:

Software Agents Architecture for Intelligent Agents - Agent communication -Negotiation and Bargaining - Argumentation among Agents - Trust and Reputation in Multi-agent systems.

UNIT V:

Applications: AI applications – Language Models – Information Retrieval-
Information Extraction - Natural Language Processing - Machine Translation - Speech
Recognition.

TEXT BOOKS:

1. S. Russell and P. Norvig, "Artificial Intelligence: A Modern Approach, Prentice Hall, Third Edition, 2009.
2. Artificial Intelligence: A Modern Approach, 4th Edition, Stuart Russell, peter Norvig University of California at Berkeley, Pearson education, 2020.

REFERENCES:

1. M. Tim Jones, -Artificial Intelligence: A Systems Approach (Computer Science), Jones and Bartlett Publishers, Inc.; First Edition, 2008
2. Nils J. Nilsson, -The Quest for Artificial Intelligence, Cambridge University Press, 2009.

5G AND BEYOND COMMUNICATIONS
(Professional Elective-V)

IV B.Tech. II Semester

Course Code: A228444

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Understand basic concepts of MIMO systems
2. Contrast among Mobile Wireless Technologies
3. Demonstrate the channel effects in radio wave propagation.
4. Recite the Design Considerations for mm Wave communication
5. Know the scope of Future Mobile Technologies

UNIT I: Multiple Input Multiple Output (MIMO) Communications:

Spatial Multiplexing, Spatial Diversity, Beam forming in MIMO systems, Hybrid Precoding, 5G Communication Landscape, Related work on 5G.

UNIT II:

Introduction to Mobile Wireless Technology Generations:

5G, WISDOM, GIMVC, Requirements of 5G, standardization of WISDOM, Vision of 5G, WISDOM Concept and Challenges, Cellular D2D Communication, D2D Using Physical Layer Network Coding, Using FFR and Using Cognitive Radio.

SMNAT: Introduction, Network Architecture and the Process, Implementation of SMNAT for In-Band-D2D and Interoperability with WISDOM, Description of Network elements of SMNAT and Call Flow for Session Establishment.

UNIT III:

Radio Wave Propagation for Mm Wave:

Introduction, Large-scale Propagation Channel Effects, Small-Scale Channel Effects, Spatial Characterization of Multipath and Beam Combining, Outdoor Channel Models, Indoor Channel Models.

UNIT IV:

Higher layer Design Considerations for Mm Wave:

Challenges when Networking Mm Wave Devices, Beam Adaptation Protocols, Relaying for Coverage Extension, Support for Multimedia Transmission, Multiband considerations, Performance of Cellular networks, Mm Wave Standardization: ECMA-387, IEEE 802.11ad.

UNIT V: BEYOND 2020

Major Challenges Surrounding Future Cyber Security, Users Awareness, Spectrum Related Security Issues in CRNs. Challenges for 2020 and beyond, Future Mobile Technologies, High Altitude Stratospheric Platform Station Systems, Human Bond Communications, CONASENSE.

TEXTBOOKS:

1. Ramjee Prasad, *5G: 2020 and Beyond* , River Publishers
2. T. S. Rappaport, R. W. Heath Jr., R. C. Daniels, and J. M. Murdock, *Millimeter Wave Wireless Communication*, Pearson Education , 2015.

REFERENCES:

1. M. Manish, G. Devendra, P. Pattanayak, and N. Ha, *5G and Beyond Wireless Systems PHY Layer Perspective*, Springer Series in Wireless Technology
2. M. Vaezi, Z. Ding, and H. V. Poor, *Multiple Access techniques for 5G Wireless Networks and Beyond*, Springer Nature , Switzerland, 201



MACHINE LEARNING
(Professional Elective-V)

IV B.Tech. II Semester

Course Code: A228445

COURSE OUTCOMES:

L	T	P	C
3	0	0	3

After completion of the course student will be able to

1. Understand basics of Artificial Neural Networks computational model
2. Understand the essentials of feature engineering, state-of-art tools and concepts of machine Learning
3. Design and evaluate different types of supervised learning algorithms for classification and Regression tasks
4. Design and evaluate different types of unsupervised learning algorithms for clustering tasks.
5. Design and evaluate strong learners for better real time prediction ensemble learning Algorithms

UNIT-I:

Artificial Neural Networks: Introduction, Understanding the Biological Neuron, Exploring the Artificial Neuron, Types of Activation Functions, Early Implementations of ANN, Architectures of Neural Network: Single-layer and multi-layer feed forward ANNs, Recurrent network, Learning Process in ANN, Back propagation.

UNIT-II:

Introduction to Machine Learning: What is Machine Learning, Why Machine Learning, Types of Machine Learning models, Challenges of Machine Learning, Applications of Machine Learning, Essential libraries and Tools, Generalization over fitting and under fitting, Bias- variance trade-off, and metrics.

UNIT-III:

Supervised Learning: Classification and Regression, Linear Regression: Single and Multiple, Logistic Regression, K-Nearest Neighbour, Naive Bayes Classifier, Decision Tree.

UNIT-IV:

Unsupervised Learning and Pre-processing: Types of Unsupervised Learning, Challenges in Unsupervised Learning, Applications of Unsupervised Learning, Pre-processing and Scaling, clustering, K-Means Clustering, Agglomerative Clustering.

UNIT-V:

Ensemble Learning and Random Forest: Voting Classifiers, Bagging and pasting, Random Patches and Random subspaces, Random Forest, Boosting-AdaBoost.

TEXT BOOKS:

1. Saikat Dutt, Subramanian Chandramouli, Amit Kumar Das "Machine Learning", Pearson Education India, 2018.
2. Aurelien Geron, "Hands-on Machine Learning with Scikit-Learn, Keras, and TensorFlow Concepts, Tools, and Techniques to Build Intelligent Systems" O'Reilly Media, Inc, 2017.

REFERENCE BOOKS:

1. Andreas C. Müller, Sarah Guido, "Introduction to Machine Learning with Python", O'Reilly Media, Inc, October 2016.
2. Tom M. Mitchell, "Machine Learning", McGraw-Hill Education (India) Private Limited, 2013.
3. Ethem Alpaydin, "Introduction to Machine Learning (Adaptive Computation and Machine Learning)", The MIT Press, 2004.
4. Stephen Marsland, "Machine Learning: An Algorithmic Perspective", CRC Press, 2009.

MULTIMEDIA DATABASE MANAGEMENT SYSTEMS
(Professional Elective-VI)

IV B.Tech. II Semester

Course Code: A228446

L	T	P	C
3	0	0	3

COURSE OUTCOMES

At the end of the course the student should be able to

1. Gain knowledge of fundamentals of DBMS, database design and normal forms
2. Master the basics of SQL for retrieval and management of data.
3. Be acquainted with the basics of transaction processing and concurrency control.
4. Familiarity with database storage structures and access techniques
5. Understand the indexing in multimedia DBMS

UNIT - I

Database System Applications: A Historical Perspective, File Systems versus a DBMS, the Data Model, Levels of Abstraction in a DBMS, Data Independence, Structure of a DBMS
Introduction to Database Design: Database Design and ER Diagrams, Entities, Attributes, and Entity Sets, Relationships and Relationship Sets, Additional Features of the ER Model, Conceptual Design With the ER Model

UNIT - II

Introduction to the Relational Model: Integrity constraint over relations, enforcing integrity constraints, querying relational data, logical data base design, introduction to views, destroying/altering tables and views.

Relational Algebra, Tuple relational Calculus, Domain relational calculus.

UNIT - III

SQL: Queries, Constraints, Triggers: form of basic SQL query, UNION, INTERSECT, and EXCEPT, Nested Queries, aggregation operators, NULL values, complex integrity constraints in SQL, triggers and active data bases.

Schema Refinement: Problems caused by redundancy, decompositions, problems related to decomposition, reasoning about functional dependencies, FIRST, SECOND, THIRD normal forms, BCNF, lossless join decomposition, multi-valued dependencies, FOURTH normal form, FIFTH normal form.

UNIT - IV

Transaction Concept, Transaction State, Implementation of Atomicity and Durability, Concurrent Executions, Serializability, Recoverability, Implementation of Isolation, Testing for serializability, Lock Based Protocols, Timestamp Based Protocols, Validation- Based Protocols, Multiple Granularity, Recovery and Atomicity, Log-Based Recovery, Recovery with Concurrent Transactions.

UNIT - V

Data on External Storage, File Organization and Indexing, Cluster Indexes, Primary and Secondary Indexes, Index data Structures, Hash Based Indexing, Tree base Indexing, Comparison of File Organizations, Indexes and Performance Tuning, Intuitions for tree Indexes, Indexed Sequential Access Methods (ISAM), B+ Trees: A Dynamic Index Structure.

TEXT BOOKS:

1. Database Management Systems, Raghurama Krishnan, Johannes Gehrke, *Tata Mc Graw Hill* 3rd Edition
2. Database System Concepts, Silberschatz, Korth, *Mc Graw hill*, V edition.

REFERENCE BOOKS:

1. Database Systems design, Implementation, and Management, Peter Rob & Carlos Coronel 7th Edition.
2. Fundamentals of Database Systems, ElmasriNavrate, *Pearson Education*
3. Introduction to Database Systems, C. J. Date, *Pearson Education*
4. Oracle for Professionals, The X Team, S.Shah and V. Shah, *SPD*.
5. Database Systems Using Oracle: A Simplified guide to SQL and PL/SQL, Shah, *PHI*.
6. Fundamentals of Database Management Systems, M. L. Gillenson, *Wiley Student Edition*.



SYSTEM ON CHIP ARCHITECTURE
(Professional Elective-VI)

IV B.Tech. II Semester

Course Code: A228447

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Expected to understand SOC Architectural features.
2. Acquire the knowledge on processor selection criteria and limitations
3. Acquires the knowledge of memory architectures on SOC.
4. Understands the interconnection strategies and their customization on SOC.
5. Elaborate Reconfiguration Technologies in SOCA.

UNIT –I:

Introduction to the System Approach: System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory and Addressing. System level interconnection, An approach for SOC Design, System Architecture and Complexity.

UNIT –II:

Processors: Introduction, Processor Selection for SOC, Basic concepts in Processor Architecture, Basic concepts in Processor Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT –III:

Memory Design for SOC: Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – I , and D – Caches , Multilevel Caches, Virtual to real translation , SOC Memory System , Models of Simple Processor – memory interaction.



UNIT - IV:

Interconnect Customization: Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses, Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time. SOC Customization:

UNIT - V:

Configuration: An overview, Customizing Instruction Processor, Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance - Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

TEXT BOOKS:

1. Computer System Design System-on-Chip by Michael J. Flynn and Wayne Luk, Wiley India Pvt. Ltd.
2. ARM System on Chip Architecture – Steve Furber -2nd Ed., 2000, Addison Wesley Professional.

REFERENCE BOOKS:

1. Design of System on a Chip: Devices and Components – Ricardo Reis, 1st Ed., 2004, Springer
2. Co-Verification of Hardware and Software for ARM System on Chip Design (Embedded Technology)-Jason Andrews -Newnes, BK and CDROM
3. System on Chip Verification – Methodologies and Techniques -Prakash Rashinkar, Peter Paterson and Leena Singh L, 2001, Kluwer Academic Publishers.



SOFTWARE DEFINED RADIO

(Professional Elective-VI)

IV B.Tech. II Semester

Course Code: A228448

L	T	P	C
3	0	0	3

COURSE OUTCOMES:

At the end of the course the student should be able to

1. Explain the architecture and functioning of traditional hardware radios and software-defined radios, and compare their advantages, limitations, and applications.
2. Analyze the block-level architecture of SDR systems, including RF front-end, frequency conversion, and digital signal processing components.
3. Apply RF system design concepts such as noise, channel capacity, link budget, ADC/DAC fundamentals, sampling techniques, and multirate signal processing used in SDR.
4. Evaluate different signal processing hardware platforms—DSPs, FPGAs, reconfigurable processors—and software architectures, standards, and real-time operating systems used in SDR development.
5. Describe and analyze smart antenna systems, including phased arrays, DOA estimation, beam forming techniques, and their integration with SDR for advanced wireless communication.

UNIT - I:

Introduction – Software Defined Radio: A Traditional Hardware Radio Architecture, limitations of HDR, Advantages of using SDR, Applications of SDR.

UNIT- II:

A Basic Software Defined Radio Architecture: Introduction – 2G Radio Architectures- Hybrid Radio Architecture- Basic Software Defined Radio Block Diagram- SDR limitations. System Level Functioning Partitioning- Digital Frequency Conversion Partitioning.

UNIT- III:

RF System Design: Introduction- Noise and Channel Capacity- Link Budget- Receiver Requirements- Multicarrier Power Amplifiers- Signal Processing Capacity Tradeoff. Introduction to Analog-to-Digital and Digital-to-Analog Conversion, Fundamentals - Sample Rate - Band pass Sampling – Oversampling - Anti alias Filtering – Quantization. Digital Frequency Up- and Down Converters- Introduction- Frequency Converter Fundamentals- Digital NCO- Digital Mixers- Digital Filters- Half band Filters- CIC Filters- Decimation, Interpolation, and Multi rate Processing-DUCs - Cascading Digital Converters and Digital Frequency Converters.



UNIT- IV:

Signal Processing Hardware Components: Introduction- SDR Requirements for Processing Power- DSPs- DSP Devices- DSP Compilers- Reconfigurable Processors- Adaptive Computing Machine- FPGAs

Software Architecture and Components: Introduction- Major Software Architecture Choices – Hardware– Specific Software Architecture-Software Standards for Software Radio-Software Design Patterns- Component Choices- Real Time Operating Systems- High Level Software Languages- Hardware Languages.

UNIT- V:

Smart Antennas for Software Radio: Introduction - 3G smart Antenna Requirements-Phased Antenna Array Theory- Applying Software Radio Principles to Antenna Systems- Smart Antenna Architectures- Optimum combining/ Adaptive Arrays- DOA Arrays- Beam Forming for CDMA- Downlink Beam Forming.

TEXT BOOKS:

1. Paul Burns, Software Defined Radio for 3G, Artech House, 2002.
2. Tony J Roupael, RF and DSP for SDR, Elsevier Newnes Press,2008

REFERENCE BOOKS:

1. JoukoVanakka, Digital Synthesizers and Transmitter for Software Radio, Springer, 2005.
2. P Kenington, RF and Baseband Techniques for Software Defined Radio, Artech House, 2005.