

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
COURSE FILE

COMPILER DESIGN



(ESTD - 1999)

Vidya Jyothi Institute of Technology

(An Autonomous Institution)

**(Accredited by NAAC & NBA , Approved By A.I.C.T.E., New
Delhi, Permanently Affiliated to**

J.N.T. University, Hyderabad)

(Aziz Nagar, C.B.Post, Hyderabad -500075)

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

REGULATION: R15
BATCH: 2017-2021

ACADEMIC YEAR: 2019-20
PROGRAM: B.TECH (COMPUTER SCIENCE AND ENGINEERING)

YEAR/SEM: III/II

COURSE NAME: COMPILER DESIGN

COURSE CODE: A26524
PRE REQUISITE: FORMAL LANGUAGES AND AUTOMATA THEORY

COURSE COORDINATOR: Dr.D.Aruna Kumari

COURSE INSTRUCTORS:

1. Dr.K.Ramesh Babu
2. M.Vijaya
3. Zaheer

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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Head of the Department
Computer Science and Engineering
VJIT, Hyderabad-50075.

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Syllabus

COMPILER DESIGN

COMPILER DESIGN**B. Tech. III Year II Semester**

L	T	P	C
3	1	0	3

Course Outcomes:

At the end of the course student would be able to

1. Formulate tokens for various programming languages.
2. Apply principles of parsing techniques to do syntax analysis.
3. Formulate semantic rules to do semantic analysis.
4. Apply optimization techniques on the intermediate code.
5. Generate the target code.

UNIT – I

Overview of Compilation: Phases of Compilation – Lexical Analysis, Regular Grammar and regular expression for common programming language features, pass and Phases of translation, interpretation, bootstrapping, data structures in compilation – LEX lexical analyzer generator.

Top down Parsing: Context free grammars, Top down parsing – Backtracking, LL (1), recursive descent parsing, Predictive parsing, Preprocessing steps required for predictive parsing.

UNIT – II

Bottom up parsing: Shift Reduce parsing, LR and LALR parsing, Error recovery in parsing, handling ambiguous grammar, YACC – automatic parser generator.

UNIT – III

Semantic analysis: Intermediate forms of source Programs – abstract syntax tree, polish notation and three address codes. Attributed grammars, Syntax directed translation, Type checker.

Symbol Tables: Symbol table format, organization for block structures languages, hashing, and tree structures representation of scope information. Block structures and non block structure storage allocation: static, Runtime stack and heap storage allocation, storage allocation for arrays, strings and records.

UNIT – IV

Code optimization: Consideration for Optimization, Scope of Optimization, local optimization, loop optimization, frequency reduction, folding, DAG representation.

Data flow analysis: Flow graph, data flow equation, global optimization, redundant sub expression elimination, Induction variable elements, Live variable analysis, Copy propagation.

UNIT – V

Object code generation: Object code forms, machine dependent code optimization, register allocation and assignment generic code generation algorithms, DAG for register allocation.

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Text Books & Reference Books

COMPILER DESIGN

Text Book:

1. Alfred V Aho, Jeffrey D Ullman, Principles of Compiler Design, Pearson Education, 2001.

Reference Books:

1. J P Trembly and P G Sorenson, The Theory and practice of Compiler Writing, McGraw Hill, 2005.
2. Alfred V Aho, Ravi sethi, Jeffrey D Ullman, Compilers-Principles, Techniques and Tools, Pearson Education, second edition.
3. Dick Grone, Henri E Bal, Criel J H Jacobs, Modern Compiler Design, Wiley Dreamtech, 2006.

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Time Table

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

VIDYA JYOTHI INSTITUTE OF TECHNOLOGY
Department of Computer Science & Engineering

Sec: CSE-B

Year/Sem: III - II

W.E.F: 09/12/2019

ROOM NO: N-203

DAY	9.00 – 9.55	9.55 – 10.50	10.50 – 11.45	11.45 – 12.30	12.30 – 1.25	1.25 – 2.20	2.20 – 3.15	3.15- 4.10
MON	CD	MEFA	PDBS	LUNCH	WT/CT LAB			Value Added
TUE	DWDM	OE			OOAD	CD	WT(T)	NPTEL
WED	OOAD	OE			CD/DM LAB			HACKER RANK
THU	MEFA	OOAD	CD		DWDM	WT	PDBS	CISCO
FRI	DWDM	WT	MEFA		CD	OOAD(T)	DWDM(T)	CISCO
SAT	WT	DWDM	OOAD		DWDM	MEFA(T)	CD(T)	Value Added

Subject	Name of the Faculty	
WT	Web Technologies	Mr.M.Tarakeshwar Rao
CD	Compiler Design	Dr. D.Aruna Kumari
OOAD	Object Oriented Analysis and Design	Ms.Sailaja
DWDM	Data Warehousing & Data Mining	Mr. Abdul Majeed
MEFA	Managerial Economics & Financial Analysis	Ms. G.Rajitha
PD&BS	Personality Development & Behavioral Skills	Ms.Padma Venkat
WT&CT LAB	Web Technologies & Case Tools Lab	Mr.M.Tarakeshwar Rao / Ms. Sailaja/G.Surekha
CD&DM LAB	Compiler Design & Data Mining Lab	Mr.B.Thikkana / Mr. Abdul Majeed/ Mr.Y.Praveen Kumar
OE	Database Management Systems	Mr.B.Srinivasulu

Class Incharge
 III YEAR Coordinator

Mr.M.Tarakeswar Rao
 Ms. M.Vijaya

Time Table I/C

H.O.D

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 Computer Science and Engineering
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**Program Outcomes (PO's),
Program Specific Outcomes
(PSO's) & Program Educational
Objectives (PEO's)**

Programme Outcomes (PO's)

1. **Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization for the solution of complex engineering problems.
2. **Problem analysis:** Identify, formulate, research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3. **Design/development of solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for public health and safety, and cultural, societal, and environmental considerations.
4. **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
5. **Modern tool usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools, including prediction and modeling to complex engineering activities, with an understanding of the limitations.
6. **The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal, and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7. **Environment and sustainability:** Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
8. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
9. **Individual and team work:** Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
10. **Communication:** Communicate effectively on complex engineering activities with the engineering community and with the society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
11. **Project management and finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
12. **Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

Program Specific Outcomes (PSO):

PSO 1: The ability to design and develop Algorithms to provide optimized solutions for societal needs

PSO 2: Apply standard approaches and practices in Software Project Development through trending technologies

Program Educational Objectives (PEO's)

PEO1: Enhance the employability of the graduate in software industries/Public sector/Research organizations

PEO2: Acquire analytical and computational abilities to pursue higher studies for professional growth

PEO3: Work in multidisciplinary project teams with effective communication skills and leadership qualities

PEO4: Develop professional ethics among the students and promote entrepreneurial abilities

Mapping of Course Outcomes (CO's) With Program Outcomes (PO's) & Program Specific Outcomes (PSO's)

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING



VIDYA JYOTHI INSTITUTE OF TECHNOLOGY Department of Computer Science & Engineering

Year & Sem: III year II Sem

Course name: Compiler Design

Course Code: A16523

Regulation: R15

COURSE OUTCOMES:

After completing this course the student must demonstrate the knowledge and ability to	
CO1	Differentiate the phases in compilation & parsing.
CO2	Identify the process in parsing and semantic analysis.
CO3	Explain about symbol tables and code optimization methods.
CO4	Explain about code optimization methods.
CO5	Analyze data flow and generate object code.

]

CO - PO MAPPING:

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
CO 1	3	3	3	3	1	3	2	2	1	1	-	3
CO 2	3	3	3	3	3	3	2	2	1	1	-	3
CO 3	3	3	3	3	2	3	-	2	2	2	2	3
CO 4	3	3	3	3	2	3	-	2	2	2	2	3
CO 5	3	3	3	3	2	3	2	2	2	2	2	3
Avg	3	3	3	3	2	3	2	2	1.6	1.6	2	3

CO - PSO MAPPING:

	PSO1	PSO2
CO1	3	3
CO2	3	3
CO3	2	2
CO4	3	2
CO5	3	2
Avg	2.8	2.4


Signature of the Course Coordinator.


Sign. of HOD

Head of the Department
Computer Science and Engineering
VJIT, Hyderabad-50075.

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Academic Calendar

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II B.Tech I & II Semester Academic Calendar for the Academic Year 2019-20

II YEAR I SEMESTER		Commencement of Class Work 17.06.2019	
	From	To	Duration
I Spell of Instruction	17.06.2019	10.08.2019	8 WEEKS
I Mid Examinations	13.08.2019	17.08.2019	4 DAYS
II Spell of Instruction	19.08.2019	05.10.2019	7 WEEKS
Dussehra Holidays	07.10.2019	12.10.2019	1 WEEK
II Spell of Instruction Continuation	14.10.2019	19.10.2019	1 WEEK
II Mid Examinations	21.10.2019	24.10.2019	4 DAYS
Practical Examinations	25.10.2019	29.10.2019	4 DAYS
Betterment Examinations	30.10.2019	01.11.2019	3 DAYS
End Semester Examinations	02.11.2019	18.11.2019	2 WEEKS
Supplementary Examinations	19.11.2019	04.12.2019	2 WEEKS
Mid-II Examinations (For Lateral Entry)	18.11.2019	21.11.2019	4 DAYS
Practical Examinations (For Lateral Entry)	22.11.2019	26.11.2019	4 DAYS
Betterment Examinations (For Lateral Entry)	27.11.2019	30.11.2019	4 DAYS
II YEAR II SEMESTER		Commencement of Class Work 02.12.2019	
I Spell of Instruction	02.12.2019	10.01.2020	6 WEEKS
Pongal Holidays	11.01.2020	15.01.2020	5 DAYS
Technical/Sports fest	16.01.2020	18.01.2020	3 DAYS
I Spell of Instruction Continuation	20.01.2020	01.02.2020	2 WEEKS
I Mid Examinations	03.02.2020	08.02.2020	1 WEEK
II Spell of Instruction	10.02.2020	04.04.2020	8 WEEKS
II Mid Examinations	06.04.2020	09.04.2020	4 DAYS
Practical Examinations	13.04.2020	17.04.2020	4 DAYS
Betterment Examinations	18.04.2020	22.04.2020	4 DAYS
End Semester Examinations	23.04.2020	08.05.2020	2 WEEKS
Supplementary Examinations	11.05.2020	23.05.2020	2 WEEKS
Commencement of classes will be from 15.06.2020			


DIRECTOR

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Course Schedule

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Course Schedule

Distribution of Contact Hours

Unit	Topic	Book1	Total No. of Hours
I	Introduction to Compilers:	Ch1-1.1-1.6 Ch2-2.1-2.9 Ch3-3.1-3.8	10
II	Syntactic Specification: Basic Parsing Techniques:	Ch4-4.1,4.4	13
III	Construction of efficient Parsers	Ch4-4.5,4.9	11
IV	Syntax Directed Translation Symbol table	Ch5-5.1-5.6 Ch7-7.1-7.6	10
V	Code Optimization: Code Generation	Ch9-9.1-9.9 Ch10-10.1-10.2	10
Total contact classes for syllabus coverage			54

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Lesson Plan

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VIDYA JYOTHI INSTITUTE OF TECHNOLOGY (AUTONOMOUS)

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

SUBJECT: COMPILER DESIGN

ACADEMIC YEAR: 2019-20

NAME: Dr.D.Aruna Kumari
SEM/B

YEAR/SEM/SECTION: III B.TECH/I

Lecture No	Topic	Teaching Learning Process
Unit-I		
1	Introduction to compilers	Chalk & Board
2	Structure of compiler-Phases of Compiler	Chalk & Board
3	Symbol table management, Grouping of phases in to passes	Chalk & Board
4	Compiler vs Interpreter	Power Point Presentation
5	Lexical- Analysis: role and need of LA	Power Point Presentation
6	Input- buffering, Regular expressions for identifiers, Signed numbers etc.	Power Point Presentation
7	A Language for specifying lexical Analyzer, lexical phase errors	Power Point Presentation
8	Top down parsing – Backtracking	Chalk & Board, Interactive learning
9	LL(1).	Chalk & Board
10	Recursive descent parsing.	Chalk & Board
11	Predictive Parsing	Chalk & Board
12	Preprocessing steps required for Predictive Parsing	Chalk & Board
Unit-II		
1	Bottom up parsing	Chalk & Board
2	Shiftreduce parsing	Power Point Presentation

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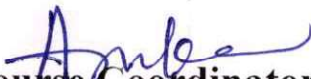
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3	LR	Chalk & Board, Role play
4	LALR parsing	Power Point Presentation
5	Error Recovery in Parsing	Chalk & Board
6	Handling Ambiguous Grammer	Chalk & Board
7	YACC.	Power Point Presentation
8	YACC – automatic parser generator.	Chalk & Board
Unit-III		
1	Semantic analysis	Power Point Presentation
2	Intermediate forms os source programs	Power Point Presentation
3	Abstraact Systax tree ,Postfix notationand Three Address Code	Power Point Presentation
4	Attributed grammars, Syntax directed translation, Type checker.	Power Point Presentation
5	Symbol tables:Symbol table format	Power Point Presentation
6	Organization for block structure languages,	Power Point Presentation, Quiz
7	Hashing and tree structure representation of scope information	Chalk & Board
8	Block structure and non block structure storage allocation: static	Chalk & Board
9	Runtime stack and heap storage allocation	Power Point Presentation
10	storage allocation for arrays, strings and records.	Chalk & Board
Unit-IV		
1	Code optimization:	Demonstrated by PPT

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2	Consideration for optization	Demonstrated by PPT
3	Scope of Optimization	Demonstrated by PPT
4	Local Optimization	Demonstrated by PPT
5	Frequency reduction folding DAG representation	Demonstrated by PPT
6	Data flow analysis	Demonstrated by PPT
7	Flow graph,data flow equeation	Demonstrated by PPT
8	Global optimizatiopn	Demonstrated by PPT
9	Redundent sub expression elimination	Demonstrated by PPT
10	Induction variable elements, live variable analysis, Copy propagation	Demonstrated by PPT
Unit-V		
1	Object code generation	Power Point Presentation
2	Object code forms	Chalk & Board
3	Machine dependent code optimization	Chalk & Board
4	Machine in dependent code optimization	Power Point Presentation
5	Register Allocation and Assignment	Chalk & Board
6	Generic code generation	Power Point Presentation
7	Generic code generation algorithms	Power Point Presentation, PBL
8	DAG for register allocation.	Power Point Presentation

Total No of classes: 54


Course Coordinator


CSE-HOD
Head of the Department
Computer Science and Engineering
VJIT, Hyderabad-50075.

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Assignment Questions

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(AUTONOMOUS)**

COMPUTER SCIENCE & ENGINEERING

ASSIGNMENT 1

Branch: CSE

Year&Sem: III-I

SUB: Compiler Design

Academic Year: 2019-20

Faculty Name: Dr.D.Aruna Kumari

Marks: 25M

S.No	Question	Marks	CO	BL	PO's
1	Explain what are the different phases of Compiler?	5	1	L2	1-10,12
2	What is pass & phases? Explain the differences between them.	5	1	L1,L2	1-10,12
3	What is Predictive parsing? Write the steps of predictive parser.	5	2	L1	1-10,12
4	What is Recursive descent parsing? Identify the limitations of recursive descent parser?	5	2	L1	1-10,12
5	What is Shift Reduce parsing? Explain with an Example.	5	3	L1,L2	1-6,8-12

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COMPUTER SCIENCE & ENGINEERING

ASSIGNMENT 2

Branch: CSE

Year&Sem: III-I

SUB: Compiler Design

Academic Year: 2019-20

Faculty Name: Dr.D.Aruna Kumari

Marks: 25M

S.No	Question	Marks	CO	BL	PO's
1	What is ordered and unordered symbol table? What is the function of symbol table in the compilation process? Explain.	5	3	L1	1-6,8-12
2	Discuss about stack allocation, heap allocation. Explain. Discuss various symbol table organization techniques.	5	3	L2	1-6,8-12
3	Explain different principle sources of optimization techniques with suitable examples.	5	4	L2	1-6,8-12
4	Explain various loop optimization techniques? Explain dominators with n Examples.	5	4	L2	1-6,8-12
5	Describe reducible & non reducible flow graphs with a example.	5	5	L2	1-12

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QUESTION PAPERS MID-I & II

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III Year B.Tech II Semester I Mid Examination

Branch: CSE
Sub: Compiler Design
Date: 18.02.2020

Duration: 90Min
Marks: 20
Session: FN

Bloom's Level:

Remember	L1
Understand	L2
Apply	L3
Analyze	L4
Evaluate	L5
Create	L6

PART-A (3Q×2M =6 Marks)		Outcomes		BL	Marks
ANSWER ALL THE QUESTIONS		CO	PO		
1.i)	List out the difference between Compiler and interpreter.	1	1-10,12	IV	2
[OR]					
ii)	Write the new grammar after eliminating left recursion for $E \rightarrow E+E E*E id$	1	1-10,12	II	2
2.i)	What is YACC? Explain in detail.	2	1-10,12	II	2
[OR]					
ii)	What is an augmented grammar? Describe with an example.	2	1-10,12	II	2
3.i)	What are the benefits of intermediate code generation?	3	1-6,8-12	I	2
[OR]					
ii)	Define Polish Notation.	3	1-6,8-12	I	2
PART-B (5+5+4= 14 Marks)		Outcomes		BL	Marks

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ANSWER ALL THE QUESTIONS		CO	PO		
4.i)	Explain the different phases of a compiler, showing the output of each phase using the following statement. Position:=Initial+rate*60.	1	1-10,12	II	5
[OR]					
ii)	Construct the predictive parser table for the following grammar and parse the input string id+id*id. $E \rightarrow TE'$ $E' \rightarrow +TE' \mid \epsilon$ $T \rightarrow FT'$ $T' \rightarrow *FT' \mid \epsilon$ $F \rightarrow (E) \mid id$	1	1-10,12	IV	5
5.i)	Construct the CLR parsing table for the following grammar and parse. $S \rightarrow CC$ $C \rightarrow aC$ $C \rightarrow d$	2	1-10,12	IV	5
[OR]					
ii.a)	With neat sketch explain the structure of LR parser and the rules to compute LR item.	2	1-10,12	II	3
b)	Consider the following grammar: $E \rightarrow E+E$ $E \rightarrow E * E$ $E \rightarrow (E)$ $E \rightarrow id$ Show the shift-reduce parser action for the string: id+id*id	2	1-10,12	III	2
6.i)	Explain Quadruples, triples and indirect triples and translate $A := -B*(C/D)$	3	1-6,8-12	II	4
[OR]					
ii)	Explain Intermediate code forms in detail.	3	1-6,8-12	II	4

VJIT(A)



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III B.Tech II Semester II Mid Examination, October/November-2020

Subject : Compiler Design

Time: 1 Hour

Bloom's Level:

BRANCH: CSE

Max Marks:20

Remember	L1
Understand	L2
Apply	L3
Analyze	L4
Evaluate	L5
Create	L6

PART-A		Course Outcomes		Bloom's Level	Marks
ANSWER ALL THE QUESTIONS		CO	P O		
1.a)	What is symbol table? List the various data structures that can be used to organize a symbol table.	CO ₃	1-10,12	L1	6
[OR]					
b)	Explain Heap storage allocation with an example.	CO ₅	1-10,12	L2	6
PART-B		Course Outcomes		Bloom's Level	Marks
ANSWER ANY TWO QUESTIONS		CO	P O		
2)	What are the different loop optimization methods? Explain them with examples.	CO ₃	1-10,12	L2	7
3)	What are the different local optimization methods? Explain them with examples.	CO ₄	1-10,12	L2	7
4)	Explain in detail about DAG for Register Allocation.	CO ₄	1-10,12	L1	7

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5)	Generate code for the following expression: $x = (a+b)-((c-d)-e)$.	CO5	1-10,12	L4	7
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Unit Wise Questions

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Aziz Nagar Gate, Hyderabad-75

III YEAR B.TECH CSE- II SEM

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UNIT : I

Long Answers Type Questions: (5 questions):

1. Analyze the different phases of a compiler, showing the output of each phase using the following statement. $\text{Position} = \text{initial} + \text{rate} * 60$.
2. Explain about Language Processing System
3. Explain about role and need of lexical analyzer
4. Explain lexical errors with an example
5. Write the regular expression for the following
 - a) For Identifiers
 - b) For unsigned numbers

Short Answer Questions:

1. What is Compiler?
2. Define the role of Loader
3. List out the differences between Compiler and interpreter.
4. Identify the tokens from the statement $E = M + C * 60$
5. Explain symbol table management.
6. What is error handler?
7. Analyze the need of Cross compile.
8. What is Pass ?
9. Explain about passes.
10. What is the role of preprocessor?
11. Give the complete specification of LEX tool and describe various section of it.
12. Write a LEX program to perform lexical analysis.
13. Explain role of lexical analyzer
14. What are Front end Phases and Back End Phases
15. Define Lexeme.
16. Define pattern.
17. What is Regular Expression?
18. Explain regular expressions for identifiers.
19. Write the Regular Expression for numbers?
20. Define the role of Linker

UNIT : II

Long Answers Type Questions: (5 questions):

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1. What is context free grammar? With example.
2. Explain about
 - a. Parse tree
 - b. semantic error
 - c. syntactic error
3. Write are the preprocessing steps required for predictive parse table construction.
4. Construct the predictive parser for the following grammar.
 $S \rightarrow (L) / a$
 $L \rightarrow L, S / S$
5. Construct the predictive parser table for the following grammar and parse the input string $id+id*id$

$$E \rightarrow TE' \quad E' \rightarrow +TE' \mid \epsilon \quad T \rightarrow FT' \quad T' \rightarrow *FT' \mid \epsilon \quad F \rightarrow (E) \mid id$$

Short Answer Questions:

1. What is Context Free Grammar?
2. What is parse tree?
3. Explain Capabilities of Context Free Grammar.
4. What are syntactic phase errors?
5. What is top down parser?
6. What is Recursive descent parser?
7. What is Non Recursive descent parser?
8. What is Back-tracking?
9. What are semantic errors?
10. What do you mean by LL(1) grammar? Give example.
11. What are the problems in top down parsing?
12. Write the algorithm for computing FIRST function.
13. Write the algorithm for computing FOLLOW function.
14. Compute the FIRST and FOLLOW functions for the following grammar.
 $E \rightarrow TE' \quad E' \rightarrow +TE' \mid \epsilon \quad T \rightarrow FT' \quad T' \rightarrow *FT' \mid \epsilon \quad F \rightarrow (E) \mid id$
15. Draw and explain the block diagram for predictive parser.
16. Consider the grammar $s \rightarrow (L) \mid a, L \rightarrow L, S \mid S$
 - a. What are the terminals, non – terminals and start symbol?
 - b. Find the parse tree for (a, a) .
17. Consider the grammar $s \rightarrow (L) \mid a, L \rightarrow L, S \mid S$
 - a. Construct left most derivation for $(a, (a, a))$.
 - b. Construct right most derivation for $(a, (a, a))$.
18. Derive the string “aaabbabbba” using the following grammar.
 $S \rightarrow aB \mid bA, A \rightarrow a \mid aS \mid bAA, B \rightarrow b \mid bS \mid aBB$
19. What is LL(1) grammar?
20. What are basic parsing techniques?

UNIT : III

Long Answers Type Questions: (5 questions):

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1. With neat sketch explain the structure of LR parser and the rules to compute LR item.
2. Consider the following grammar:

$E \rightarrow E+E$

$E \rightarrow E * E$

$E \rightarrow (E)$

$E \rightarrow id$

Show the shift-reduce parser action for the string: **id*(id+id).**

3. Construct the SLR parsing table for the following grammar and parse the input string "a*b+a"

$E \rightarrow E+T \mid T \quad T \rightarrow TF \mid F \quad F \rightarrow F * [a|b]$

4. a) Write the steps for the efficient construction of LALR parsing table.
b) Construct the LALR parsing table for the following grammar.

$S \rightarrow L=R \mid R \quad L \rightarrow *R \mid id \quad R \rightarrow L$

5. List out the differences between SLR, CLR and LALR.

Short Answer Questions:

1. What is bottom Up Parser?
2. What Shift Reduce Parser?
3. Explain about LR Parsers?
4. Write the steps of canonical collection of LR(0) items.
5. Differentiate between Top-down and Bottom-up parsers.
6. List out the differences between SLR, CLR and LALR.
7. What is Handle pruning?
8. Explain the various actions performed by shift-reduce parsers with an example.
9. Write down the advantages and disadvantages of LR parsers.
10. Draw and explain the block diagram of LR parser.
11. Write an algorithm to find LR(0) items.
12. List out the steps for construction of SLR parsing table.
13. List out the steps for construction of CLR parsing table.
14. List out the steps for construction of LALR parsing table.
15. Perform the shift reduce parsing of the input string "id-id*id" using the following grammar.
 $E \rightarrow E-E \mid E * E \mid id$
16. Perform the shift reduce parsing of the input string "**intid,id;**" using the following grammar.
 $S \rightarrow TL; \quad T \rightarrow int \mid float \quad L \rightarrow L, id \mid id$
17. Differentiate between LL and LR parsers.
18. Construct LR(1) parsing table for the following grammar.
 $S \rightarrow CC \quad C \rightarrow aC \mid d$
19. What are the various conflicts that occur during shift reduce parsers?
20. Consider the grammar
 $S \rightarrow S + S$
 $S \rightarrow S * S$
 $S \rightarrow id$
Perform Shift Reduce parsing for input string "id + id + id".

UNIT : IV

Long Answers Type Questions: (5 questions):

Long Answers Type Questions: (5 questions):

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1. a) What is an Abstract syntax tree? How to construct it? Explain by writing syntax directed definition.
2. Explain about Quadruples, triples and indirect triples and translate $(a+b)*(c+d)+(a*b/c)*b+60$. And list advantages and disadvantages.
3. a) Can we reuse the symbol table space? Explain through an example.
b) Write short notes on functions of semantic analysis.
4. What is symbol table? List the various data structures that can be used to organize a symbol table? Compare the performance.
5. What is runtime stack? Explain storage allocation strategies used for recursive procedure calls.

Short Answer Questions:

1. What Syntax Directed Translation?
2. What is semantic rule?
3. Write the semantic rules for DTL, T int|real, L L,id|id.
4. Write about order of evaluation of semantic rules in syntax directed translation.
5. Write about dependency graphs in syntax directed translations.
6. What are the benefits of intermediate code generation?
7. How to generate polish notation using translation schemes?
8. Write the quadruple, triple, indirect triple for the expression $-(a*b) + (c+d)-(a+b+c+d)$.
9. Write an algorithm for constructing the dependency graph for a given parse tree.
10. Analyse the different types of three address statements.
11. Explain L-attributed syntax directed translation.
12. Explain S-attributed syntax directed translation.
13. Construct the syntax tree for the expression $(a*b) + (c-d) * (a*b) + b$.
14. Differentiate synthesis and inherited translation.
15. Explain any two storage allocation strategies.
16. What is an intermediate code? Explain different types of intermediate codes forms and represent the following statement in different forms: $W = (A + B) - (C + D) + (A + B + C)$.
17. What is an activation record? Explain how it is related with run time storage organization?
18. Describe about type expressions.
19. What are the various operations performed on the symbol table?
20. Explain about Representing Scope information.

UNIT : V

Long Answers Type Questions: (5 questions):

1. Explain different principal sources of optimization technique with suitable examples?
2. What are the different loop optimization methods? Explain them with examples.
3. What are the issues to be considered during code generation?
4. Explain about Peephole optimization techniques.
5. Explain in detail about Register Allocation and Assignment?

Short Answer Questions:

1. Mention the issues to be considered while applying the techniques for code optimization.
2. List the properties of optimizing compilers.
3. Explain how code motion and frequency reduction used for loop optimizations?
4. Write about the techniques in local and global transformations.

5. What is copy propagation and dead code elimination?
6. Explain Redundant Sub Expression Elimination?
7. Discuss the role of strength reduction during code optimization of a compiler.
8. Give the criteria for achieving machine independent code optimization.
9. Compare local optimization with global optimization.
10. Explain the following a) Copy propagation b) constant folding.
11. Explain about various levels and types of optimizations.
12. Give the criteria for achieving machine dependent code optimization.
13. What is code motion?
14. What is induction variable?
15. What is global optimization?
16. Explain a Simple Code generator?
17. What is Register Allocation and Assignment?
18. Short note on Peephole optimization.
19. Explain about Object Programs.
20. What are the Problems in Code Generation?

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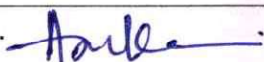
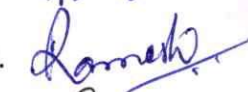
Minutes of Course Review Meeting

COMPILER DESIGN

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Meeting 1

Date: 21/1/2020

Details of Meeting No – 1	
Date of Meeting	17/5/2021
Member's Present	1. Dr.Aruna kumari 2. Dr.Ramesh babu 3. M.Vijaya 4.Zaheer
Details	Points discussed in the meeting: • Preparation of Unit wise questions and give assignment to students • Teaching Learning Practices • Status of Syllabus coverage of Mid I.
Signatures	1.  2.  3.  4. 


Course Coordinator

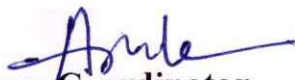
COMPILER DESIGN


CSE-HOD
Head of the Department
Computer Science and Engineering
VJIT, Hyderabad-50075.

Meeting 2

Date: 08.10.2020

Details of Meeting No – 2 (online meeting)	
Date of Meeting	24/7/2021
Member's Present	1. Dr.Aruna kumari 2. Dr.Ramesh babu 3. M.Vijaya 4.Zaheer
Details	Discussion on Teaching Learning resource Preparation for Mid 2
Signatures	1.  2.  3.  4. 


Course Coordinator


CSE-HOD
Head of the Department
Computer Science and Engineering
VJIT, Hyderabad-50075.

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

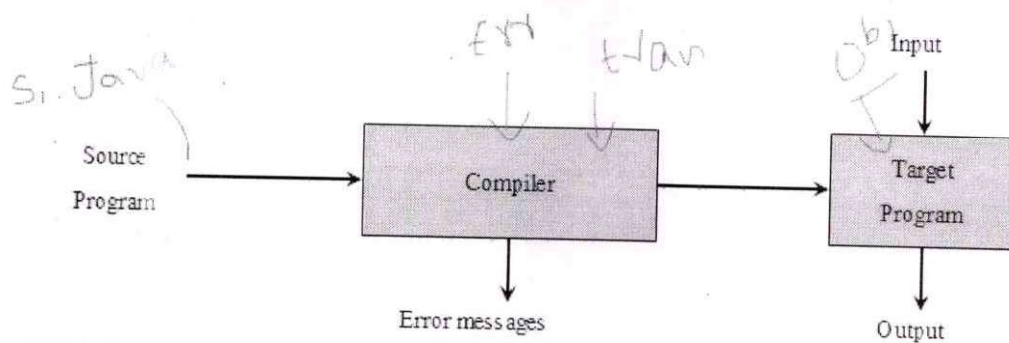
Power Point Presentation

COMPILER DESIGN

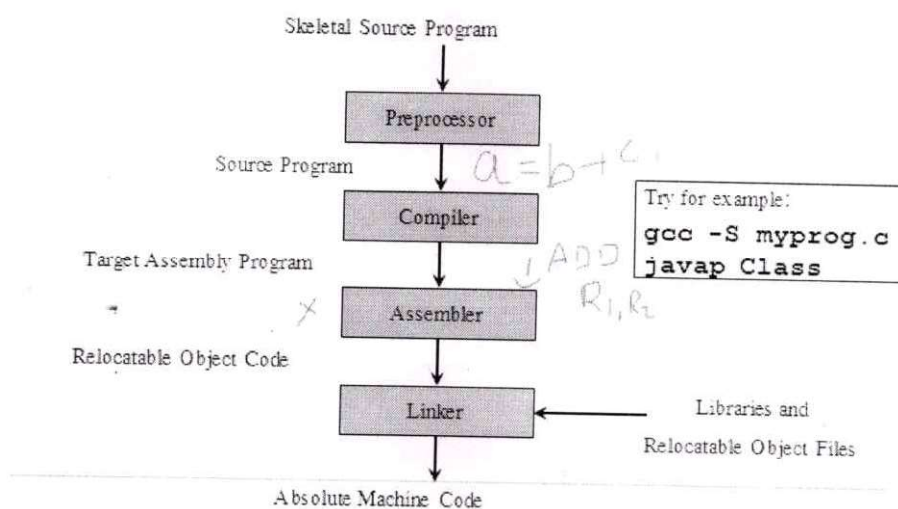
Compilers

■ "Compilation"

- Translation of a program written in a source language into a semantically equivalent program written in a target language



Preprocessors, Compilers, Assemblers, and Linkers--.. Language Processing System



Phases

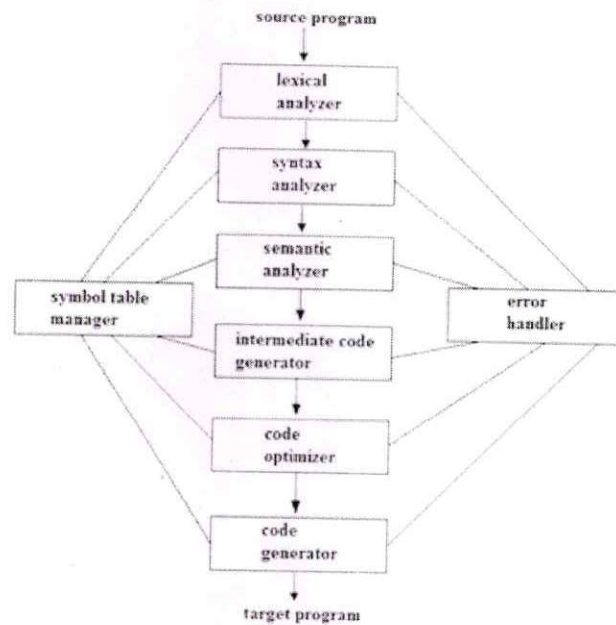
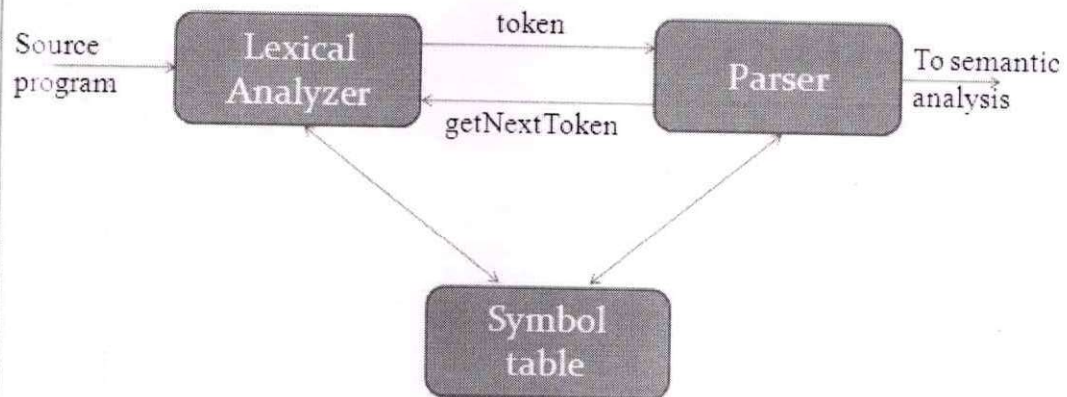


Fig 1.5 Phases of a compiler

Exercises

1. Identify the Differences Between Compiler and Interpreter
2. Identify the token from the following statements
$$\text{sum} = \text{c+icc} / 79$$
3. List the components of the Language Processing systems

The role of lexical analyzer



Example

Token	Informal description	Sample lexemes
if	Characters i, f	if
else	Characters e, l, s, e	else
comparison	< or > or <= or >= or == or !=	<=, !=
id	Letter followed by letter and digits	pi, score, D2
number	Any numeric constant	3.14159, 0, 6.02e23
literal	Anything but " surrounded by "	"core dumped"

```
printf("total = %d\n", score);
```

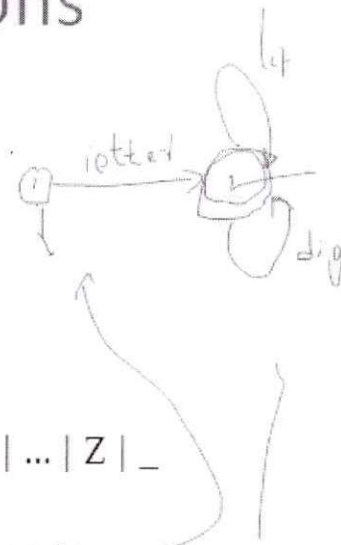
Regular definitions

$d_1 \rightarrow r_1$

$d_2 \rightarrow r_2$

...

$d_n \rightarrow r_n$



Example:

$\text{letter_} \rightarrow A | B | \dots | Z | a | b | \dots | z | _$

$\text{digit} \rightarrow 0 | 1 | \dots | 9$

$\text{id} \rightarrow \text{letter_} (\text{letter_} | \text{digit})^*$

Lexical Analyzer Generator - Lex

Lex Source program
lex.l

Lexical
Compiler

lex.yy.c

lex.yy.c

C
compiler

a.out

Input stream

A.out

Sequence
of tokens

Ambiguous grammar

- For some input string, if there exists more than one LMD or RMD, Grammar is called as Ambiguous

LMD 1

■ $E \rightarrow \underline{E} + E \quad \rightarrow r3$

□ $Id + \underline{E} \Rightarrow r2$

□ $Id + \underline{E} * E \Rightarrow r3$

□ $Id + id * \underline{E} \Rightarrow r3$

■ $Id + id * id$

LMD 2

$E \rightarrow E * E$

$E \rightarrow E + E * E$

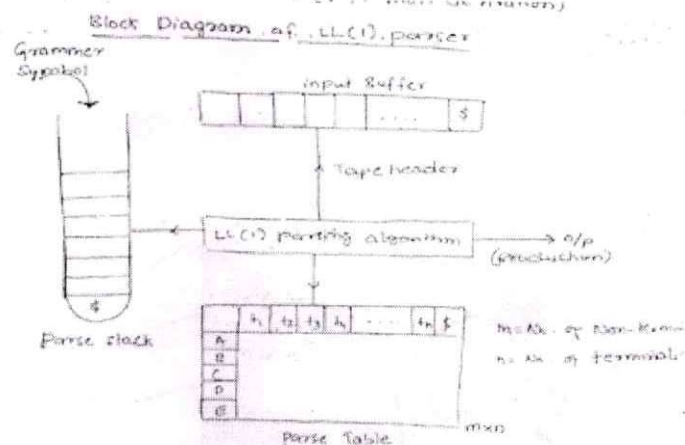
$E \rightarrow id + E * E$

$E \rightarrow id + id * E$

$E \rightarrow id + id * id$

LL (1) Parser or Table Driven Parser

LL(1) $\uparrow$
 ↳ No. of symbol taken into count for parsing
 ↳ Left to Right
 ↳ LMD (Left most derivation)



Example 4.8. Consider the following grammar for arithmetic expressions.

$E \rightarrow E + T \mid T$
 $T \rightarrow T * F \mid F$
 $F \rightarrow (E) \mid id$

(4.10)

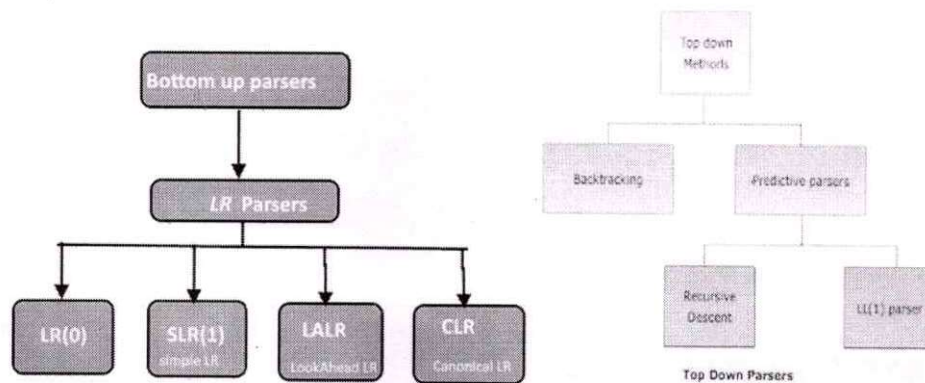
$E \rightarrow TE'$
 $E' \rightarrow +TE' \mid \epsilon$
 $T \rightarrow FT'$
 $T' \rightarrow *FT' \mid \epsilon$
 $F \rightarrow (E) \mid id$

First(α) is a set of terminal symbols that begin in strings derived from α .

Follow(α) is a set of terminal symbols that appear immediately to the right of α .

$\text{FIRST}(E) = \text{FIRST}(T) = \text{FIRST}(F) = \{ (, id \}$
 $\text{FIRST}(E') = \{ +, \epsilon \}$
 $\text{FIRST}(T') = \{ *, \epsilon \}$
 $\text{FOLLOW}(E) = \text{FOLLOW}(E') = \{), \$ \}$
 $\text{FOLLOW}(T) = \text{FOLLOW}(T') = \{ +,), \$ \}$
 $\text{FOLLOW}(F) = \{ +, *,), \$ \}$

Classification



LL Parser	LR Parser
First L of LL is for left to right and second L is for leftmost derivation.	L of LR is for left to right and R is for rightmost derivation.
It follows the left most derivation.	It follows reverse of right most derivation.
Using LL parser parser tree is constructed in top down manner.	Parser tree is constructed in bottom up manner.
In LL parser, non-terminals are expanded.	In LR parser, terminals are compressed.
Starts with the start symbol(S).	Ends with start symbol(S).
Ends when stack used becomes empty.	Starts with an empty stack.
Pre-order traversal of the parse tree.	Post-order traversal of the parser tree.
Terminal is read after popping out of stack.	Terminal is read before pushing into the stack.
It may use backtracking or dynamic programming.	It uses dynamic programming.
LL is easier to write.	LR is difficult to write.
Example: LL(0), LL(1)	Example: LR(0), SLR(1), LALR(1), CLR(1)

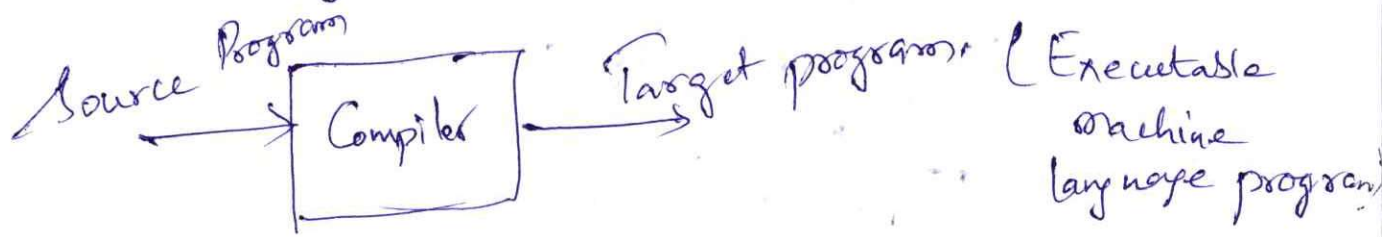
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Lecture Notes

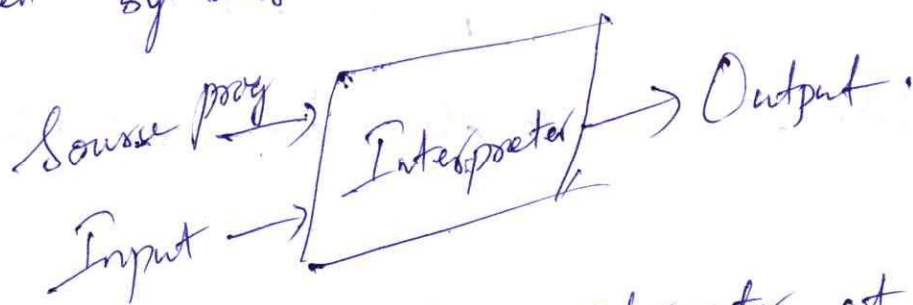
COMPILER DESIGN

Unit - 1

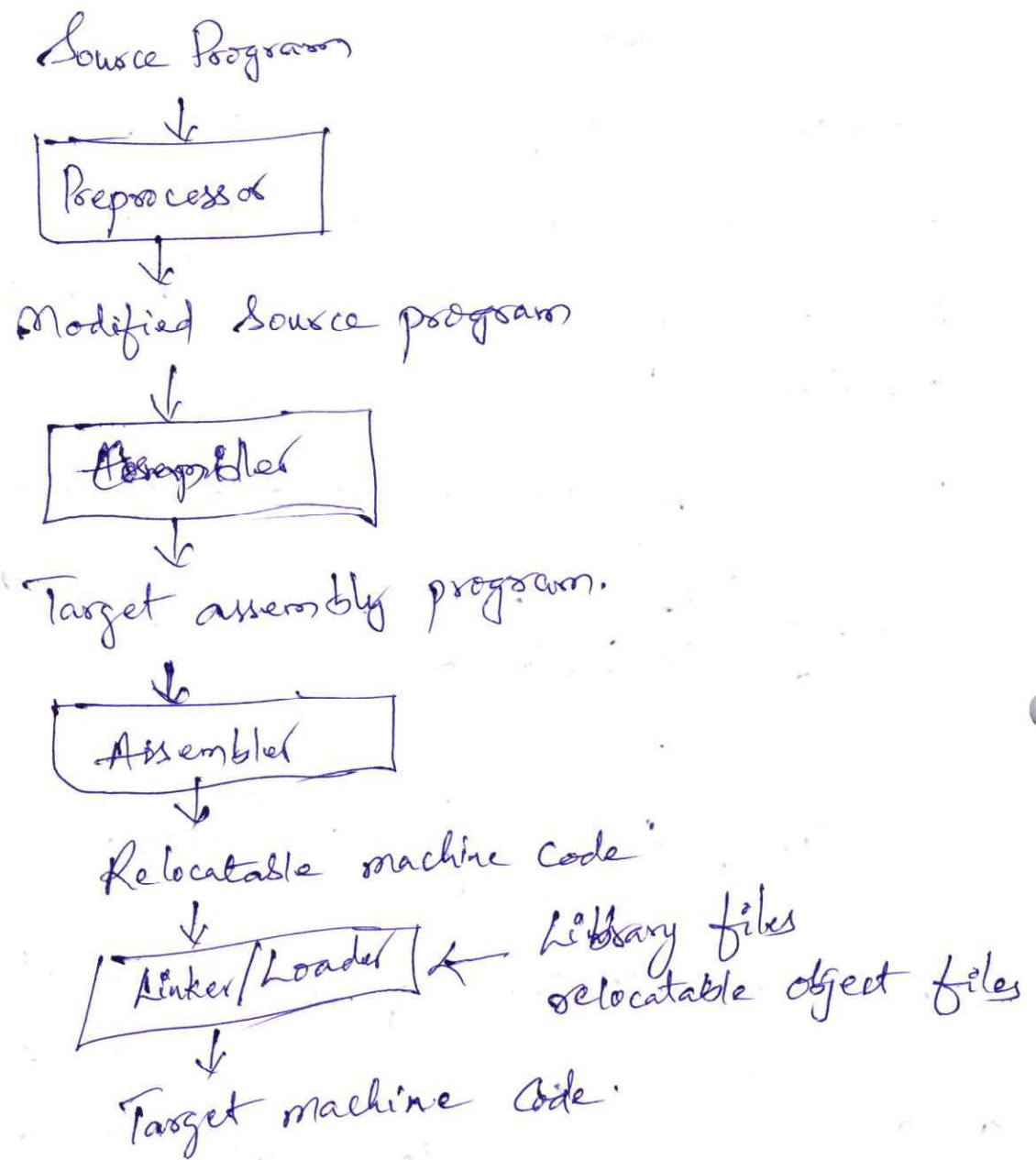
- Compiler is a program that can read a program in one language - the source lang. - and translate it into an equivalent program in another language - the target language.
- To report any errors if any detected during translation.



- Interpreter: Instead of producing a target program as a translation; an interpreter directly executes the operations specified in source program on inputs supplied by user.



- Compiler is faster than interpreter at mapping i/p's to o/p's.
- However interpreter gives better

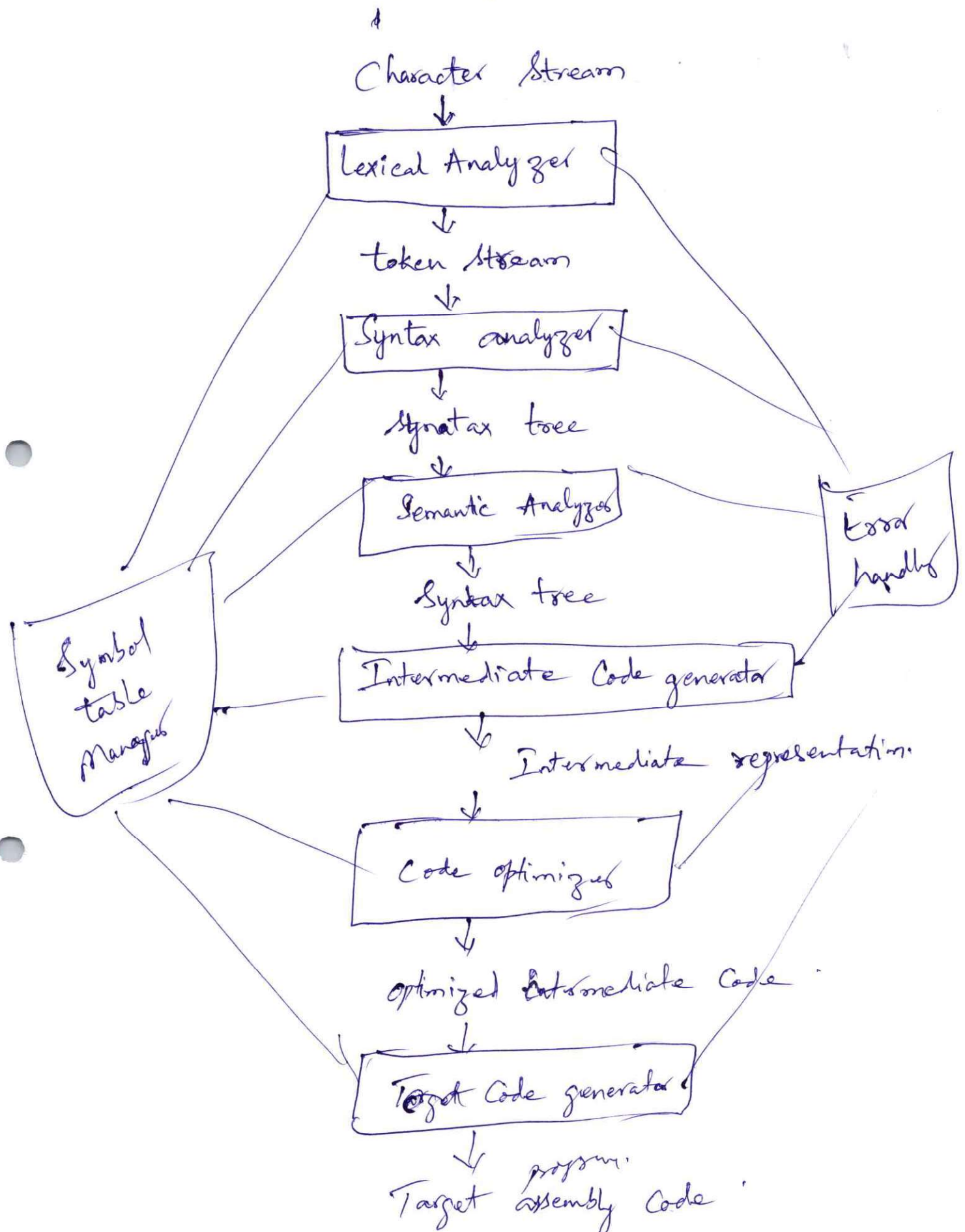


→ Preprocessor directives

- # include
- # define
- Macro expansion

→ Linker links relocatable object files and library files.

Phases of Compiler



61, 68, 75, 76, 79, 80, 83, 86, 88, 93, 94, 95, 97, 98

A4, A7, ^{A9}130, 133, 136, 137,

UNIT -1

1.1 OVERVIEW OF LANGUAGE PROCESSING SYSTEM

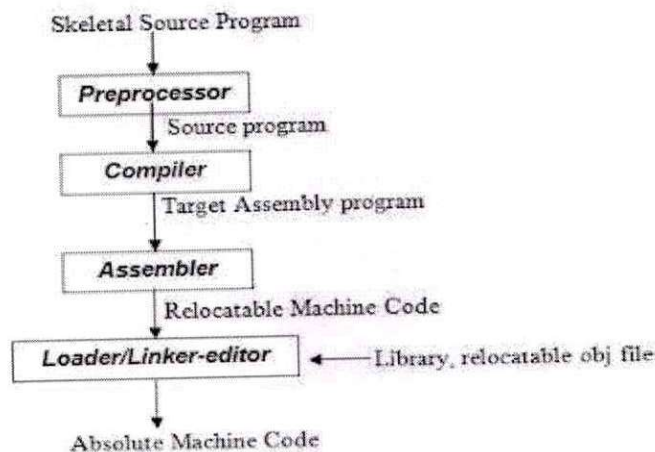


Fig 1.1 Language -processing System

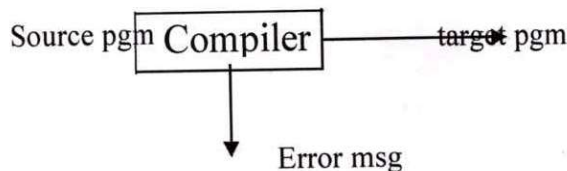
1.2 Preprocessor

A preprocessor produce input to compilers. They may perform the following functions.

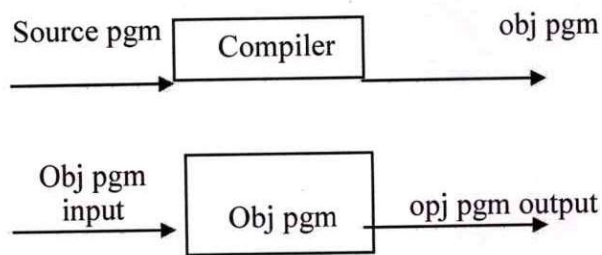
1. *Macro processing*: A preprocessor may allow a user to define macros that are short hands for longer constructs.
2. *File inclusion*: A preprocessor may include header files into the program text.
3. *Rational preprocessor*: these preprocessors augment older languages with more modern flow-of-control and data structuring facilities.
4. *Language Extensions*: These preprocessor attempts to add capabilities to the language by certain amounts to build-in macro

1.3 COMPILER

Compiler is a translator program that translates a program written in (HLL) the source program and translate it into an equivalent program in (MLL) the target program. As an important part of a compiler is error showing to the programmer.

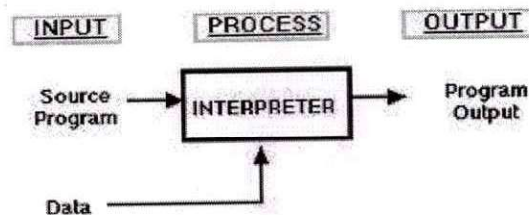


Executing a program written in HLL programming language is basically of two parts. the source program must first be compiled/translated into an object program. Then the resulting object program is loaded into memory and executed.



1.4 ASSEMBLER: programmers found it difficult to write or read programs in machine language. They begin to use a mnemonic (symbols) for each machine instruction, which they would subsequently translate into machine language. Such a mnemonic machine language is now called an assembly language. Programs known as assemblers were written to automate the translation of assembly language into machine language. The input to an assembler program is called source program, the output is a machine language translation (object program).

1.5 INTERPRETER: An interpreter is a program that appears to execute a source program as if it were machine language.



Languages such as BASIC, SNOBOL, LISP can be translated using interpreters. JAVA also uses interpreter. The process of interpretation can be carried out in following phases.

1. Lexical analysis
2. Syntax analysis
3. Semantic analysis
4. Direct Execution

Advantages:

- Modification of user program can be easily made and implemented as execution proceeds.
- Type of object that denotes a various may change dynamically.
- Debugging a program and finding errors is simplified task for a program used for interpretation.
- The interpreter for the language makes it machine independent.

Disadvantages:

- The execution of the program is *slower*.
- *Memory* consumption is more.

performs another very important role, World the error-detection. Any violation of HLL specification would be detected and reported to the programmers. Important role of translator are:

2 Loader and Link-editor:

Once the assembler produces an object program, that program must be placed into memory and executed. The assembler could place the object program directly in memory and transfer control to it, thereby causing the machine language program to be executed. This would waste core by leaving the assembler in memory while the user's program was being executed. Also the programmer would have to retranslate his program with each execution, thus wasting translation time. To overcome this problems of wasted translation time and memory. System programmers developed another component called loader

"A loader is a program that places programs into memory and prepares them for execution." It would be more efficient if subroutines could be translated into object form the loader could "relocate" directly behind the user's program. The task of adjusting programs so they may be placed in arbitrary core locations is called relocation. Relocation loaders perform four functions.

1.6 TRANSLATOR

A translator is a program that takes as input a program written in one language and produces as output a program in another language. Beside program translation, the translator

- 1 Translating the hll program input into an equivalent ml program.
- 2 Providing diagnostic messages wherever the programmer violates specification of the hll.

1.7 TYPE OF TRANSLATORS:-

- ✦ INTERPRETOR
- ✦ COMPILER
- ✦ PREPROCESSOR

1.8 LIST OF COMPILERS

1. Ada compilers
- 2 .ALGOL compilers
- 3 .BASIC compilers
- 4 .C# compilers
- 5 .C compilers
- 6 .C++ compilers
- 7 .COBOL compilers
- 8 .D compilers
- 9 .Common Lisp compilers
10. ECMAScript interpreters
11. Eiffel compilers
12. Felix compilers
13. Fortran compilers
14. Haskell compilers
- 15 .Java compilers
16. Pascal compilers
17. PL/I compilers
18. Python compilers
19. Scheme compilers
20. Smalltalk compilers
21. CIL compilers

1.9 STRUCTURE OF THE COMPILER DESIGN

Phases of a compiler: A compiler operates in phases. A phase is a logically interrelated operation that takes source program in one representation and produces output in another representation. The phases of a compiler are shown in below There are two phases of compilation.

- a. Analysis (Machine Independent/Language Dependent)
- b. Synthesis(Machine Dependent/Language independent)

Compilation process is partitioned into no-of-sub processes called '**phases**'.

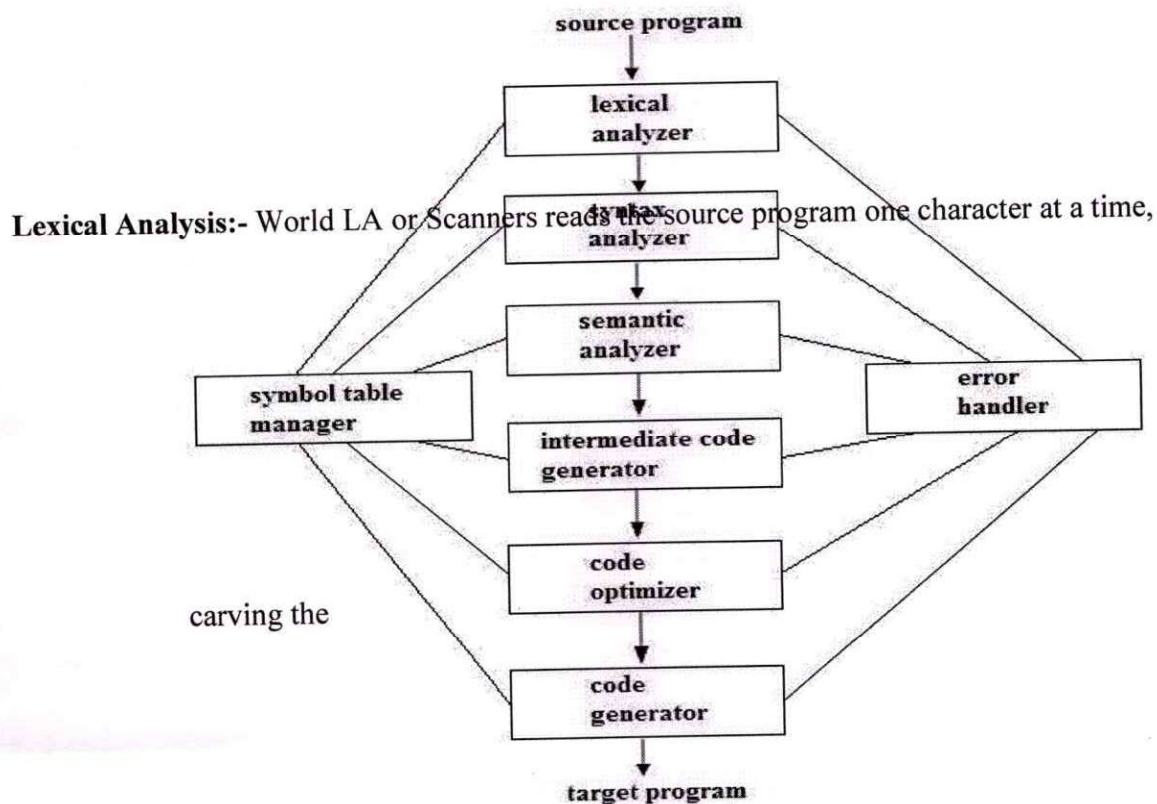


Fig 1.5 Phases of a compiler

source program into a sequence of automic units called **tokens**.

Syntax Analysis:-

The second stage of translation is called Syntax analysis parsing. In this phase expressions, statements, declarations etc... are identified by using the results of lexical analysis. Syntax analysis is aided by using techniques based on formal grammar of the programming language.

Intermediate Code Generations:-

An intermediate representation of the final machine language code is produced. This phase bridges the analysis and synthesis phases of translation.

Code Optimization :-

This is optional phase described to improve the intermediate code so that the output runs faster and takes less space.

Code Generation:-

The last phase of translation is code generation. A number of optimizations to **reduce the length of machine language program** are carried out during this phase. The output of the code generator is the machine language program of the specified computer.

Table Management (or) Book-keeping:-

This is the portion to **keep the names** used by the program and records essential information about each. The data structure used to record this information called a 'Symbol Table'.

Error Handlers:-

It is invoked when a flaw error in the source program is detected.

The output of LA is a stream of tokens, which is passed to the next phase, the syntax analyzer or parser. The SA groups the tokens together into syntactic structure called as **expression**. Expression may further be combined to form statements. The syntactic structure can be regarded as a tree whose leaves are the tokens called as parse trees.

The parser has two functions. It checks if the tokens from lexical analyzer, occur in pattern that are permitted by the specification for the source language. It also imposes on tokens a tree-like structure that is used by the sub-sequent phases of the compiler.

Example, if a program contains the expression $A+B$ after lexical analysis this expression might appear to the syntax analyzer as the token sequence $id+id$. On seeing the $/$, the syntax analyzer should detect an error situation, because the presence of these two adjacent binary operators violates the formulations rule for an expression.

Syntax analysis is to make explicit the hierarchical structure of the incoming token stream by **identifying which parts of the token stream should be grouped**.

Example, $(A/B)*C$ has two possible interpretations.)

- 1, divide A by B and then multiply by C or
- 2, multiply B by C and then use the result to divide A.

each of these two interpretations can be represented in terms of a parse tree.

Intermediate Code Generation:-

the intermediate code generation uses the structure produced by the syntax analyzer to create a stream of simple instructions. Many styles of intermediate code are possible. One common style uses instruction with one operator and a small number of operands.

the output of the syntax analyzer is some representation of a parse tree. the intermediate code generation phase transforms this parse tree into an intermediate language representation of the source program.

Code Optimization

This is optional phase described to improve the intermediate code so that the output runs faster and takes less space. Its output is another intermediate code program that does the same job as the original, but in a way that saves time and / or spaces.

- 1, Local Optimization:-

There are local transformations that can be applied to a program to make an improvement. For example,

If $A > B$ goto L2

Goto L3

L2 :

This can be replaced by a single statement

If A < B goto L3

Another important local optimization is the elimination of common sub-expressions

World

A := B + C + D

E := B + C + F

Might be evaluated as

T1 := B + C

A := T1 + D

E := T1 + F

Take this advantage of the common sub-expressions B + C.

2, Loop Optimization:-

Another important source of optimization concerns about **increasing the speed of loops**. A typical improvement is to move a computation that produces the same result each time around the loop to a point, in the program just before the loop is entered.

Code generator :-

Cg produces the object code by deciding on the memory locations for data, selecting code to access each datum and selecting the registers in which each computation is to be done. Many computers have only a few high speed registers in which computations can be performed quickly. A good code generator would attempt to utilize registers as efficiently as possible.

Table Management OR Book -keeping :-

A compiler needs to collect information about all the data objects that appear in the source program. The information about data objects is collected by the early phases of the compiler-lexical and syntactic analyzers. The data structure used to record this information is called as Symbol table.

Error Handling :-

One of the most important functions of a compiler is the detection and reporting of errors in the source program. The error message should allow the programmer to determine exactly where the errors have occurred. Errors may occur in all or the phases of a compiler.

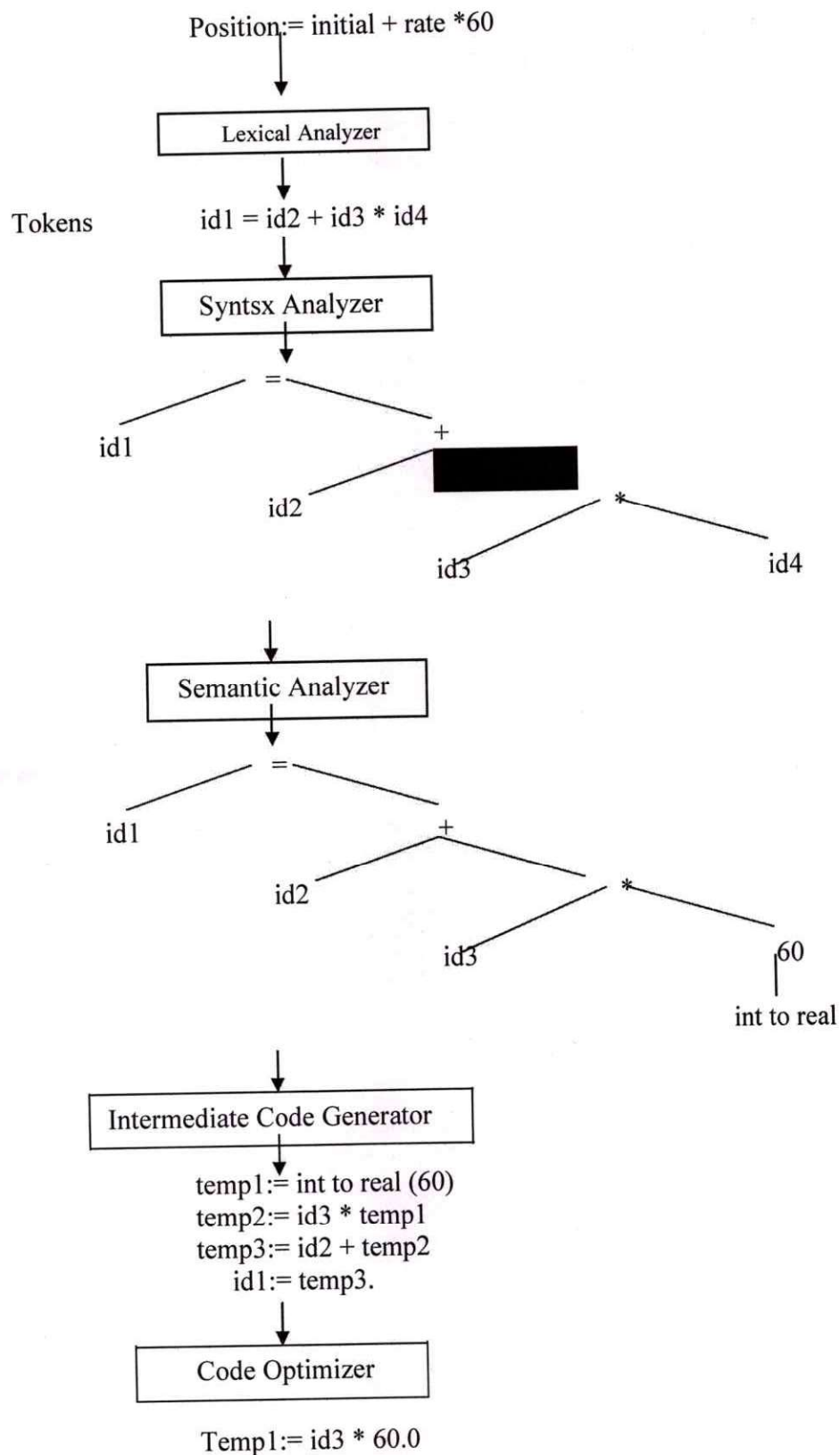
Whenever a phase of the compiler discovers an error, it must report the error to the error handler, which issues an appropriate diagnostic msg. Both of the table-management and error-handling routines interact with all phases of the compiler.

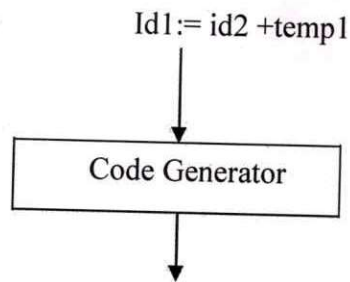
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1870-1871
1871

1872-1873
1873-1874
1874-1875

1875-1876
1876-1877
1877-1878
1878-1879
1879-1880

Example:





```

MOVF id3, r2
MULF *60.0, r2
MOVF id2, r2
ADDF r2, r1
MOVF r1, id1
  
```

1.10 TOKEN

LA reads the source program one character at a time, converting the source program into a sequence of automatic units called 'Tokens'.

- 1, Type of the token.
- 2, Value of the token.

Type : variable, operator, keyword, constant

Value : Name of variable, current variable (or) pointer to symbol table.

If the symbols given in the standard format the LA accepts and produces token as output. Each token is a sub-string of the program that is to be treated as a single unit. Tokens are two types.

- 1, Specific strings such as IF (or) semicolon.
- 2, Classes of string such as identifiers, label, constants.

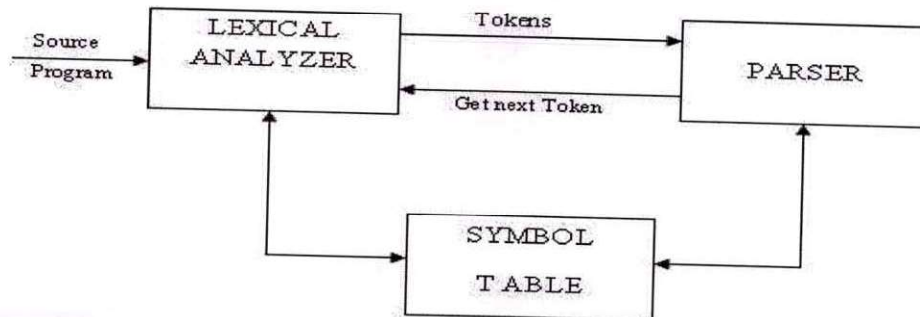
LEXICAL ANALYSIS

OVER VIEW OF LEXICAL ANALYSIS

- To identify the tokens we need some method of describing the possible tokens that can appear in the input stream. For this purpose we introduce regular expression, a notation that can be used to describe essentially all the tokens of programming language.
- Secondly, having decided what the tokens are, we need some mechanism to recognize these in the input stream. This is done by the token recognizers, which are designed using transition diagrams and finite automata.

ROLE OF LEXICAL ANALYZER

the LA is the first phase of a compiler. Its main task is to read the input character and produce as output a sequence of tokens that the parser uses for syntax analysis.



Upon receiving a 'get next token' command from the parser, the lexical analyzer reads the input character until it can identify the next token. The LA returns to the parser representation for the token it has found. The representation will be an integer code, if the token is a simple construct such as parenthesis, comma or colon.

LA may also perform certain secondary tasks as the user interface. One such task is stripping out from the source program the commands and white spaces in the form of blank, tab and new line characters. Another is correlating error message from the compiler with the source program.

LEXICAL ANALYSIS VS PARSING:

Lexical analysis	Parsing
A Scanner simply turns an input String (say a file) into a list of tokens. These tokens represent things like identifiers, parentheses, operators etc.	A parser converts this list of tokens into a Tree-like object to represent how the tokens fit together to form a cohesive whole (sometimes referred to as a sentence).
The lexical analyzer (the "lexer") parses individual symbols from the source code file into tokens. From there, the "parser" proper turns those whole tokens into sentences of your grammar	A parser does not give the nodes any meaning beyond structural cohesion. The next thing to do is extract meaning from this structure (sometimes called contextual analysis).

TOKEN, LEXEME, PATTERN:

Token: Token is a sequence of characters that can be treated as a single logical entity. Typical tokens are,

1) Identifiers 2) keywords 3) operators 4) special symbols 5) constants

Pattern: A set of strings in the input for which the same token is produced as output. This set of strings is described by a rule called a pattern associated with the token.

Lexeme: A lexeme is a sequence of characters in the source program that is matched by the pattern for a token.

Example:

Description of token

token	lexeme	pattern
const	const	const
if	if	If
relation	<, <=, =, >, >=, >	< or <= or = or > or >= or letter followed by letters & digit
i	pi	any numeric constant
nun	3.14	any character b/w "and "except"
literal	"core"	pattern

A pattern is a rule describing the set of lexemes that can represent a particular token in source program.

LEXICAL ERRORS:

Lexical errors are the errors thrown by your lexer when unable to continue. Which means that there's no way to recognise a *lexeme* as a valid *token* for your lexer. Syntax errors, on the other side, will be thrown by your scanner when a given set of **already** recognised valid tokens don't match any of the right sides of your grammar rules. Simple panic-mode error handling system requires that we return to a high-level parsing function when a parsing lexical error is detected.

Error-recovery actions are:

- i. Delete one character from the remaining input.
- ii. Insert a missing character into the remaining input.
- iii. Replace a character by another character.
- iv. Transpose two adjacent characters.

DIFFERENCE BETWEEN COMPILER AND INTERPRETER

- A compiler converts the high level instruction into machine language while an interpreter converts the high level instruction into an intermediate form.
- Before execution, entire program is executed by the compiler whereas after translating the first line, an interpreter then executes it and so on.
- List of errors is created by the compiler after the compilation process while an interpreter stops translating after the first error.
- An independent executable file is created by the compiler whereas interpreter is required by an interpreted program each time.
- The compiler produces object code whereas interpreter does not produce object code.
- In the process of compilation the program is analyzed only once and then the code is generated whereas source program is interpreted every time it is to be executed and every time the source program is analyzed. Hence interpreter is less efficient than compiler.
- Examples of interpreter: A *UPS Debugger* is basically a graphical source level debugger but it contains built-in C interpreter which can handle multiple source files. Example of compiler: *Borland C compiler* or *Turbo C compiler* compiles the programs written in C or C++.

REGULAR EXPRESSIONS

Regular expression is a formula that describes a possible set of string.

Component of regular expression..

X	the character x
.	any character, usually accept a new line
[x y z]	any of the characters x, y, z,
R?	a R or nothing (=optionally as R)
R*	zero or more occurrences.....
R+	one or more occurrences
R1R2	an R1 followed by an R2
R2R1	either an R1 or an R2.

A token is either a single string or one of a collection of strings of a certain type. If we view the set of strings in each token class as an language, we can use the regular-expression notation to describe tokens.

Consider an identifier, which is defined to be a letter followed by zero or more letters or digits. In regular expression notation we would write.

Identifier = letter (letter | digit)*

Here are the rules that define the regular expression over alphabet .

- ϵ is a regular expression denoting $\{\epsilon\}$, that is, the language containing only the empty string.
- For each 'a' in Σ , a is a regular expression denoting $\{a\}$, the language with only one string consisting of the single symbol 'a'.
- If R and S are regular expressions, then

(R) | (S) means $L_R \cup L_S$
 R.S means $L_R.L_S$
 R* denotes L_R^*

REGULAR DEFINITIONS

For notational convenience, we may wish to give names to regular expressions and to define regular expressions using these names as if they were symbols.

Identifiers are the set or string of letters and digits beginning with a letter. The following regular definition provides a precise specification for this class of string.

Example-1,

$Ab^*cd?$ Is equivalent to $(a(b^*)) | (c(d?))$

Pascal identifier

Letter - $A | B | | Z | a | b | | z$
 Digits - $0 | 1 | 2 | | 9$
 Id - letter (letter / digit)*

Recognition of tokens:

We learn how to express pattern using regular expressions. Now, we must study how to take the patterns for all the needed tokens and build a piece of code that examines the input string and finds a prefix that is a lexeme matching one of the patterns.

Stmt $\rightarrow$ if expr then stmt
 | If expr then else stmt

| e

Expr $\rightarrow$ term relop term

| term

Term $\rightarrow$ id

| number

For relop, we use the comparison operations of languages like Pascal or SQL where = is "equals" and < > is "not equals" because it presents an interesting structure of lexemes. The terminal of grammar, which are if, then, else, relop, id and number are the names of tokens as far as the lexical analyzer is concerned, the patterns of the tokens are described using regular definitions.

digit $\rightarrow$ [0,9]
digits $\rightarrow$ digit+
number $\rightarrow$ digit.(digit)?(e.[+]?digits)?
letter $\rightarrow$ [A-Z,a-z]
id $\rightarrow$ letter(letter/digit)*
if $\rightarrow$ if
then $\rightarrow$ then
else $\rightarrow$ else
relop $\rightarrow$ </>/<=>/==/<>

In addition, we assign the lexical analyzer the job stripping out white space, by recognizing the "token" we defined by:

ws $\rightarrow$ (blank/tab/newline)⁺

Here, blank, tab and newline are abstract symbols that we use to express the ASCII characters of the same names. ws is different from the other tokens in that, when we recognize it, we do not return it to parser, but rather restart the lexical analysis from the character that follows the white space. It is the following token that gets returned to the parser.

Lexeme	Token Name	Attribute Value
Any ws		
if	if	
then	then	
else	else	
Any id	id	pointer to table entry
Any number	number	pointer to table entry
<	relop	LT

<=	relop	LE
=	relop	ET
< >	relop	NE

TRANSITION DIAGRAM:

Transition Diagram has a collection of nodes or circles, called states. Each state represents a condition that could occur during the process of scanning the input looking for a lexeme that matches one of several patterns.

Edges are directed from one state of the transition diagram to another. each edge is labeled by a symbol or set of symbols.

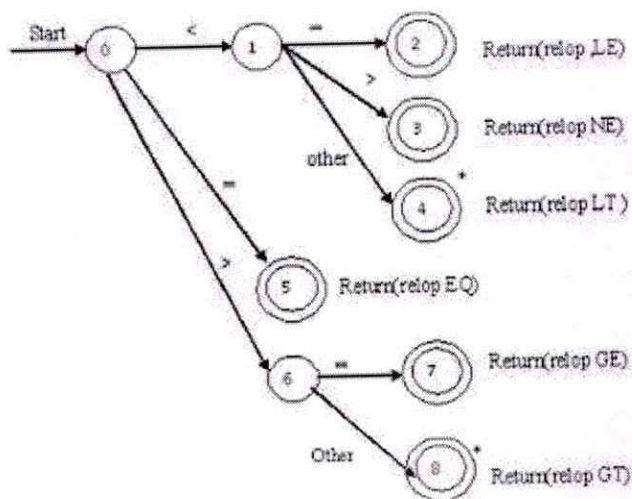
If we are in one state s , and the next input symbol is a , we look for an edge out of state s labeled by a . if we find such an edge, we advance the forward pointer and enter the state of the transition diagram to which that edge leads.

Some important conventions about transition diagrams are

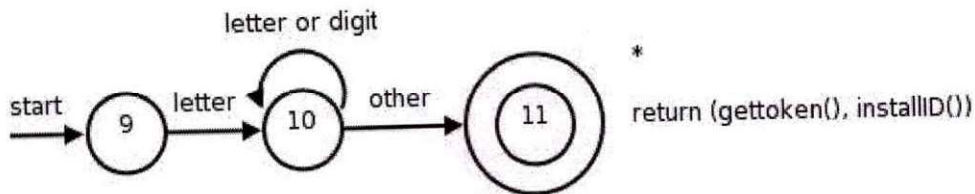
1. Certain states are said to be accepting or final. These states indicate that a lexeme has been found, although the actual lexeme may not consist of all positions b/w the lexeme Begin and forward pointers we always indicate an accepting state by a double circle.

2. In addition, if it is necessary to return the forward pointer to position, then we shall additionally place a * near that accepting state.

3. One state is designed the state, or initial state, it is indicated by an edge labeled "start" entering from nowhere. the transition diagram always begins in the state before any input symbols have been used.



As an intermediate step in the construction of a LA, we first produce a stylized flowchart, called a transition diagram. Position in a transition diagram, are drawn as circles and are called as states.



The above TD for an identifier, defined to be a letter followed by any no of letters or digits. A sequence of transition diagram can be converted into program to look for the tokens specified by the diagrams. Each state gets a segment of code.

If	=	if
Then	=	then
Else	=	else
Relop	=	< <= = > >=
Id	=	letter (letter digit) *
Num	=	digit

AUTOMATA

An automation is defined as a system where information is transmitted and used for performing some functions without direct participation of man.

- 1, an automation in which the output depends only on the input is **called an automation without memory**.
- 2, an automation in which the output depends on the input and state also is **called as automation with memory**.
- 3, an automation in which the output depends only on the state of the machine is **called a Moore machine**.
- 3, an automation in which the output depends on the state and input at any instant of time is **called a mealy machine**.

DESCRIPTION OF AUTOMATA

- 1, an automata has a mechanism to read input from input tape,
- 2, any language is recognized by some automation, Hence these automation are basically language 'acceptors' or 'language recognizers'.

Types of Finite Automata

- Deterministic Automata
- non-Deterministic Automata.

DETERMINISTIC AUTOMATA

A deterministic finite automata has at most one transition from each state on any input. A DFA is a special case of a NFA in which:-

- 1, it has no transitions on input ϵ ,

2, each input symbol has at most one transition from any state.

DFA formally defined by 5 tuple notation $M = (Q, \Sigma, \delta, q_0, F)$, where

Q is a finite 'set of states', which is non empty.

Σ is 'input alphabets', indicates input set.

q_0 is an 'initial state' and q_0 is in Q ie, q_0, Σ, Q

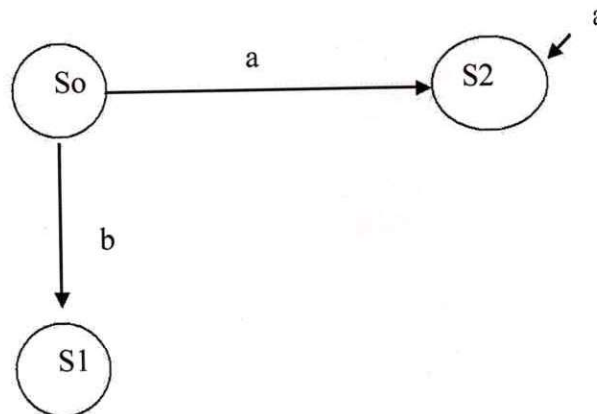
F is a set of 'Final states', ~~World~~

δ is a 'transmission function' mapping function, using this function the next state can be determined.

The regular expression is converted into minimized DFA by the following procedure:

Regular expression $\rightarrow$ NFA $\rightarrow$ DFA $\rightarrow$ Minimized DFA

The Finite Automata is called DFA if there is only one path for a specific input from current state to next state.



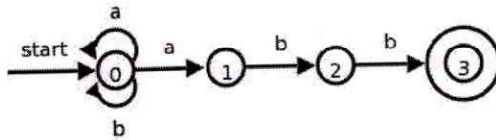
From state S_0 for input 'a' there is only one path going to S_2 . similarly from S_0 there is only one path for input going to S_1 .

O DETERMINISTIC AUTOMATA

✚ A FA is a mathematical model that consists of

- A set of states S .
- A set of input symbols Σ .
- A transition for move from one state to an other.
- A state so that is distinguished as the start (or initial) state.
- A set of states F distinguished as accepting (or final) state.
- A number of transition to a single symbol.

- ✦ This can be diagrammatically represented by a directed graph, called a transition graph, in which the nodes are the states and the labeled edges represent the transition function.
- ✦ This graph looks like a transition diagram, but the same character can label two or more transitions out of one state and edges can be labeled by the special symbol ϵ as well as by input symbols.
- ✦ The transition graph for an NFA that recognizes the language $(a | b)^* abb$ is shown



DEFINITION OF CFG

It involves four quantities.

CFG contain terminals, N-T, start symbol and production.

- ✦ Terminal are basic symbols from which string are formed.
- ✦ N-terminals are synthetic variables that denote sets of strings
- ✦ In a Grammar, one N-T are distinguished as the start symbol, and the set of string it denotes is the language defined by the grammar.

JNTU The production of the grammar specify the manner in which the terminal and -T can be combined to form strings.

- ✦ Each production consists of a N-T, followed by an arrow, followed by a string of one terminal and terminals.

DEFINITION OF SYMBOL TABLE

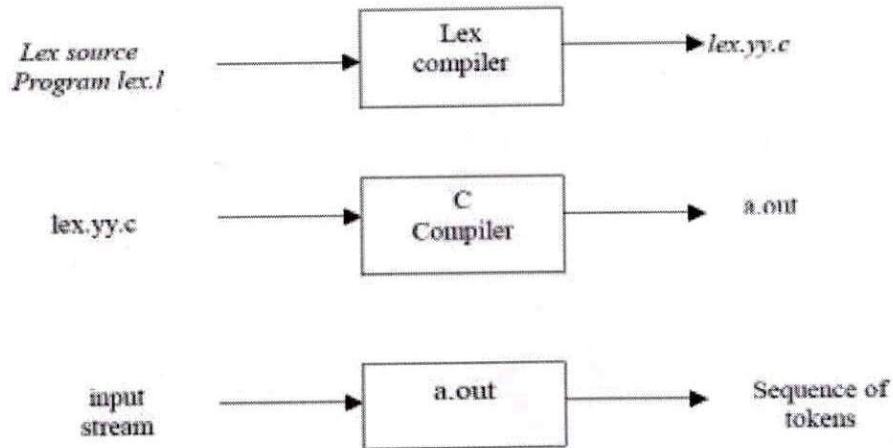
- ✦ An extensible array of records.
- ✦ The identifier and the associated records contains collected information about the identifier.

FUNCTION identify (Identifier name)

RETURNING a pointer to identifier information contains

- ✦ The actual string
- ✦ A macro definition
- ✦ A keyword definition
- ✦ A list of type, variable & function definition
- ✦ A list of structure and union name definition
- ✦ A list of structure and union field selected definitions.

Creating a lexical analyzer with Lex



Lex specifications:

A Lex program (the .l file) consists of three parts:

declarations

%%

translation rules

%%

auxiliary procedures

1. The *declarations* section includes declarations of variables, manifest constants (A manifest constant is an identifier that is declared to represent a constant e.g. #define PIE 3.14), and regular definitions.
2. The *translation rules* of a Lex program are statements of the form :

<i>p1</i>	{ <i>action 1</i> }
<i>p2</i>	{ <i>action 2</i> }
<i>p3</i>	{ <i>action 3</i> }
...	...
...	...

where each *p* is a regular expression and each *action* is a program fragment describing what action the lexical analyzer should take when a pattern *p* matches a lexeme. In Lex the actions are written in C.

3. The third section holds whatever *auxiliary procedures* are needed by the *actions*. Alternatively these procedures can be compiled separately and loaded with the lexical analyzer.

Department of CSE

Note: You can refer to a sample lex program given in page no. 109 of chapter 3 of the book: *Compilers: Principles, Techniques, and Tools* by Aho, Sethi & Ullman for more clarity.

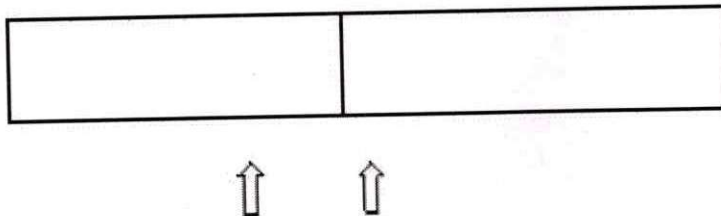
INPUT BUFFERING

The LA scans the characters of the source pgm one at a time to discover tokens. Because of large amount of time can be consumed scanning characters, specialized buffering techniques have been developed to reduce the amount of overhead required to process an input character.

Buffering techniques:

1. Buffer pairs
2. Sentinels

The lexical analyzer scans the characters of the source program one at a time to discover tokens. Often, however, many characters beyond the next token may have to be examined before the next token itself can be determined. For this and other reasons, it is desirable for the lexical analyzer to read its input from an input buffer. Figure shows a buffer divided into two halves of, say 100 characters each. One pointer marks the beginning of the token being discovered. A look ahead pointer scans ahead of the beginning point, until the token is discovered. We view the position of each pointer as being between the character last read and the character next to be read. In practice each buffering scheme adopts one convention either a pointer is at the symbol last read or the symbol it is ready to read.

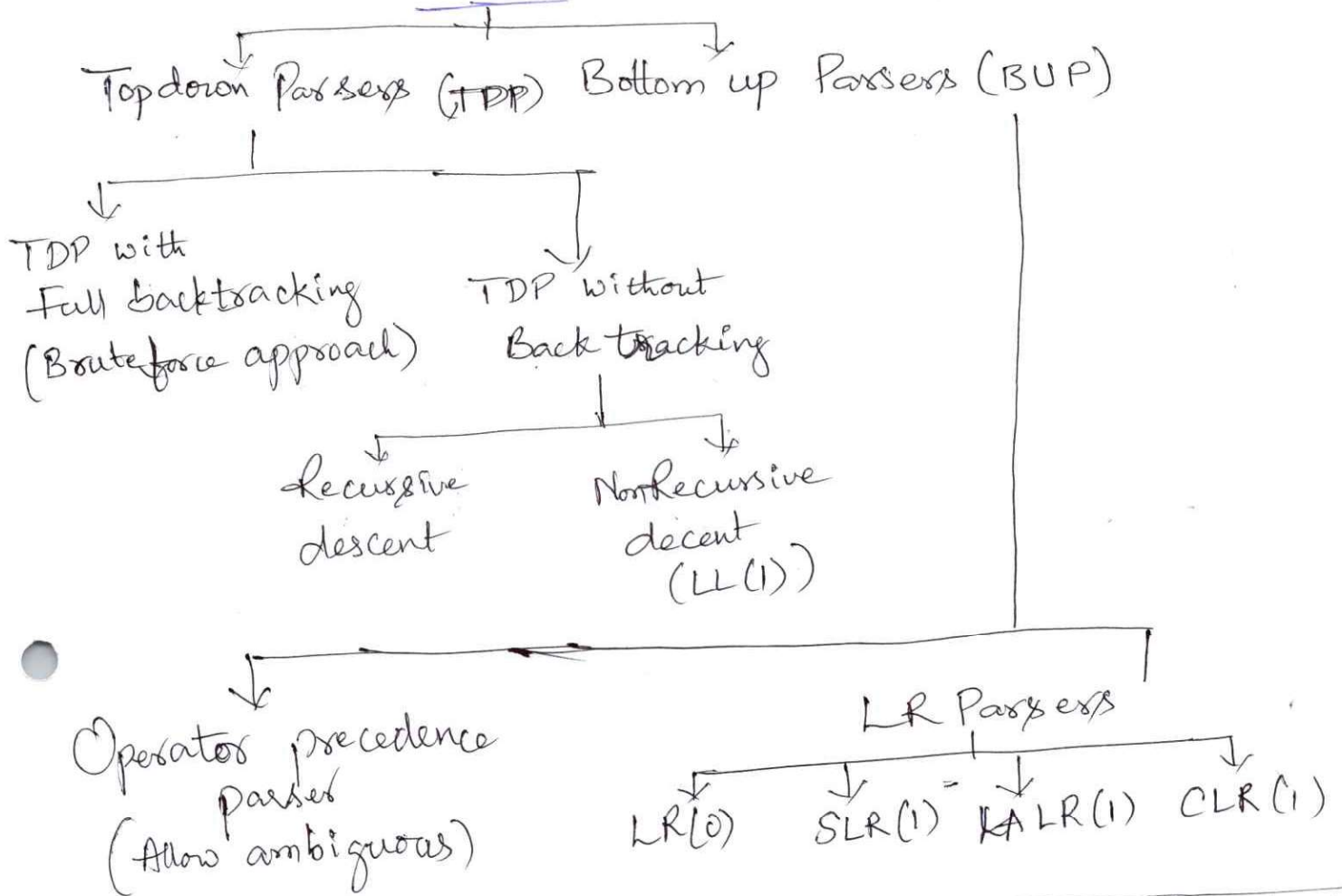


Token beginnings look ahead pointer

Token beginnings look ahead pointer The distance which the lookahead pointer may have to travel past the actual token may be large. For example, in a PL/I program we may see: `DECLARE (ARG1, ARG2... ARG n)` Without knowing whether `DECLARE` is a keyword or an array name until we see the character that follows the right parenthesis. In either case, the token itself ends at the second E. If the look ahead pointer travels beyond the buffer half in which it began, the other half must be loaded with the next characters from the source file. Since the buffer shown in above figure is of limited size there is an implied constraint on how much look ahead can be used before the next token is discovered. In the above example, if the look ahead traveled to the left half and all the way through the left half to the middle, we could not reload the right half, because we would lose characters that had not yet been grouped into tokens. While we can make the buffer larger if we chose or use another buffering scheme, we cannot ignore the fact that overhead is limited.

Unit-2

Parsers



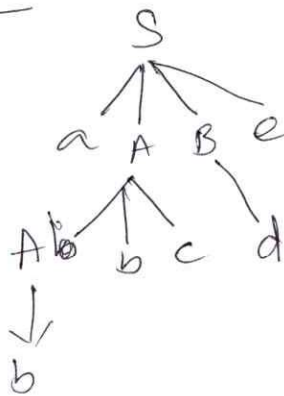
$S \rightarrow aABe$

$A \rightarrow Abc/b$

$B \rightarrow d$

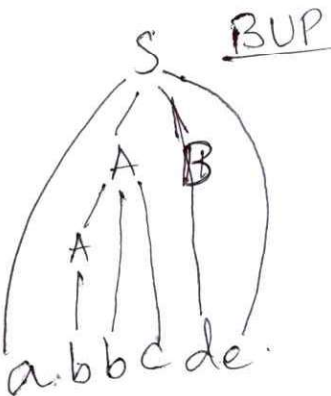
String: abbbcd

TDP



Note: In TDP we have to decide what production to use.

Note: In TDP we have to decide what production, we have to choose.



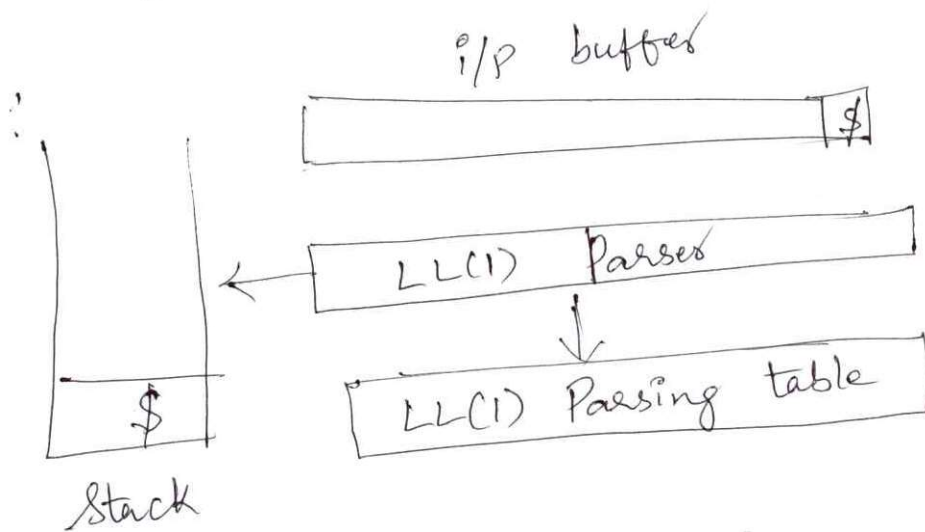
Note: TDP follows LMD.
 → BUP follows Reverse of RMD.

Note: In BUP, the task is when to reduce

Scan i/p from left to right

LL(1) Leftmost derivation

No. of lookaheads (how many symbols we see to take decision).

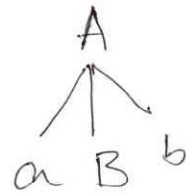


First() :- first character of the string that can be obtained by applying a production rule.

Follow() :-

$A \rightarrow aBb$

$B \rightarrow c|e$



Input string is "ab" to parse

→ As the first character in i/p is 'a', the parser applies

$A \rightarrow aBb$

→ Now the parser checks the second character of i/p string which is 'b', but the parser can't get any string derivable from 'B' that contains 'b' as first character.

→ But grammar contains a production rule $B \rightarrow e$, if it's applied then B will vanish and the parser gets "ab". But the parser can apply it only when it knows that the character that follows B is same as the current character in the input.

2) $S \rightarrow ACB \mid Cbb \mid Ba$	<u>FIRST</u> $\{d, g, e, h, b, a\}$
$A \rightarrow da \mid BC$	$\{d, g, e, h\}$
$B \rightarrow g \mid e$	$\{g, e\}$
$C \rightarrow h \mid e$	$\{h, e\}$

Follow (X) to be set of terminals that can appear immediately to the right of Nonterminal X in some sentential form.

Rules to Compute "Follow" Set.

- 1) $FOLLOW(S) = \{\$ \}$ // where S is start symbol.
- 2) If $A \rightarrow pBq$ is a production, where p, B and q are any grammar symbols, then everything in $FIRST(q)$ except ϵ is in $FOLLOW(B)$.
- 3) If $A \rightarrow qB$ is a production, then everything in $FOLLOW(A)$ is in $FOLLOW(B)$.
- 4) If $A \rightarrow pBq$ is a production and $FIRST(q)$ contains ϵ , then $FOLLOW(B)$ contains $\{FIRST(q) - \epsilon\} \cup FOLLOW(A)$.

Eg:

$E \rightarrow TE'$
 $E' \rightarrow +TE' \mid e$
 $T \rightarrow FT'$
 $T' \rightarrow *FT' \mid e$
 $F \rightarrow (E) \mid id$

<u>First</u>	<u>Follow</u>
$\{C, id\}$	$\{\$, \epsilon,)\}$
$\{+, e\}$	$\{\$, \epsilon,)\}$
$\{C, id\}$	$\{+, \$, \epsilon,)\}$
$\{*, e\}$	$\{+, \$, \epsilon,)\}$
$\{C, id\}$	$\{*, +, \$, \epsilon,)\}$

Follow () $\hat{=}$ what is the terminal followed by a non terminal.

Eg: $A \rightarrow a B b$

Follow (B) $= \{b\}$;

$FIRST(X)$ for a grammar symbol 'X' is set of terminals that begin the strings derivable from X.

Rules to compute FIRST

1) If X is a terminal, then $FIRST(X) = \{X\}$.

2) If $X \rightarrow \epsilon$, is a production rule, then add ϵ to $first(X)$.

3) If $X \rightarrow Y_1 Y_2 Y_3 \dots Y_n$ is a production.

(a) $FIRST(X) = FIRST(Y_1)$.

(b) If $FIRST(Y_1)$ contains ϵ , then $FIRST(X) = \{FIRST(Y_1) - \epsilon\} \cup \{FIRST(Y_2)\}$

(c) If $FIRST(Y_i)$ contains ϵ for all $i=1$ to n then add ϵ to $FIRST(X)$.

Eg:

① $E \rightarrow T E'$

$E' \rightarrow + T E' \mid \epsilon$

$T \rightarrow F T'$

$T' \rightarrow * F T' \mid \epsilon$

$F \rightarrow (E) \mid id$

FIRST

$FIRST(T) = \{C, id\}$.

$\{+, \epsilon\}$

$\{C, id\}$

$\{*, \epsilon\}$

$\{C, id\}$

	<u>Action</u>						<u>Goto</u>
	id	+	*	(	)	\$	E
0	s3	e1	e1	s2	e2	e1	1
1	e3	s4	s5	e3	e2	Acc	
2	s3	e1	e1	s2	e2	e1	6
3	s4	s4	s4	s4	s4	s4	
4	s3	e1	e1	s2	e2	e1	7
5	s3	e1	e1	s2	e2	e1	8
6	e3	s4	s5	e3	s4	e4	
7	s1	s1	s5	s1	s1	s1	
8	s2	s2	s2	s2	s2	s2	
9	s3	s3	s3	s3	s3	s3	

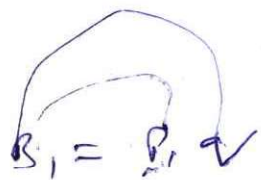
e1: Missing operand (+, *, \$)

e2: Unbalanced right parenthesis. ()

e3: missing operator. (id, (

e4: Missing right parenthesis (\$).

id +)

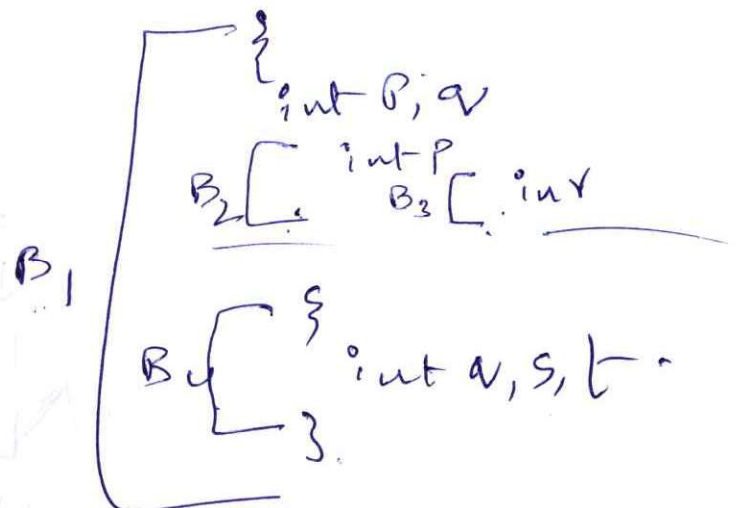


$B_2 = P$

$B_3 = id, \checkmark$

$B_4 = P, S, t$

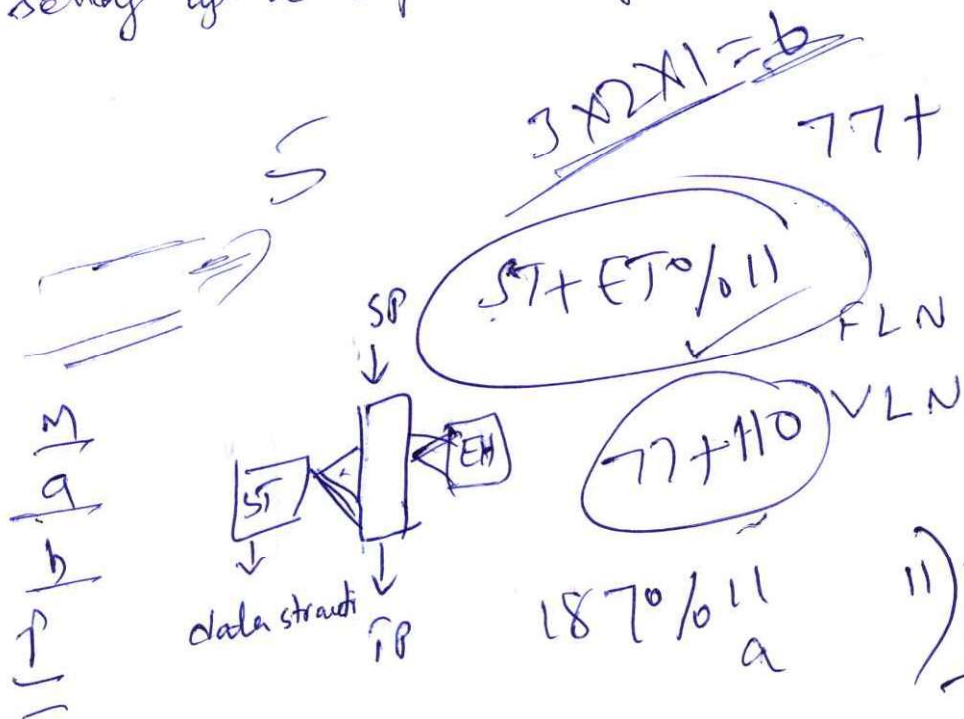
$P(B_1)$



Symbol Tables

- Data structures used to hold information about source program constants.
- Information collected ~~by~~ incrementally by analysis phases of compiler and used by synthesis phases to generate target code.
- To record ~~the~~ variable names used in the source program and collect information about various attributes of each name.
- Attributes may provide information about storage allocated for a name, its type, its scope, and in case of procedure names, such things as the number and types of its arguments, the method of passing each argument and return type etc.
- Scope of a declaration is the portion of a program to which the declaration applies. The scope shall be implemented by setting up a separate symbol table for each scope.

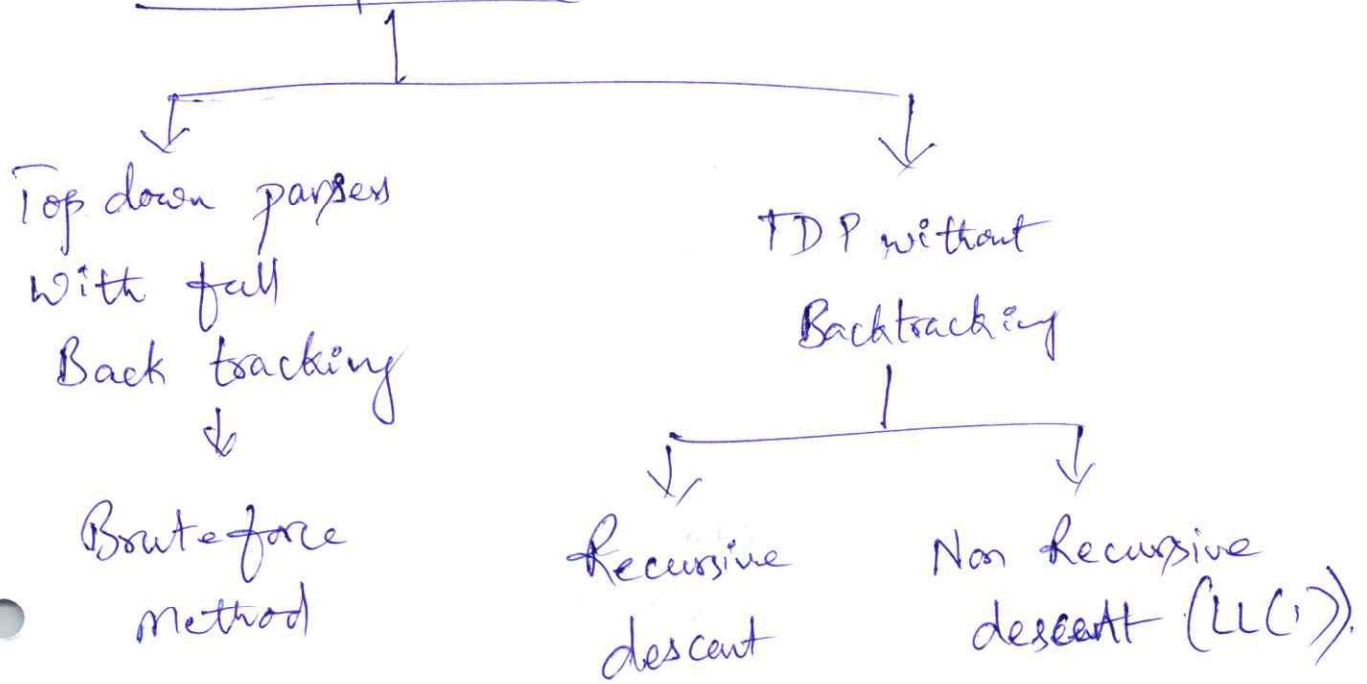
→



$$187 \% 11 = a$$

$$\begin{array}{r} 11 \overline{) 187} 17 \\ \underline{11} \\ 77 \\ \underline{77} \\ 0 \end{array}$$

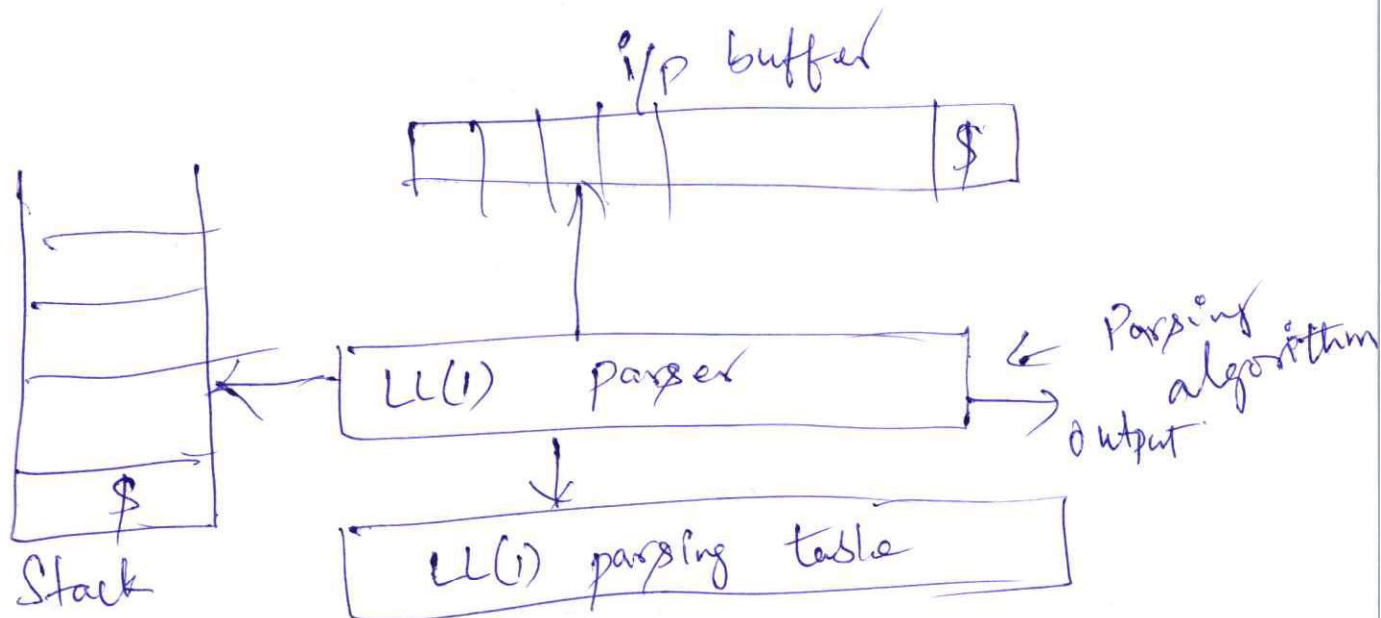
Top down parsers (Predictive parsers).



look ahead.

LL(1) Parser

Scan i/p from left to right — Leftmost derivation



Matched	Stack	Input	Action
	$E \$$	$id + id * id \$$	output $E \rightarrow TE'$
	$TE' \$$	$id + id * id \$$	output $T \rightarrow FT'$
	$FT'E' \$$	$id + id * id \$$	output $F \rightarrow id$
	$idTE' \$$	$id + id * id \$$	match id .
id	$T'E' \$$	$+ id * id \$$	output $T' \rightarrow E$
id	$E \$$	$+ id * id \$$	output $E \rightarrow +TE'$
id	$+TE' \$$	$+ id * id \$$	match $+$
$id +$	$TE' \$$	$id * id \$$	output $T \rightarrow FT'$
$id +$	$FT'E' \$$	$id * id \$$	output $F \rightarrow id$
$id + id$	$idTE' \$$	$id * id \$$	match id
$id + id$	$T'E' \$$	$* id \$$	output $T' \rightarrow *FT'$
$id + id$	$*FT'E' \$$	$* id \$$	match $*$
$id + id *$	$FT'E' \$$	$id \$$	output $F \rightarrow id$
$id + id *$	$idTE' \$$	$id \$$	match id .
$id + id * id$	$T'E' \$$	$\$$	output $T' \rightarrow E$
$id + id * id$	$E \$$	$\$$	output $E' \rightarrow E$.
$id + id * id$	$\$$	$\$$	

- Parser controlled by a program that considers 'X', the symbol on top of stack and 'a' the current i/p symbol.
- If 'X' is non-terminal, the parser chooses an ~~X~~-production by consulting entry $M[X, a]$ of the parsing table M. Otherwise, it checks for a match b/w the terminal X and current i/p symbol 'a'.

$E \rightarrow iE'$

$E' \rightarrow +iE' / e$

$E()$

```
{  
  if (d == 'i')  
  {  
    match('i')  
  }  
  E'()  
}
```

$d = \text{getchar}();$

$E'()$

```
{  
  if (d == '+')  
  {  
    match('+');  
    match('i');  
  }  
  E'()  
}
```

else
return

$\text{match}(\text{char } t)$

```
{  
  if (d == t)  
    d = getchar();  
  else  
    printf("error");  
}
```

$\text{main}()$

```
{  
  E();
```

```
  if (d == '$')
```

```
    printf("parsing success");
```

```
  } else
```

```
    printf("parsing unsuccessful");  
}
```

RDP: It is a ^{type of} TDP built from set of mutually recursive procedures where each nonterminal of a given grammar is implemented as a procedure.

$$E \rightarrow E+T \mid E-T \mid T$$

$$T \rightarrow T \times F \mid T / F \mid F$$

$$F \rightarrow (E) \mid id.$$

$$E \rightarrow TE'$$

$$E' \rightarrow +TE' \mid \epsilon$$

$$E \rightarrow TE'$$

$$E' \rightarrow -TE' \mid \epsilon$$

$$T \rightarrow *FT'$$

$$T' \rightarrow *FT' \mid \epsilon$$

$$F \rightarrow (E) \mid id.$$

$$A \rightarrow A\alpha \mid \beta$$

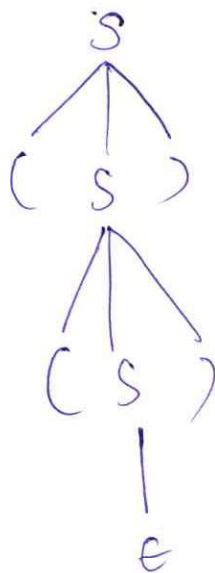
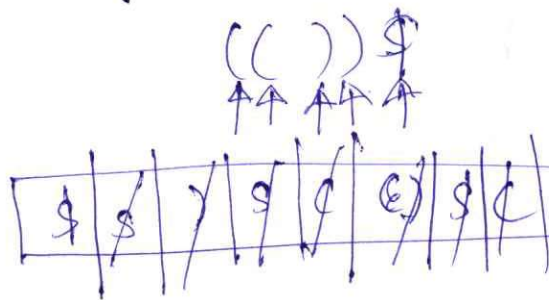
$$A \rightarrow \beta A'$$

$$A' \rightarrow \alpha A' \mid \epsilon$$

$\text{First}(\alpha) = \alpha$ is any string of grammar symbols, to be the set of terminals that begin strings derived from α .

$$S \rightarrow (S) / \epsilon$$

	(	)	\$
S	$S \rightarrow (S)$	$S \rightarrow \epsilon$	$S \rightarrow \epsilon$



$$S \rightarrow AaAb / BbBa$$

$$A \rightarrow \epsilon$$

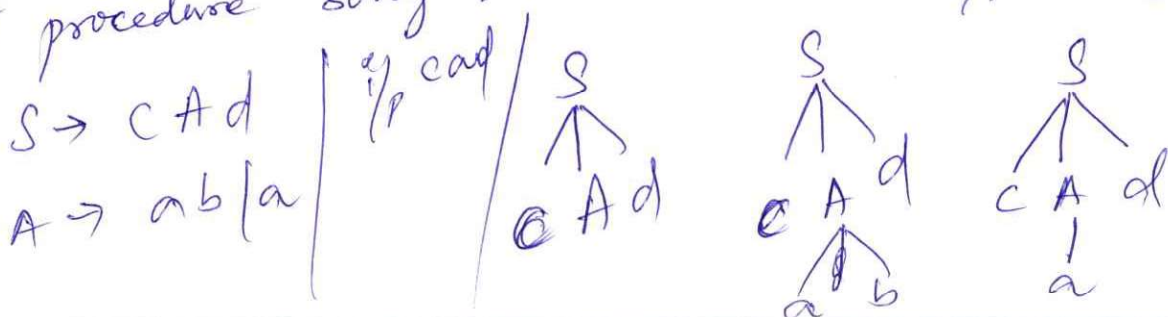
$$B \rightarrow \epsilon$$

	a	b	\$
S	$S \rightarrow AaAb$	$S \rightarrow BbBa$	
A	$A \rightarrow \epsilon$	$A \rightarrow \epsilon$	
B	$B \rightarrow \epsilon$	$B \rightarrow \epsilon$	

Recursive Descent parsing

→ RDP parsing program consists of a set of procedures, one for each nonterminal.

→ Execution begins with the procedure for the start symbol, which halts and announces success if its procedure body scans the entire i/p string.



	<u>first</u>	<u>Follow</u>
$E \rightarrow TE'$	$\{id, (\}$	$\{ \$,) \}$
$E' \rightarrow +TE' \epsilon$	$\{ +, \epsilon \}$	$\{ \$,) \}$
$T \rightarrow FT'$	$\{ id, (\}$	$\{ \$,), + \}$
$T' \rightarrow *FT' \epsilon$	$\{ *, \epsilon \}$	$\{ \$,), + \}$
$F \rightarrow id (E)$	$\{ id, (\}$	$\{ *, \$,), + \}$

Parsing Table

	id	$+$	$*$	$($	$)$	$\$$
E	$E \rightarrow TE'$			$E \rightarrow TE'$		
E'		$E' \rightarrow +TE'$			$E' \rightarrow \epsilon$	$E' \rightarrow \epsilon$
T	$T \rightarrow FT'$			$T \rightarrow FT'$		
T'		$T' \rightarrow \epsilon$	$T' \rightarrow *FT'$		$T' \rightarrow \epsilon$	$T' \rightarrow \epsilon$
F	$F \rightarrow id$			$F \rightarrow (E)$		

②

$S \rightarrow ACB | Cbb | Ba$
 $A \rightarrow da | BE$
 $B \rightarrow g | \epsilon$
 $C \rightarrow h | \epsilon$

First
 $\{d, g, h, \epsilon, b, a\}$
 $\{d, g, \epsilon, h\}$
 $\{g, \epsilon\}$
 $\{h, \epsilon\}$

Follow
 $\{\$ \}$
 $\{h, g, \$ \}$
 $\{a, h, g, \$ \}$
 $\{b, h, g, \$ \}$

③

$S \rightarrow aBDh$
 $B \rightarrow cG$
 $G \rightarrow bG | \epsilon$
 $D \rightarrow EF$
 $E \rightarrow g | \epsilon$
 $F \rightarrow f | \epsilon$

First
 $\{a\}$
 $\{c\}$
 $\{b, \epsilon\}$
 $\{g, f, \epsilon\}$
 $\{g, \epsilon\}$
 $\{f, \epsilon\}$

Follow
 $\{\$ \}$
 $\{g, f, h\}$
 $\{g, f, h\}$
 $\{h\}$
 $\{f, h\}$
 $\{h\}$

④

$S \rightarrow A$
 ~~$A \rightarrow aB | Ad$~~
 ~~$A \rightarrow aBA'$~~
 ~~$b \rightarrow bA' \rightarrow Ad | \epsilon$~~
 $B \rightarrow b$
 $C \rightarrow g$

First
 $\{a\}$
 $\{a\} \rightarrow \{d, \epsilon\}$
 $\{b\}$
 $\{g\}$

Follow
 $\{\$ \}$
 $\{\$ \}$
 $\{\$ \}$
 $\{d, \$ \}$
 NA

⑤

$S \rightarrow (L) | a$
 $L \rightarrow SL'$
 $L' \rightarrow , SL' | \epsilon$

$\{ (, a \}$
 $\{ (, a \}$
 $\{ , , \epsilon \}$

$\{ \$, ,,) \}$
 $\{) \}$
 $\{ , \}$

⑥ $S \rightarrow AaAb \mid BbBa$ $A \rightarrow \epsilon$ $B \rightarrow \epsilon$	<u>First</u> $\{a, b\}$ $\{\epsilon\}$ $\{\epsilon\}$	<u>Follow</u> $\{\$ \}$ $\{a, b\}$ $\{b, a\}$
--	--	--

① $E \rightarrow E + E \rightarrow ①$ ② $S \rightarrow i S e S \mid i S \mid a$

$E \rightarrow E * E \rightarrow ②$

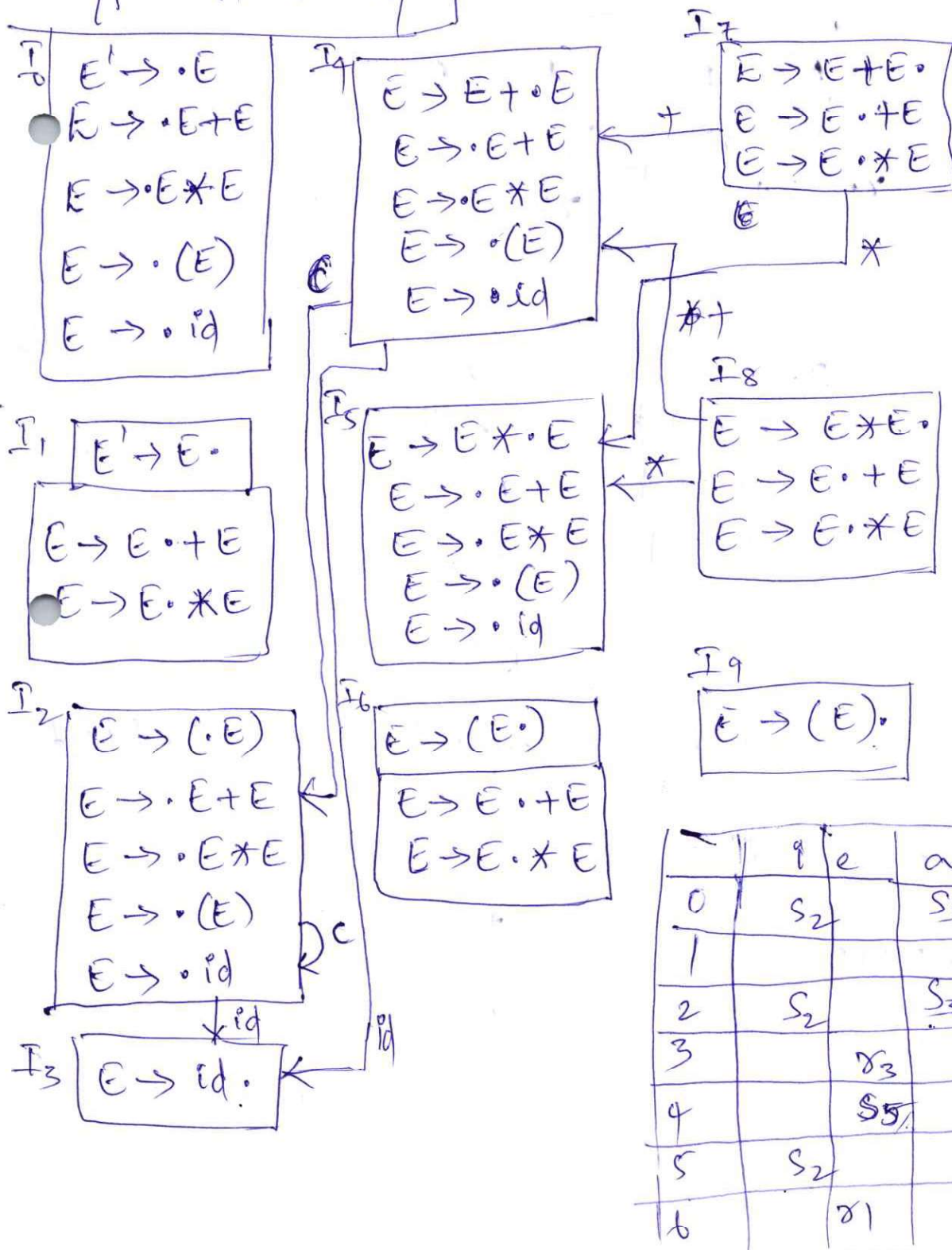
$E \rightarrow (E) \rightarrow ③$

$E \rightarrow id \rightarrow ④$

i/p = ~~id~~ a e a \$

Follow(E) = { \$, +, *,) }

i/p = id + id * id \$



	i	e	a	\$	\$
0	s ₂		s ₃		1
1				Accept	4
2	s ₂		s ₃		
3		r ₃		r ₃	
4		s ₅		r ₂	
5	s ₂		s ₃		6
6		r ₁		r ₁	

	id	+	*	(	)	\$	E
0	S3			S2			1
1		S4	S5				
2	S3			S2			6
3	S3	S4	S5	S2	S4	S5	7
4	S3			S2			7
5	S3			S2			8
6		S4	S5		S9		
7		S4/S2	S5/S2		S4	S5	
8		S4/S2	S5/S2		S2	S2	
9		S4	S5		S4	S5	

Stack

Input

Action

0
 0 id 3
 0 E 1
 0 E 1 + 4
 0 E 1 + 4 id 3
 0 E 1 + 4 E 7
 0 E 1 + 4 E 7 * 5 id 3
 0 E 1 + 4 E 7 * 5 id 3 \$
 0 E 1 + 4 E 7 * 5 E 8 \$
 0 E + 4 E 7 \$
 0 E 1 \$

shift
 $E \rightarrow id$
 shift
 shift
 $E \rightarrow id$
 shift / reduce Conflict
 but use shift
 shift
 $E \rightarrow id$
 $E \rightarrow E * E$
 $E \rightarrow E + E$
 Accept

$$\frac{i}{p} = id * id + id$$

<u>Stack</u>	<u>Input</u>	<u>Action</u>
0	id * id + id \$	shift
0 id 5	* id + id \$	reduce $F \rightarrow id$
0 F 3	* id + id \$	reduce $T \rightarrow F$
0 T 2	* id + id \$	shift
0 T 2 * 7	id + id \$	shift
0 T 2 * 7 id 5	+ id \$	reduce $F \rightarrow id$
0 T 2 * 7 F 10	+ id \$	reduce $T \rightarrow T * F$
0 T 2	+ id \$	reduce $E \rightarrow T$
0 E 1	+ id \$	shift
0 E 1 + 6	id \$	shift
0 E 1 + 6 id 5	\$	reduce $F \rightarrow id$
0 E 1 + 6 F 3	\$	reduce $T \rightarrow F$
0 E 1 + 6 T 9	\$	reduce $E \rightarrow E + T$
0 E 1	\$	Accept

$$S \rightarrow iSeS / iS / a$$
 I_0

$$\begin{array}{l} S' \rightarrow \cdot S \\ S \rightarrow \cdot iSeS / iS / a \end{array}$$
 I_1

$$S' \rightarrow S \cdot$$

$$\begin{array}{l} S \rightarrow i \cdot SeS / i \cdot S \\ S \rightarrow \cdot iSeS / \cdot iS / a \end{array}$$
 I_3

$$S \rightarrow a \cdot$$
 I_4

$$S \rightarrow iS \cdot eS / iS \cdot$$
 I_5

$$\begin{array}{l} S \rightarrow iSe \cdot S \\ S \rightarrow \cdot iSeS / \cdot iS / a \end{array}$$
 I_6

$$S \rightarrow iSeS \cdot$$
Stack
Input
Action

0

 $iiaea\$$

shift

0 i 2

 $iaea\$$

shift

0 i 2 i 2

 $aea\$$

shift

0 i 2 i 2 a 3

 $ea\$$
 $S \rightarrow a$

0 i 2 i 2 S 4

 $ea\$$

shift

0 i 2 i 2 S 4 e 5

0 i 2 S 4

 $a\$$

shift

0 i 2 i 2 S 4 e 5 a 3

0 S 1

 $\$$
 $ea\$$
 $S \rightarrow a$

0 i 2 i 2 S 4 e 5 S 6

 $\$$
 $S \rightarrow iSeS$

0 i 2 S 4

 $\$$
 $S \rightarrow iS$

0 S 1

 $\$$

Accept

 $S \rightarrow iS$
~~shift~~ $S \rightarrow iS$
~~shift~~ $iSeS$

CLR(1) Parsing Table

State	Action			Goto	
	c	d	\$	s	C
0	S3	S4		1	2
1			Accept		
2	S6	S7			5
3	S3	S4			8
4	r3	r3			
5			r1		
6	S6	S7			9
7			r3		
8	r2	r2			
9			r2		

Stack

Input

Action

0

dcd\$

Shift

cd4

cd\$

C → d

cd2

cd\$

Shift

cd2c

d\$

Shift

cd2c6d

\$

C → d

cd2c6C9

\$

C → cC

cd2C5

\$

S → C'C

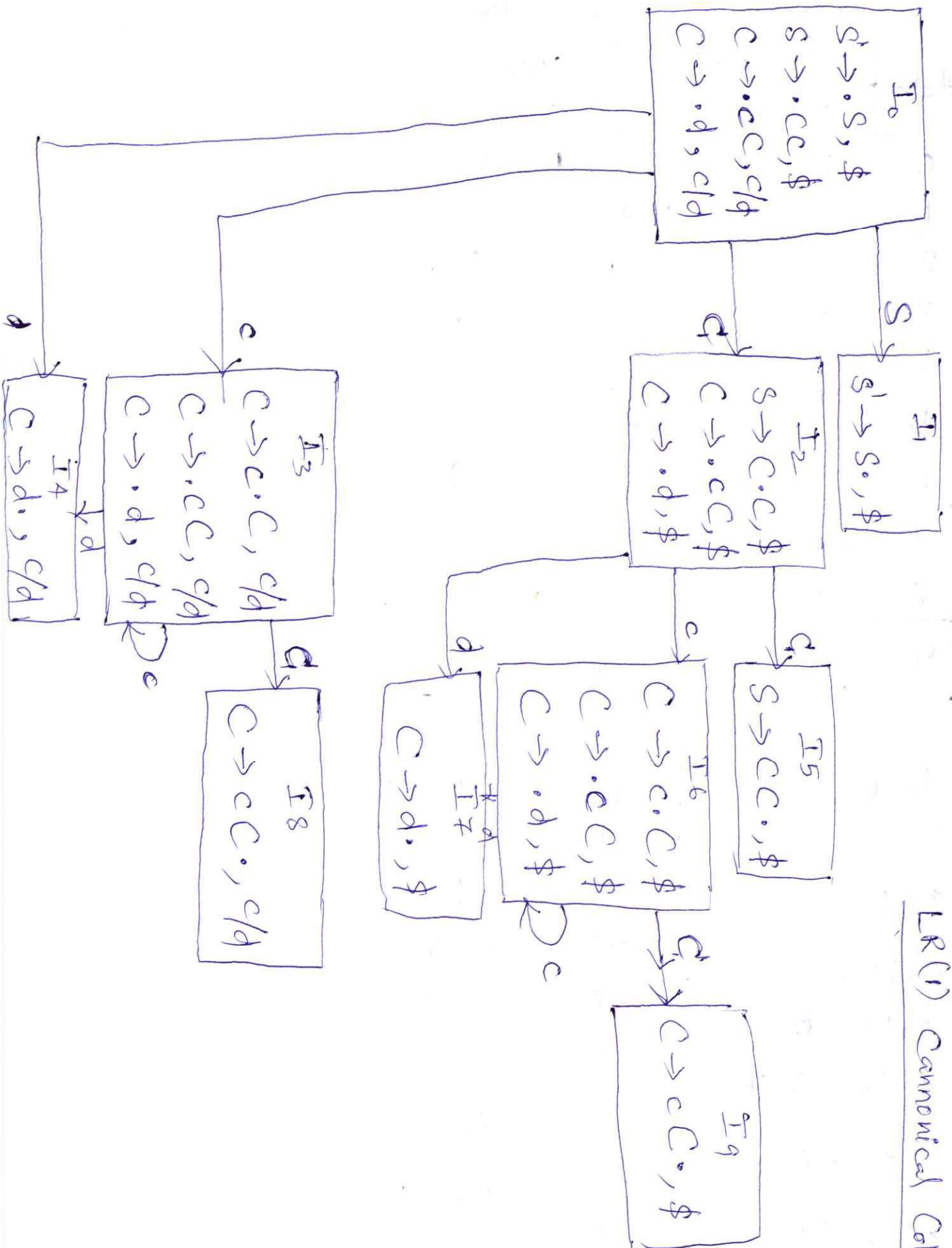
CS1

\$

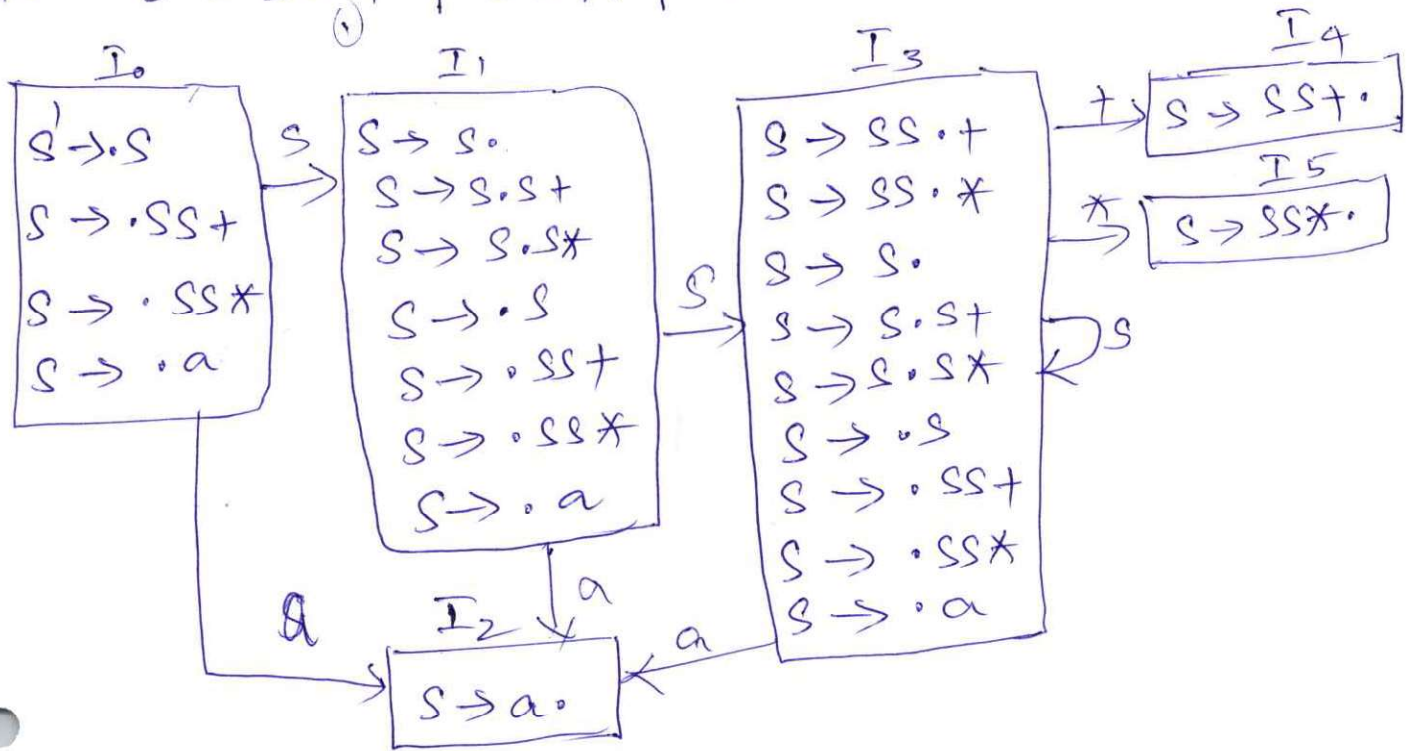
Accept

$S \rightarrow S, S \rightarrow CC, C \rightarrow cC \mid d$

LR(1) Canonical Collection



Ex: $S \rightarrow SS + \mid SS * \mid a$



	Action					Goto	
	+	*	a	\$		S	
0			S2			1	
1				Accept			
2	r3	r3	r3	r3			
3	S4	S5	S2				
4	r1	r1	r1	r1			
5	r2	r2	r2	r2			

Input: $a a * a +$

Input = aabb

Stack	Input	Action
0	aabb\$	Shift
0a3	abb\$	Shift
0a3a3	bb\$	Shift
0a3a3b4	b\$	Shift reduce
0a3a3A6	b\$	reduce.
0a3A6	b\$	reduce.
0A2	b\$.	Shift
0A2b4	\$	reduce.
0A2A5	\$	reduce.
0S1	\$.	Accept

→ Four possible actions a shift-reduce parser can make:

- (1) Shift :- Shifts the next i/p symbol on-to the top of the stack.
- (2) Reduce :- The right end of the string to be reduced must be at the top of the stack. locate the left end of the string within the stack and decide with what nonterminal to replace the string.
- (3) Accept :- Announce successful completion of parsing.
- (4) Error :- Discover syntax error and call an error recovery routine.

→ The handle will always appear on top of the stack, never inside.

Shift - Reduce Parsing (S-R parsing)

→ S-R parsing is a form of bottom up parsing in which a stack holds grammar symbols and an i/p buffer holds the rest of the string to be parsed.

Stack	Input
\$	w\$

→ The handle always appears at the top of the stack, just before it is identified as the handle.

→ During left to right scan of the i/p string, the parser shifts zero, or more i/p symbols onto the stack, until it is ready to reduce a string β of grammar symbols on top of the stack.

→ It then reduces ' β ' to the head of the appropriate production. The parser repeats this cycle until it has detected an error or until the stack contains the start symbol and the i/p is empty.

Stack	Input
\$S	\$

→ Upon entering this configuration, the parser halts and announces successful completion of parsing.

Eg:-	Stack	Input	Action
	\$	id1 * id2 \$	shift
	\$id1	* id2 \$	reduce by $F \rightarrow id$
	\$F	* id2 \$	reduce by $T \rightarrow F$
	\$T	* id2 \$	shift
	\$T*	id2 \$	shift
	\$T*id2	\$	reduce by $F \rightarrow id$
	\$T*F	\$	reduce by $T \rightarrow T * F$
	\$T	\$	reduce by $E \rightarrow T$
	\$E	\$	accept.

Bottom-UP- Parsing (BUP)

- BUP is known as shift-reduce parsing.
- A general method of shift reduce parsing is called LR parsing.
- Shift reduce attempts to construct a parse tree for an I/p string beginning at leaves and working up towards the root.
- We can think of this process as one of "reducing" a string 'w' to the start symbol of grammar.
- At each reduction step a particular substring matching the right side of a production is replaced by the symbol on the left of that production.

Eg:-
 $S \rightarrow aABe$ ①
 $A \rightarrow Abc|b$ ② ③
 $B \rightarrow d$ ④

String "abcde" can be reduced to "S"

$aABcde \rightarrow$ ③ $A \rightarrow b$

$aAde \rightarrow A \rightarrow Abc$

$aABe \rightarrow B \rightarrow B.d$

$S \rightarrow S \rightarrow aABe$

Handle:- A substring that matches the right side of a production.

- A "handle" of a right-sentential form γ is a production $A \rightarrow \beta$ and a position of ' γ ' where the string β may be found and replaced by A .
- The string 'w' to the right of the handle contains only terminal symbols.

- If the grammar is ~~unambiguous~~ unambiguous, then every right sentential form of a grammar has exactly one handle.
- Reducing β to A in $\alpha\beta w$ can be ~~not~~ thought of as "pruning the handle", that is removing the ~~old~~ ~~the~~ children of 'A' ~~for~~ from the parse tree.

→ A reduction is the reverse of a step in derivation.

Handle Pruning

- Bottom up parsing during left to right scan of the i/p constructs a right most derivation in reverse.

Eg:-

Right sentential form	Handle	Reducing production
$id_1 * id_2$	id_1	$F \rightarrow id$
$F * id_2$	F	$T \rightarrow F$
$T * id_2$	id_2	$F \rightarrow id$
$T * F$	$T * F$	$T \rightarrow T * F$
T	T	$E \rightarrow T$

$E \rightarrow E * T \mid T ; T \rightarrow T * F \mid F ; F \rightarrow (E) \mid id .$

- A rightmost derivation in reverse can be obtained by "handle pruning".

Closure:

If I is set of items for a grammar G then

$\text{Closure}(I)$ is the set of items constructed from I by two rules

- (1) Initially, add every item in I to $\text{closure}(I)$
- (2) If $A \rightarrow \alpha \cdot B \beta$ is in $\text{Closure}(I)$ and $B \rightarrow \gamma$ is a production, then add the item $B \rightarrow \cdot \gamma$ to $\text{closure}(I)$, if it is not already there. Apply this rule until no more new items can be added to $\text{closure}(I)$.

Goto

$\text{Goto}(I, x)$ where I is a set of items and x is a grammar symbol.

$\text{Goto}(I, x)$ is defined to be the closure of the set of all items $[A \rightarrow \alpha x \cdot \beta]$ such that $[A \rightarrow \alpha \cdot x \beta]$ is in I :

	Action							
	id	+	*	(	)	\$	E	Go to
0	S5			S4			1	3
1		S6	S4			Accept		
2		x ₂	S7		x ₂			
3		x ₄	x ₄		x₄ two x ₄			
4	S5			S4			8	3
5		x ₆	x ₆					
6	S5			S4				3
7	S5			S4				9
8		S6			S11			10
9		x ₁	S7		x ₁	x ₁		
10		x ₃	x ₃		x ₃	x ₃		
11		x ₅	x ₅		x ₅	x ₅		

LR(0)

Given Grammar

$$S \rightarrow AA$$

$$A \rightarrow aA \mid b$$

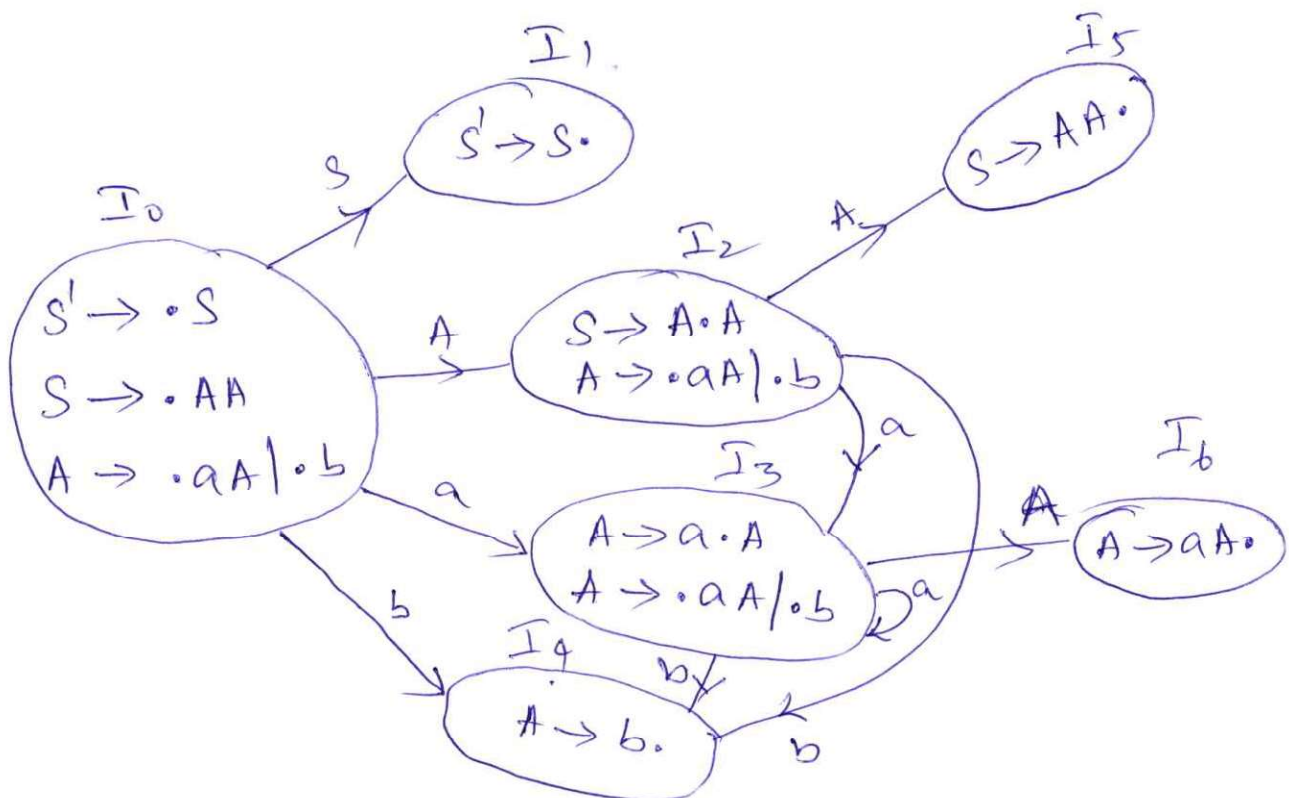
Augmented Grammar

$$S' \rightarrow S \quad \text{--- ①}$$

$$S \rightarrow AA \quad \text{--- ②}$$

$$A \rightarrow aA \mid b \quad \text{--- ③, ④}$$

Canonical Collection of LR(0)



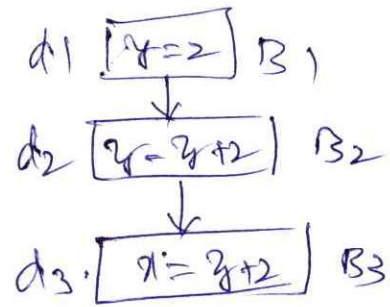
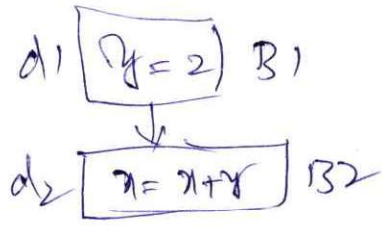
Closure :- If there is an '.' before a variable (or) non terminal then add ^{all} productions ~~related to~~ that variable.

goto :- If state does not contain final item then apply goto for each production in the state.

LR(0) Parsing Table

	<u>Action</u>			<u>Goto</u>	
	a	b	\$	S	A
0	S3	S4		1	2
1			Accept		
2	S3	S4			5
3	S3	S4			6
4	r4	r4	r4		
5	r2	r2	r2		
6	r3	r3	r3		
	Note: a				

(2) Reaching Definition: Def D reaches at Point P if there is a path from D to P along which D is not killed.



d_1 : reachable to B2 but not B3.

Adv: Used in Constant & Variable propagation

3) Live Variable: A variable 'x' is live at some point 'P' if there is a path from P to the exit, along which the ~~value~~ value of x is used before it is redefined. otherwise the variable is said to be dead.

[x=5] B1

Adv: It is useful in sig alloc
" " " " dead code elimination

B2 [y=7] [z=3] B3

[~~x=x~~] B4

4) Reaching Expression: ~~Reaching~~ expression 'e' is said to be a live exp along some path $P_i \dots P_j$ if an evaluation of 'e' exists along some path $P_i \dots P_j$ and no def of any operand exists before its evaluation along the path.

$R_e = \text{Adv}$: Useful in performing code movement optimizers

Live Variable Analysis (a) Live Range Identification

→ This analysis is useful for code generation.

→ For register allocation.

→ Two important definitions.

(1) $\text{def}(B)$ - ~~the~~ def set of variables to which values are assigned in block B , before the actual use.

(2) $\text{Use}(B)$ - ^{set of} Variables whose values ~~of~~ are may be used in block B , prior to any definition of the variable.

The formulae for computing $\text{in}[B]$ and $\text{out}[B]$ are

$$\text{in}[B] = \text{Use}[B] \cup (\text{out}(B) - \text{def}(B))$$

$$\text{out}[B] = \bigcup_S \text{in}[S] \quad \text{where 'S' is the successor of block B.}$$

→ $\text{in}[B]$ → represents variable that is live coming into a block.

→ $\text{out}[B]$ → represents that a variable is live coming out of a block if and only if it is live coming into one of its successor block.

Algorithm:

(1) Compute def and use for each block.

(2) For each block B initialize $\text{in}[B] = \emptyset$ and $\text{out}[B] = \emptyset$.

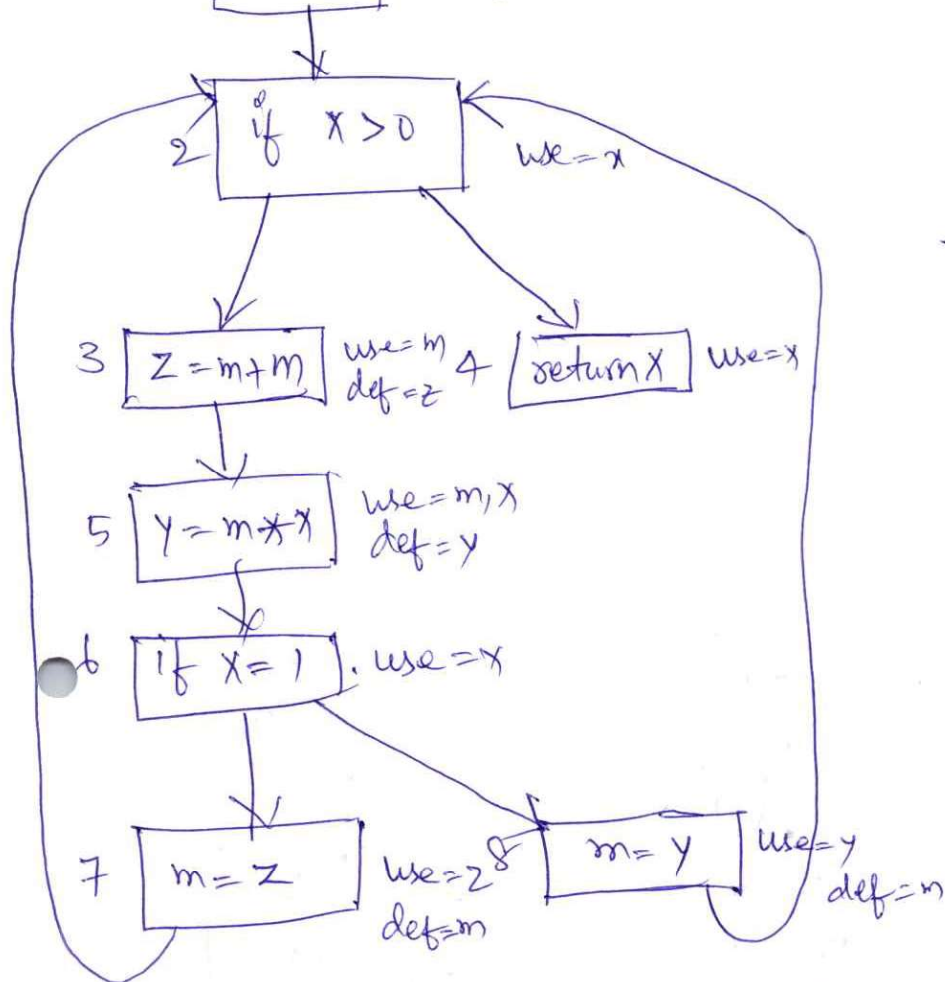
(3) For any change in the $\text{in}[B]$ do step 4 to 6.

(4) For each block B do steps 5 and 6.

(5) $\text{out}[B] = \bigcup_S \text{in}[S]$

(6) $\text{in}[B] = \text{Use}[B] \cup (\text{out}(B) - \text{def}(B))$

1 $m=1$ def = m.



Initially

	in	out
1	ϕ	ϕ
2	.	.
3	.	.
4	.	.
5	.	.
6	.	.
7	.	.
8	ϕ	ϕ

After 1st pass

	in	out
1	ϕ	ϕ
2	x	ϕ
3	m	ϕ
4	m , x	ϕ
5	m, x	ϕ
6	x	ϕ
7	x , z	x
8	x, y	x

Pass 2

	in	out
1	x	x
2	m, x	m, x
3	m, x	m, x
4	m , x	x
5	m, x	x
6	x, y, z	x, y, z
7	m, x	m, z
8	x, y	m, x

Redundant Common sub expression elimination

→ Algorithm

- (1) Discover the evaluations of $b+c$ that reach in the block containing statement S .
- (2) Create new variable ' m '.
- (3) Replace each statement $d = b+c$, which is obtained in step 1.

$$m = b+c$$

$$d = m$$

- (4) Replace statement S by $a = m$.

Eg:-

$$\begin{array}{l} t_1 = 4 * k \\ t_2 = a[t_1] \end{array}$$

$$\begin{array}{l} t_5 = 4 * k \\ t_6 = a[t_5] \end{array}$$

⇒

$$\begin{array}{l} m = 4 * k \\ t_1 = m \\ t_2 = a[t_1] \end{array}$$

$$\begin{array}{l} t_5 = m \\ t_6 = a[t_5] \end{array}$$

⇒

$$\begin{array}{l} (12) = 4 * k \\ (15) = a[(12)] \end{array}$$

$$\begin{array}{l} t_5 = (12) \\ t_6 = (15) \end{array}$$

Copy propagation

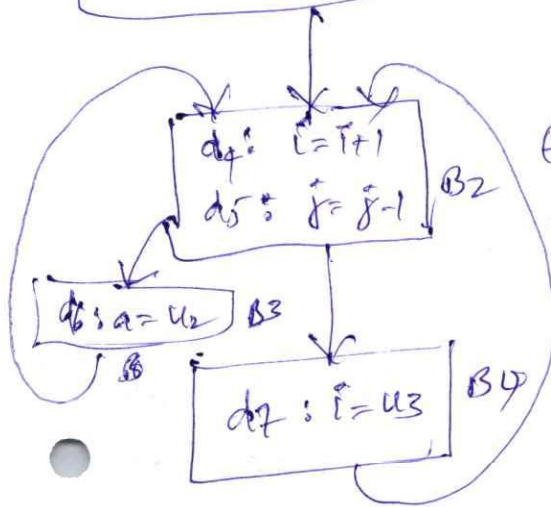
→ The assignment statement is called Copy statement.

Algorithm:-

- (1) Determine the statements in which x is used. These statements should be reachable from definition x .
- (2) There should not be any definition of x or y occur prior to use of x in that block containing S .
- (3) If S satisfies the condition mentioned in step (2) then remove S and replace all uses of x found in (1) by y .

$d_1: i = m-1$
 $d_2: j = n$
 $d_3: a = u_1$

$Gen(B_1) = \{1110000\}$
 $Kill(B_1) = \{0001111\}$



$Gen(B_2) = \{0001100\}$
 $Kill(B_2) = \{1100001\}$

$Gen(B_3) = \{0000010\}$
 $Kill(B_3) = \{0010000\}$

$Gen(B_4) = \{0000001\}$
 $Kill(B_4) = \{1001000\}$

	Initial		Pass	
	IN	Out	IN	Out
B1	ϕ	$\{1110000\}$	$\{ \phi \}$	$\{1110000\}$
B2	ϕ	$\{0001100\}$	$\{1110011\}$	$\{0001100\}$
B3	ϕ	$\{0000010\}$	$\{0001100\}$	$\{0001110\}$
B4	ϕ	$\{0000001\}$	$\{0001100\}$	$\{0000101\}$

3-Address Code

```

x = 1
while (x <= 10)
{
    y = y * 2;
    x = x + 1;
}
    
```

```

for (x = 1; x <= 10; x++)
{
    y = y * 2;
}
    
```

```

1. x = 1
2. if x > 10 goto 8
3. t1 = y * 2
4. y = t1
5. t2 = x + 1
6. x = t2
7. goto 2
8.
    
```

a[10]

```

1. t1 = addr(a)
2. t2 = 10 * 4
3. t3 = t1[t2]
    
```

```

int a[10], b[10], i, dp = 0;
for (i = 0; i < 10; i++)
{
    dp = a[i] * b[i];
}
    
```

```

1. dp = 0
2. i = 0
3. if i >= 10 goto 16
4. t1 = addr(a)
5. t2 = i * 4
6. t3 = t1[t2]
7. t4 = addr(b)
8. t5 = i * 4
9. t6 = t4[t5]
9. t6 = t4[t5]
10. t7 = t3 * t6
11. t8 = dp + t7
12. dp = t8
13. t9 = i + 1
14. i = t9
15. goto 3
16.
    
```

Switch(i+j)

```

{
    Case 1:
        x = y * 8
    Case 2:
        x = 8 / 2;
    default:
        x = x * 5
        break;
}
    
```

```

1. t = i + j
2. goto 9
3. t1 = y * 8
4. x = t1
5. t2 = 8 / 2
6. x = t2
7. t3 = x * 5
8. x = t3
9. goto 13
10. if t == 1 goto 3
11. if t == 2 goto 5
12. goto 7
    
```

a[i][j] m x n

```

[ 00 01 02
  10 11 12
  20 21 22
  30 31 32 ]
    
```

```

1. t1 = add(a)
2. t2 = i * n
3. t3 = t1 + t2
4. t4 = 4 * t3
5. t5 = t1[t4]
    
```

(3 * 3 + 1) * 4 ✓
(i * n + j) * 4 ✓

Data flow Analysis (DFA).

→ Data flow property property represents the certain information regarding usefulness of the data items for optimization.

(1) Available expressions (2) Reaching definitions.

(3) Live variables (3) Busy variables.

→ DFA is a process of computing values of data flow properties.

(a) Definition point contains def

(b) Reference point refers to data items.

(c) Evaluation point some evaluation exp is given.

$w_1 = x = 3 \rightarrow \text{def}$

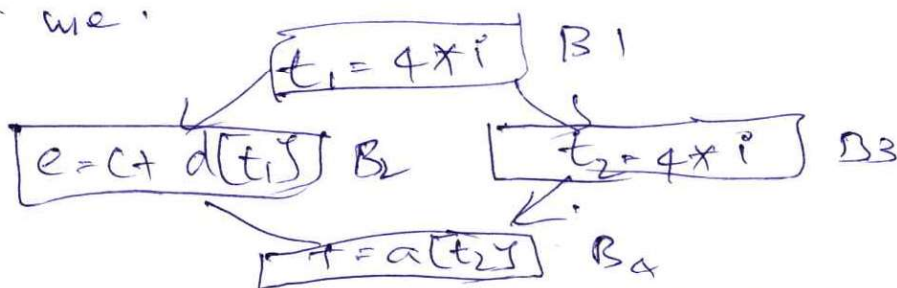
$w_2 = y = x \rightarrow \text{ref}$

$w_3 = z = a * b \rightarrow \text{eval}$

(1) Available expression:- An exp $x+y$ is available at program point 'w' if & only if all paths are reaching to w.

(a) The expression $x+y$ is said to be available at eval point.

(b) The expression $x+y$ is said to be available if no definition of any operand of exp (either x or y) follows its last eval only the path. In other words, if neither of the two operand get modified before their use.



Adv:- Used in elimination of CSE.

Basic block example

1. $t = i + j$

2. goto 10

3. $t_1 = j * 2$

4. $x = t_1$

5. $t_2 = x / 2$

6. $x = t_2$

7. $t_3 = x * 5$

8. $x = t_3$

9. goto 13

10. if $t == 1$ goto 2

11. if $t == 2$ goto 5

12. goto 7

* 1. $f = 1$

2. $x = 2$

B1

* 3. if $i \leq x$ goto 8

B2

* 4. $f = f * x$

5. $t_1 = i + 1$

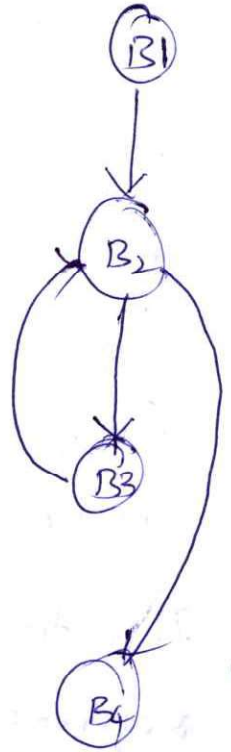
6. $i = t_1$

7. goto 3

B3

* 8. end

B4



Control flow graph

* 1. $i = 1$ [B1]

* 2. $j = 1$ [B2]

* 3. $t_1 = 10 * i$ [B3]

4. $t_2 = t_1 + j$

5. $t_3 = 8 * 2$

6. $t_4 = t_3 - 88$

7. $a[t_4] = 0.0$

8. $j = j + 1$

9. if $j < 10$ goto 2

* 10. $i = i + 1$ [B4]

11. if $i \leq 10$ goto 2

* 12. $i = 1$ [B5]

* 13. $t_5 = i - 1$ [B6]

14. $t_6 = 88 * t_5$

15. $a[t_6] = 1.0$

16. $x = i + 1$

17. if $i \leq 10$ goto 13

SYNTAX ANALYSIS

ROLE OF THE PARSER

Parser obtains a string of tokens from the lexical analyzer and verifies that it can be generated by the language for the source program. The parser should report any syntax errors in an intelligible fashion. The two types of parsers employed are:

1. Top down parser: which build parse trees from top(root) to bottom(leaves)
2. Bottom up parser: which build parse trees from leaves and work up the root.

Therefore there are two types of parsing methods— top-down parsing and bottom-up parsing

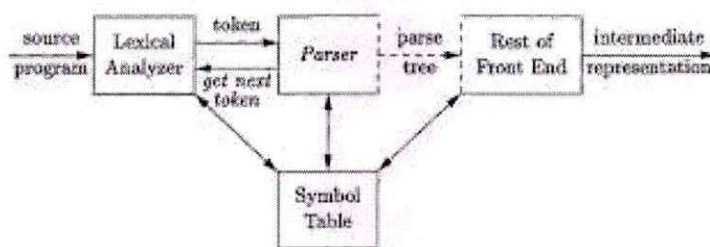


Figure 4.1: Position of parser in compiler model

TOP-DOWN PARSING

A program that performs syntax analysis is called a parser. A syntax analyzer takes tokens as input and output error message if the program syntax is wrong. The parser uses symbol-look-ahead and an approach called top-down parsing without backtracking. Top-down parsers check to see if a string can be generated by a grammar by creating a parse tree starting from the initial symbol and working down. Bottom-up parsers, however, check to see a string can be generated from a grammar by creating a parse tree from the leaves, and working up. Early parser generators such as YACC creates bottom-up parsers whereas many of Java parser generators such as JavaCC create top-down parsers.

RECURSIVE DESCENT PARSING

Typically, top-down parsers are implemented as a set of recursive functions that descent through a parse tree for a string. This approach is known as recursive descent parsing, also known as LL(k) parsing where the first L stands for left-to-right, the second L stands for

leftmost-derivation, and k indicates k -symbol lookahead. Therefore, a parser using the single symbol look-ahead method and top-down parsing without backtracking is called LL(1) parser. In the following sections, we will also use an extended BNF notation in which some regulation expression operators are to be incorporated.

A syntax expression defines sentences of the form $S \rightarrow S_1 S_2 S_3$, or a syntax of the form $S \rightarrow S_1 S_2 S_3$ defines sentences that consist of a sentence of the form S_1 followed by a sentence of the form S_2 followed by a sentence of the form S_3 . A syntax of the form $S \rightarrow S_1 S_2 S_3$ defines zero or one occurrence of the form S_1 . A syntax of the form $S \rightarrow S_1 S_2 S_3$ defines zero or more occurrences of the form S_1 .

A usual implementation of an LL(1) parser is:

- initialize its data structures,
- get the lookahead token by calling scanner routines, and
- call the routine that implements the start symbol.


```

proc syntaxAnalysis()
begin
initialize(); // initialize global data and structures
nextToken(); // get the lookahead token
program(); // parser routine that implements the start
symbol end;

```

FIRST AND FOLLOW

To compute FIRST (X) for all grammar symbols X, apply the following rules until no more terminals or ϵ can be added to any FIRST set.

1. If X is terminal, then FIRST(X) is {X}.
2. If $X \rightarrow \epsilon$ is a production, then add ϵ to FIRST(X).
3. If X is nonterminal and $X \rightarrow Y_1 Y_2 \dots Y_k$ is a production, then place a in FIRST(X) if for some i, a is in FIRST(Y_i) and ϵ is in all of FIRST(Y_1), ..., FIRST(Y_{i-1}) that is, $Y_1 \dots Y_{i-1} \Rightarrow^* \epsilon$. If ϵ is in FIRST(Y_j) for all $j=1, 2, \dots, k$, then add ϵ to FIRST(X). For example, everything in FIRST(Y_j) is surely in FIRST(X). If Y_1 does not derive ϵ , then we add nothing more to FIRST(X), but if $Y_1 \Rightarrow^* \epsilon$, then we add FIRST(Y_2) and so on.

To compute the FIRST(A) for all nonterminals A, apply the following rules until nothing can be added to any FOLLOW set.

1. Place \$ in FOLLOW(S), where S is the start symbol and \$ is the input right endmarker.
2. If there is a production $A \Rightarrow aBs$ where FIRST(s) except ϵ is placed in FOLLOW(B).
3. If there is a production $A \rightarrow aB$ or a production $A \rightarrow aBs$ where FIRST(s) contains ϵ , then everything in FOLLOW(A) is in FOLLOW(B).

Consider the following example to understand the concept of First and Follow. Find the first and follow of all nonterminals in the Grammar-

$E \rightarrow TE' \mid E' \rightarrow +TE' \mid e$ $T \rightarrow FT' \mid T' \rightarrow *FT' \mid e$

$F \rightarrow (E) \mid id$ Then:

$FIRST(E) = FIRST(T) = FIRST(F) = \{ (, id \}$

$FIRST(E') = \{ +, e \}$ $FIRST(T') = \{ *, e \}$

$FOLLOW(E) = FOLLOW(E') = \{), \$ \}$

$FOLLOW(T) = FOLLOW(T') = \{ +,), \$ \}$

$FOLLOW(F) = \{ +, *,), \$ \}$

For example, id and left parenthesis are added to $FIRST(F)$ by rule 3 in definition of $FIRST$ with $i=1$ in each case, since $FIRST(id) = (id)$ and $FIRST('(') = \{ (\}$ by rule 1. Then by rule 3 with $i=1$, the production $\rightarrow FT'$ implies that id and left parenthesis belong to $FIRST(T)$ also.

To compute $FOLLOW$, we put $\$$ in $FOLLOW(E)$ by rule 1 for $FOLLOW$. By rule 2 applied to production $F \rightarrow (E)$, right parenthesis is also in $FOLLOW(E)$. By rule 3 applied to production $E \rightarrow TE'$, $\$$ and right parenthesis are in $FOLLOW(E')$.

CONSTRUCTION OF PREDICTIVE PARSING TABLES

For any grammar G , the following algorithm can be used to construct the predictive parsing table. The algorithm is

Input : Grammar G

Output : Parsing table M World Method

1. For each production $A \rightarrow a$ of the grammar, do steps 2 and 3.
2. For each terminal a in $FIRST(a)$, add $A \rightarrow a$ to $M[A, a]$.
3. If e is in $First(a)$, add $A \rightarrow a$ to $M[A, b]$ for each terminal b in $FOLLOW(A)$. If e is in $FIRST(a)$ and $\$$ is in $FOLLOW(A)$, add $A \rightarrow a$ to $M[A, \$]$.
4. Make each undefined entry of M be error.

3.6.LL(1) GRAMMAR

The above algorithm can be applied to any grammar G to produce a parsing table M . For some Grammars, for example if G is left recursive or ambiguous, then M will have at least one multiply-defined entry. A grammar whose parsing table has no multiply defined entries is said to be LL(1). It can be shown that the above algorithm can be used to produce for every LL(1) grammar G a parsing table M that parses all and only the sentences of G . LL(1) grammars have several distinctive properties. No ambiguous left recursive grammar can be LL(1). There remains a question of what should be done in case of multiply defined entries. One easy solution is to eliminate all left recursion and left factoring, hoping to produce a grammar which will produce no multiply defined entries in the parse tables. Unfortunately there are some grammars which will give an LL(1) grammar after any kind of alteration. In general, there are no universal rules to convert multiply defined entries into single valued entries without affecting the language recognized by the parser.

The main difficulty in using predictive parsing is in writing a grammar for the source language such that a predictive parser can be constructed from the grammar. Although left recursion elimination and left factoring are easy to do, they make the resulting grammar hard to read and difficult to use the translation purposes. To alleviate some of this difficulty, a common organization for a parser in a compiler is to use a predictive parser for control

constructs and to use operator precedence for expressions. However, if an LR parser generator is available, one can get all the benefits of predictive parsing and operator precedence automatically.

3.7.ERROR RECOVERY IN PREDICTIVE PARSING

The stack of a nonrecursive predictive parser makes explicit the terminals and nonterminals that the parser hopes to match with the remainder of the input. We shall therefore refer to symbols on the parser stack in the following discussion. An error is detected during predictive parsing when the terminal on top of the stack does not match the next input symbol or when nonterminal A is on top of the stack, a is the next input symbol, and the parsing table entry $M[A, a]$ is empty.

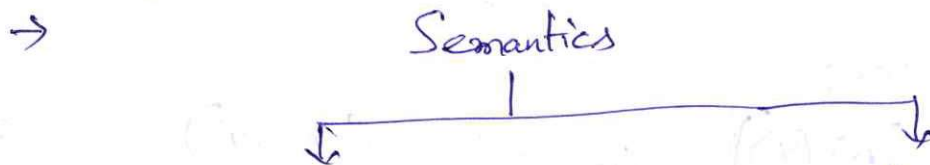
Panic-mode error recovery is based on the idea of skipping symbols in the input until a token in a selected set of synchronizing tokens appears. Its effectiveness depends on the choice of synchronizing set. The sets should be chosen so that the parser recovers quickly from errors that are likely to occur in practice. Some heuristics are as follows

As a starting point, we can place all symbols in $FOLLOW(A)$ into the synchronizing set for nonterminal A . If we skip tokens until an element of $FOLLOW(A)$ is seen and

pop A from the stack, it is likely that parsing can continue.

- ✦ It is not enough to use FOLLOW(A) as the synchronizing set for A. For example, if semicolons terminate statements, as in C, then keywords that begin statements may not appear in the FOLLOW set of the nonterminal generating expressions. A missing semicolon after an assignment may therefore result in the keyword beginning the next statement being skipped. Often, there is a hierarchical structure on constructs in a language; e.g., expressions appear within statements, which appear within blocks, and so on. We can add to the synchronizing set of a lower construct the symbols that begin higher constructs. For example, we might add keywords that begin statements to the synchronizing sets for the nonterminals generating expressions.
- ✦ If we add symbols in FIRST(A) to the synchronizing set for nonterminal A, then it may be possible to resume parsing according to A if a symbol in FIRST(A) appears in the input.
- ✦ If a nonterminal can generate the empty string, then the production deriving ϵ can be used as a default. Doing so may postpone some error detection, but cannot cause an error to be missed. This approach reduces the number of nonterminals that have to be considered during error recovery.
- ✦ If a terminal on top of the stack cannot be matched, a simple idea is to pop the terminal, issue a message saying that the terminal was inserted, and continue parsing. In effect, this approach takes the synchronizing set of a token to consist of all other tokens.

- Produces annotated syntax tree, after validating semantics of the program.
- Processors can't handle context sensitive features of programming languages.



Static Semantics

- (Do not depend on runtime system)
depends only program language definition

Eg:- (1) Variables are declared before use

(2) types of match on both sides of assignments

(3) Parameter types and number match in declaration and use. etc.

Dynamic Semantics.

- (depends on runtime and need to be checked only at execution time).

Eg:- (1) whether an overflow will occur during an arithmetic operation

(2) Array limits will be crossed during execution

(3) recursion will cross stack limits etc

→ During semantic analysis "type" information^{etc.} is stored in the "symbol table" or the syntax tree (at nodes).

→ Usually symbol table is preferable than syntax tree because it occupies less space.

→ Type of information that is stored in symbol table is

- (a) Types of variables, function parameters, array dimensions etc.
- (b) Used not only for semantic validation but also for subsequent phases of compilation.

→ Static semantics of PL can be specified using attribute grammars.

→ Attribute grammars are extensions of CFG.

Terminology to understand attribute grammars

→ Every symbol (X) of a grammar (Variables and terminals) is associated with set of attributes (denoted $X.a$, $X.b$ etc).

→ Two types of attributes:

Inherited attribute $[AI(X)]$, Synthesized $[AS(X)]$

→ Each attribute takes a value from its specific domain; which is its type.

→ Typical domains of attributes (integers, reals, chars, strings etc).

Attribute Computation Rules

→ A production has a set of attribute computation rules.

→ Rules are provided for the computation of

(a) Synthesized attributes of the LHS non-terminal of P .

(b) Inherited attributes of the RHS nonterminal of P .

→ These rules can use attributes of symbols from the production 'P' only.

* Rules are strictly local to the production P (no side effects)

→ Restrictions on the rules define different types of attribute grammars.

* L-attribute grammars (L - stands for left to right)

* S-attribute grammars (S - Synthesized)

→ Synthesized attributes are Computed in a bottom-up manner from leaves upwards.

* Always synthesized from the attribute values of its children of the node.

* Leaf nodes (terminals) have synthesized attributes initialized by the lexical analyzer.

* An AG with only synthesized attributes is an S-attributed grammar.

→ Inherited attributes flow down from ^{the} parent or siblings to the node.

eg:- $S \rightarrow ABC$; $A \rightarrow aA/a$; $B \rightarrow bB/b$; $C \rightarrow cC/c$.
 $L = \{a^n b^n c^n \mid n \geq 1\}$

① $S \rightarrow ABC$ { $S.\text{equal} \uparrow = \text{if } A.\text{Count} \uparrow = B.\text{Count} \uparrow \& B.\text{Count} \uparrow = C.\text{Count} \uparrow \text{ then True else False}$ }

② $A_1 \rightarrow a A_2$ { $A.\text{Count} \uparrow := A_2.\text{Count} + 1$ }

③ $A \rightarrow a$ { $A.\text{Count} \uparrow := 1$ }

④ $B_1 \rightarrow b B_2$ { $B.\text{Count} \uparrow := B_2.\text{Count} + 1$ }

⑤ $B \rightarrow b$ { $B.\text{Count} \uparrow := 1$ }

⑥ $C_1 \rightarrow c C_2$ { $C.\text{Count} \uparrow := C_2.\text{Count} + 1$ }

⑦ $C \rightarrow c$ { $C.\text{Count} \uparrow := 1$ }

→ We defined Attributed grammar (AG) based on CFG to generate $L(G) = \{a^n b^n c^n \mid n \geq 1\}$.

$AS(S) = \{\text{equal} \uparrow : \{T, F\}\}$

$AS(A) = AS(B) = AS(C) = \{\text{Count} \uparrow : \text{Integers}\}$.

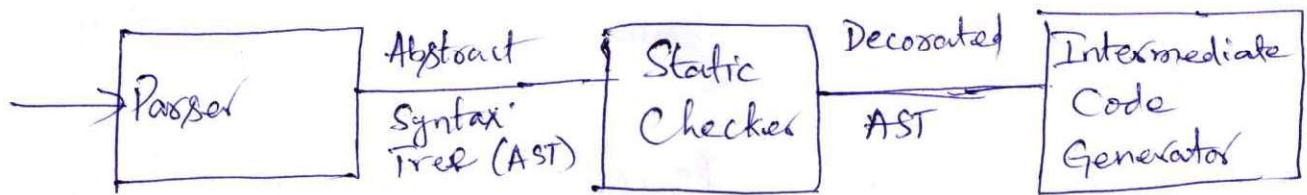
$$E \rightarrow E + T \quad \{ E.val = E_1.val + T.val \} \quad \{ \text{printf}(" + "); \}$$

$$E \rightarrow T \quad \{ E.val = T.val \} \quad \{ \}$$

$$T \rightarrow T * F \quad \{ T.val = T_1.val * F.val \} \quad \{ \text{printf}(" * "); \}$$

$$T \rightarrow F \quad \{ T.val = F.val \} \quad \{ \}$$

$$F \rightarrow \text{num} \quad \{ F.val = \text{num.val} \} \quad \{ \text{printf}(" \text{num.val} "); \}$$



Static Check

(1) Type Check :- Operator applied to incompatible operand.

Eg:- $2 \% 4.5 \rightarrow \text{error}$.

(2) Flow Control :- Statement that causes flow of Control to leave a Construct must have some place to which to transfer flow of Control.

Eg:-

```

while (1)
{
    if (Cond)
        break;
}
  
```

→ if we write break statement outside of while loop then it generates an error.

(3) Uniqueness Check :- There are situation where object must be defined exactly once.

Eg:- goto label;

→ Here label must be unique name.

(4) Name related Check :- Some times, the same name must appear two (or) more time.

Eg:-

```

main()
{
    add(2, 3);
}
  
```

ints as parameters.

→ There must be 'add' function definition with two,

Type Checking

It is a process

→ To ensure that operations are used with correct ^{data} type.

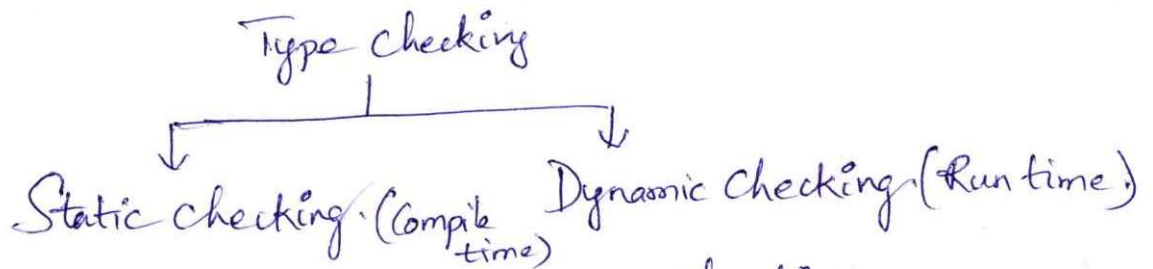
→ Allows the programmer to limit what types may be used in certain circumstances.

→ Computes values to all types.

→ Determines whether these values are used in an appropriate manner.

→ Simplest Situation: Check types of objects and report a type error in case of a violation.

→ More Complex: incorrect types may be corrected (type coexisting)



Static checking

- (1) Type checking done at Compile time.
- (2) Properties can be verified before program run.
- (3) Can catch many common errors.
- (4) Desirable when faster execution is important.

Dynamic checking

- (1) Performed during program execution.
- (2) Permits programmers to be less concerned with types.
- (3) Mandatory in some situations such as, array bounds check.
- (4) More robust and clearer code.

Construction of Basic block

Algorithm: Partition into basic block

Input:- A sequence of three address statements.

Output:- A list of basic blocks with each three-address statements in exactly one block.

Method:-

1. We first determine the set of leaders. Rules are.

(a) The first statement is a leader.

(b) Any statement which is a target statement of Conditional or unconditional statements is a leader.

(c) Any statement which immediately follows a Conditional 'goto' is a leader.

2. From one leader statement to the just prior statement of the next leader (if no such leader statement is present then upto the last statement) will form a basic block.

If there is any statement which has not been included to any basic block can be removed and discarded.

Types of 3 address Codes

1. $a = b \text{ op } c$

2. $a = \text{op } b$

3. $a = b$

4. if $a \text{ rel op } b$ goto L

5. goto L .

6. (i) $A[i] = a$

(ii) $b = A[j]$

(iii) $a = *p$

(iv) $b = \&c$

(v) $*c = a$

B) $x = 1$

do { $x = x + 1$

} while ($x \leq 5$);

(3) 1. $x = 1$

2. $t_1 = x + 1$

3. $x = t_1$

4. if $x \leq 5$ goto 2.

5.

(4) $x = 1$

while ($x \leq 10$)

{ $y = y * 2$

$x = x + 1$

4) 1. $x = 1$

2. if $x > 10$ go 8

3. $t_1 = y * 2$

4. $y = t_1$

5. $t_2 = x + 1$

6. $x = t_2$

7. goto 2

8.

9. goto 12

10. $t_3 = l * 5$

11. $l = t_3$

12.

(5) Switch ($i+j$)

{ Case 1:

$x = y * z;$

break;

Case 2:

$x = s/2;$

break;

default:

$l = l * 5;$

5) 1. $t = i+j$

2. if $t \neq 1$ goto 6

3. $t_1 = y * 8$

4. $x = t_1$

5. goto 12

6. if $t \neq 2$ goto 10

7. $t_2 = s/2$

8. $x = t_2$

$x = 5$
(2) if ($x < 5$)
 $z = z + 2$
else
 $y = y * 2$

(1) $x = 1;$
if ($x > 1$)
 $y = y + 3$

1. $x = 1$
2. if ($x \leq 1$) goto 5
3. $t = y + 3$
4. $y = t$.

1. $x = 5$
2. if $x \geq 5$ goto 6
3. $t_1 = z + 2$
4. $z = t_1$
5. goto 8
6. $t_2 = y * 2$
7. $y = t_2$
8.

Syntax Directed Translation

→ Attach rules (or) program fragments to productions in a grammar.

→ Syntax Directed Definition (SDD):

→ SDD specifies the values of attributes by associating semantic rules with the grammar productions.

Eg:- $E_1 \rightarrow E_2 + T \quad \{ E_1 \cdot \text{Code} = E_2 \cdot \text{Code} \parallel T \cdot \text{Code} \parallel '+' \}$

→ Syntax Directed Translation (SDT):

→ SDT embeds program fragments (also called as semantic actions) within productions bodies.

→ The position of the action defines the order in which the action is executed.

Eg:- $E \rightarrow E + T \quad \{ \text{print '+'} \}$

→ SDD is easier to read & specify.

→ SDT is more efficient & easy for implementation.

Eg:- SDD Vs SDT scheme (Infix to postfix Conversion)

<u>SDT Scheme</u>	<u>SDD</u>
$E \rightarrow E + T \quad \{ \text{print '+'} \}$	$E \rightarrow E + T \quad \{ E \cdot \text{Code} = E \cdot \text{Code} \parallel T \cdot \text{Code} \parallel '+' \}$
$E \rightarrow E - T \quad \{ \text{print '-'} \}$	$E \rightarrow E - T \quad \{ E \cdot \text{Code} = E \cdot \text{Code} \parallel T \cdot \text{Code} \parallel '-' \}$
$E \rightarrow T$	$E \rightarrow T \quad \{ E \cdot \text{Code} = T \cdot \text{Code} \}$
$T \rightarrow 0 \quad \{ \text{print '0'} \}$	$T \rightarrow 0 \quad \{ T \cdot \text{Code} = '0' \}$
$T \rightarrow 1 \quad \{ \text{print '1'} \}$	$T \rightarrow 1 \quad \{ T \cdot \text{Code} = '1' \}$
$T \rightarrow 9 \quad \{ \text{print '9'} \}$	$T \rightarrow 9 \quad \{ T \cdot \text{Code} = '9' \}$

Syntax Directed Definitions (SDD)

- SDD is a Context free grammar together with attributes and semantic rules.
- Attributes are associated with grammar symbols and rules are associated with productions.
- Attributes may be of kind: numbers, types, table reference, string etc.
- Attributes are of two types Inherited and Synthesized attributes.

Synthesized attribute:-

A Synthesized attribute at node 'N', is defined only in terms of attributes values at the children of 'N' and at 'N' itself.

- For a nonterminal 'A' at a parse tree node 'N' is defined by a semantic rule associated with the production at 'N'.
- Note that the production must have 'A' as its head.

Inherited attribute:-

An inherited attribute at node 'N' is defined only in terms of attribute values at N's parent, N's siblings and N itself.

- For nonterminal 'B' at a parse tree node 'N' is defined by semantic rule associated with the production at the parent of 'N'.
- Note that the production must have B as a symbol in its body.

Types of SDD's (or) SDT's

→ SDD's are classified into two types based on type of attributes the SDD includes.

(a) S-attributed SDD (b) L-attributed SDD.

S-attributed SDD

→ An SDD is S-attributed, if every attribute is synthesized.

L-attributed SDD

→ An SDD is L-attributed, if it uses both synthesized and inherited attributes with restriction of not taking values from right siblings.

→ S-attributed SDT [Postfix SDT]

(1) Uses only synthesized attributes.

(2) Semantic actions are placed at right end of production.

(3) Attributes are evaluated during bottomup parsing.

(4) Computing synthesized attributes is simple.

L-attributed SDT

(1) Uses both inherited and synthesized attributes. Each inherited attribute is restricted to inherit either from parent(s) or from left sibling only.

(2) Semantic rules can be placed anywhere at the right side of a production.

(3) Attributes are evaluated by traversing parse tree depth first, left to right.

(4) Computing inherited attributes is complex.

Dependency Graphs

- A dependency graph depicts the flow of information among the attribute instances in a particular parse tree.
- An edge from one attribute instance to another means that the value of first is needed to compute the second.
- "Dependency graphs" are a useful tool for determining ~~the~~ an evaluation order for the attribute instances in a given parse tree.
- The topological sort of the dependency graph decides the evaluation order in a parse tree.

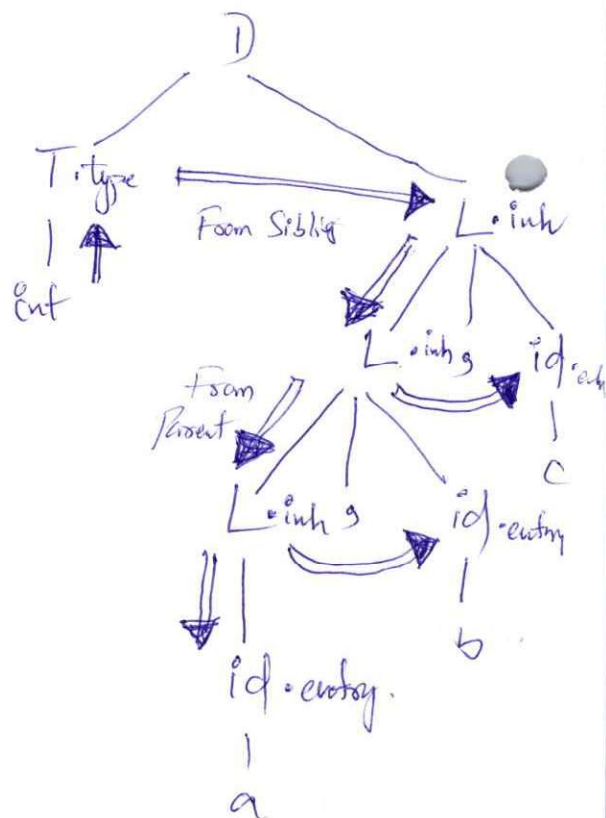
Eg:-

SDT

$D \rightarrow TL \quad \{ L.inh = T.type \}$
 $T \rightarrow int \quad \{ T.type = integer \}$
 $T \rightarrow float \quad \{ T.type = float \}$
 $L \rightarrow L_1, id \quad \{ L_1.inh = L.inh \} \quad \{ add(id.entry, L.inh) \}$
 $L \rightarrow id \quad \{ add(id.entry, L.inh) \}$

8500000056

Dependency graph for a string int a, b, c is



Eg:- SDD for type checking

Grammar Productions

Semantic Rules

$E \rightarrow E_1 + E_2$ { if $(E_1.type == E_2.type) \ \&\& \ (E_1.type == int)$
then $E.type = int$ else error; }

| $E_1 == E_2$ { if $(E_1.type == E_2.type) \ \&\& \ (E_1.type == int)$
boolean)) then $E.type = boolean$ else error; }

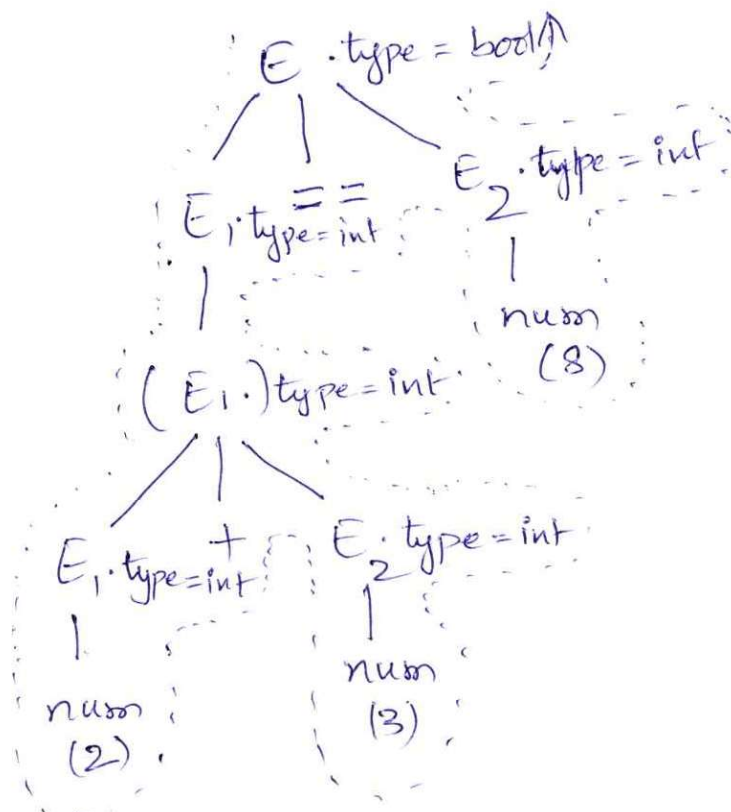
| (E_1) { $E.type = E_1.type$; }

| num { $E.type = int$; }

| true { $E.type = bool$; }

| false { $E.type = bool$; }

→ For a string $(2+3) == 8$, type checking will be done as follows.



Applications of SDT

Construction of Syntax Tree

SDD

$$E \rightarrow E_1 + T \quad \{ E.\text{node} = \text{new Node}('+', E_1.\text{node}, T.\text{node}) \}$$

$$E \rightarrow E_1 - T \quad \{ E.\text{node} = \text{new Node}('-', E_1.\text{node}, T.\text{node}) \}$$

$$E \rightarrow T \quad \{ E.\text{node} = \text{new Node}(), T.\text{node} \}$$

$$T \rightarrow (E) \quad \{ T.\text{node} = E.\text{node} \}$$

$$T \rightarrow \text{id} \quad \{ T.\text{node} = \text{new Leaf}(\text{id}, \text{id}.\text{entry}) \}$$

$$T \rightarrow \text{num} \quad \{ T.\text{node} = \text{new Leaf}(\text{num}, \text{num}.\text{val}) \}$$

→ Syntax tree for a string $a - 4 + c$.

Eg:- SDD for Inherited attributes

Grammar

$S \rightarrow TL$

$T \rightarrow \text{int}$

$T \rightarrow \text{float}$

$T \rightarrow \text{double}$

$L \rightarrow L_1, \text{id}$

$L \rightarrow \text{id}$

Semantic rules

$\{ L.in = T.type \}$

$\{ T.type = \text{int} \}$

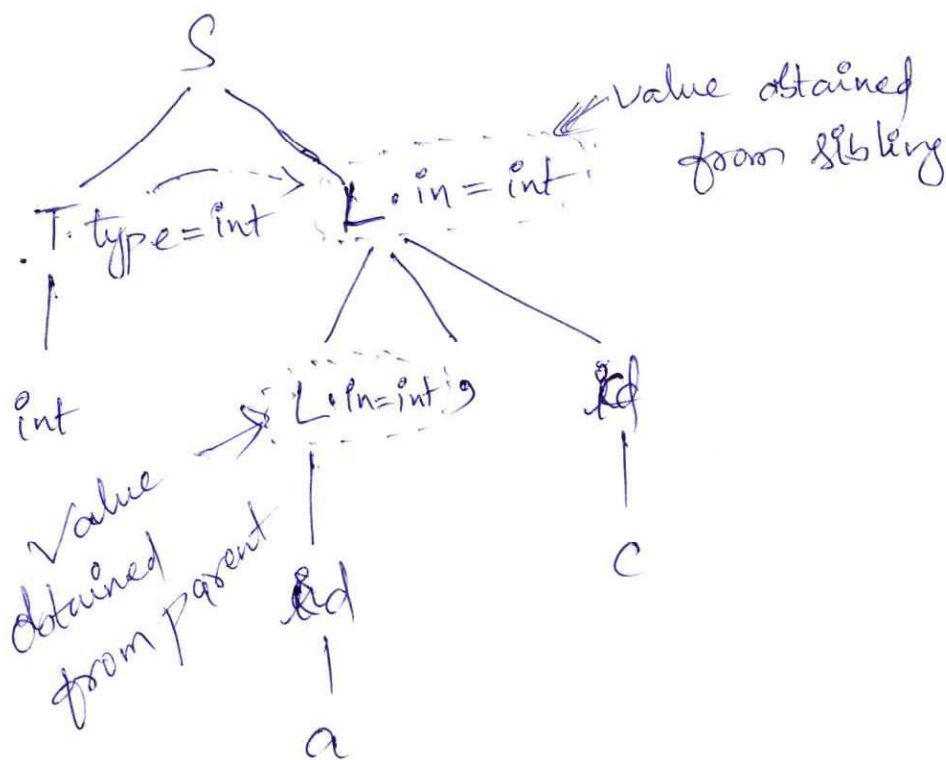
$\{ T.type = \text{float} \}$

$\{ T.type = \text{double} \}$

$\{ L.in = L.in$
 $\{ \text{Enter-type}(\text{id.entropy}, L.in) \}$

$\{ \text{Entry-type}(\text{id.entropy}, L.in) \}$

→ Parse tree for the stmt $\text{int } a, c$ is



Note:-

→ Inherited attribute values are computed when the node is visited during traversing of a parse tree.

Eg:- SDD for Synthesized Attributes

Grammar

Semantic rules

$S \rightarrow E \quad \{ \text{Print } (E.val) \}$

$E \rightarrow E_1 + T \quad \{ E.val = E_1.val + T.val \}$

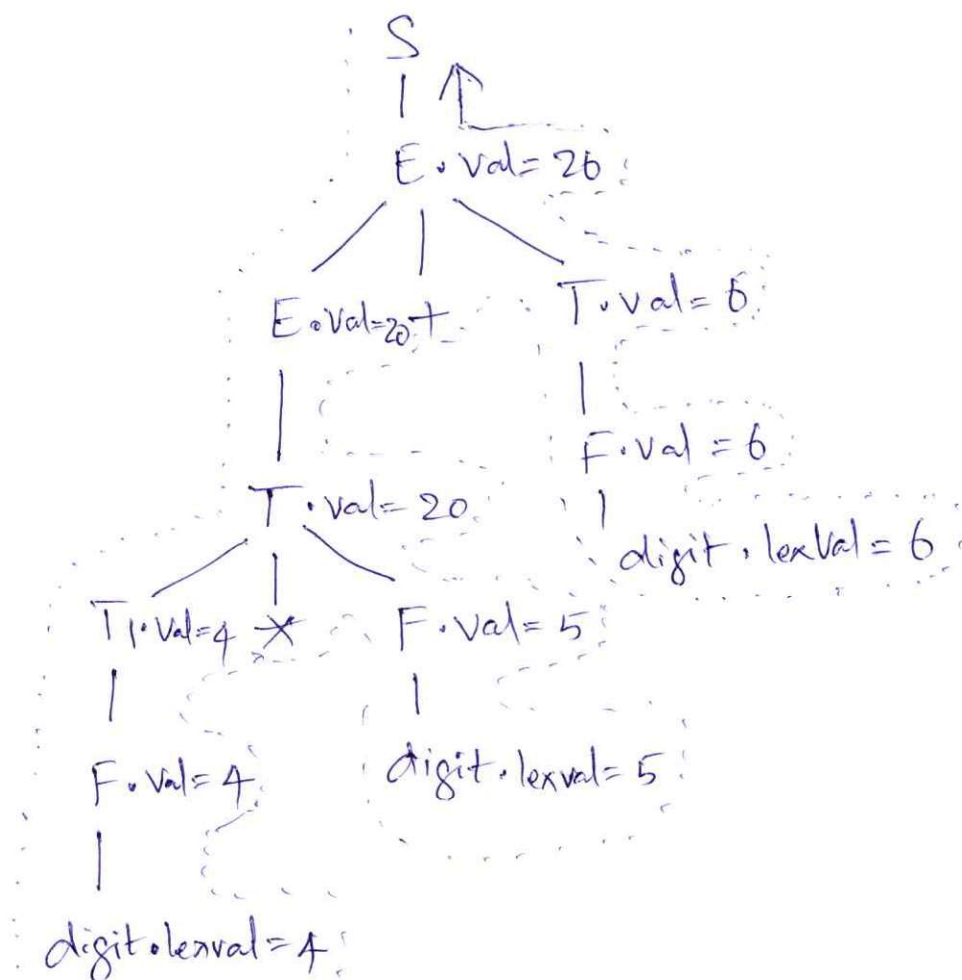
$E \rightarrow T \quad \{ E.val = T.val \}$

$T \rightarrow T_1 * F \quad \{ T.val = T_1.val * F.val \}$

$T \rightarrow F \quad \{ T.val = F.val \}$

$F \rightarrow \text{digit} \quad \{ F.val = \text{digit.lexval} \}$

→ Parse tree for string $4 * 5 + 6$



Note:-

→ Synthesized attributes are computed during reduction ^{by} ~~with~~ using corresponding semantic rules of that production.

Attribute Grammar

- Attribute grammar is a special form of Context free grammar, where some additional information (attributes) are appended to one (or) more of its non-terminals, in order to provide Context-sensitive information.
- Each attribute have well-defined domain values, such as integer, float, character, string etc.
- Attribute grammar is a medium to provide semantics to the Context free grammar.
- Attribute grammar (when viewed as parse tree) can pass values or information among the nodes of a tree.

Eg: $E \rightarrow E_1 + T \quad \{ E \cdot \text{value} = E_1 \cdot \text{value} + T \cdot \text{value} \}$

- The right part of the CFG contains the semantic rule that specify how grammar should interpreted.
- Here the values of non terminals E_1 and T are added together and result is copied into the nonterminal E .
- Based on the way the attributes get their values, they can be broadly divided into two categories
 - (a) Synthesized attributes.
 - (b) Inherited attributes.

Annotated Parse Tree

→ A parse tree, showing the values of its attributes is called an annotated parse tree.

→ The process of computing ~~values~~ the attribute values at the nodes is called annotating or decorating the parse tree.

Example :-

Productions

Semantic Rules

$L \rightarrow E n$

$\{ L.val = E.val \}$

$E \rightarrow E_1 + T$

$\{ E.val = E_1.val + T.val \}$

$E \rightarrow T$

$\{ E.val = T.val \}$

$T \rightarrow T_1 * F$

$\{ T.val = T_1.val * F.val \}$

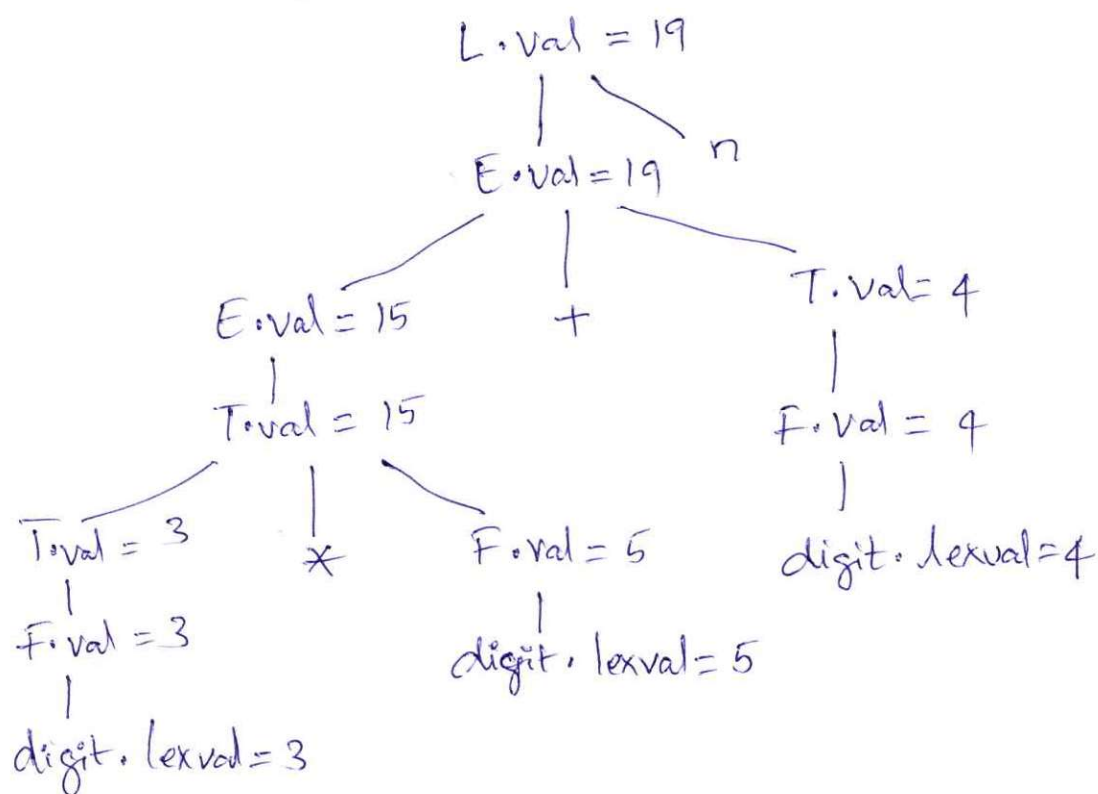
$T \rightarrow F$

$\{ T.val = F.val \}$

$F \rightarrow (E) \{ F.val = E.val \}$

$F \rightarrow \text{digit} \{ F.val = \text{digit.lexval} \}$

→ Annotated parse for the string $3 * 5 + 4 n$ is as follows.



Abstract Syntax Tree (AST)

- AST is a tree that represents the abstract syntactic structure of a language construct, where each interior and root node represents an operator and leaf nodes represent operands.
- Syntax trees are abstract (or) compact representation of parse trees. Syntax trees are also called as ASTs.

Parse Trees Vs Syntax Trees

Parse Tree

- (1) Parse tree is a graphical representation of the replacement process in a derivation.
- (2) Each interior node represents a grammar rule. Each leaf node represents a terminal.
- (3) Parse trees provide every characteristic information from the real syntax.
- (4) Parse trees are comparatively less dense than Syntax trees.

Syntax Tree

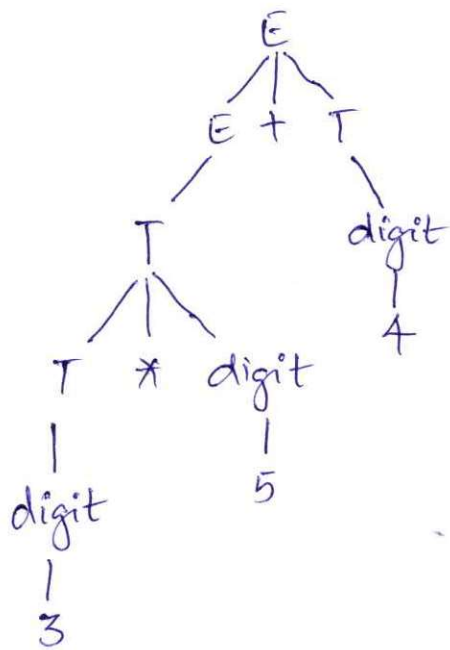
- (1) Syntax tree is compact form of parse tree.
- (2) Each interior node represents an operator. Each leaf node represents an operand.
- (3) Syntax trees do not provide every characteristic information from the real syntax.
- (4) Syntax trees are comparatively more dense than parse trees.

Note:-

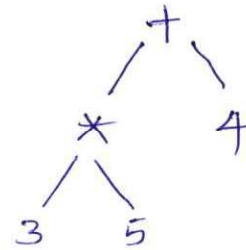
- * Syntax trees are called as AST because, they are abstract representation of the parse trees.
- * They do not provide every characteristic information from the real syntax.
For eg:- No rules nodes, no parenthesis etc.

Eg:- ① For expression $3 * 5 + 4$

Parse Tree

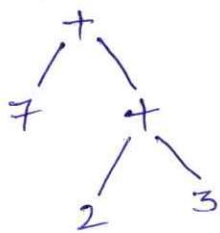


Syntax Tree

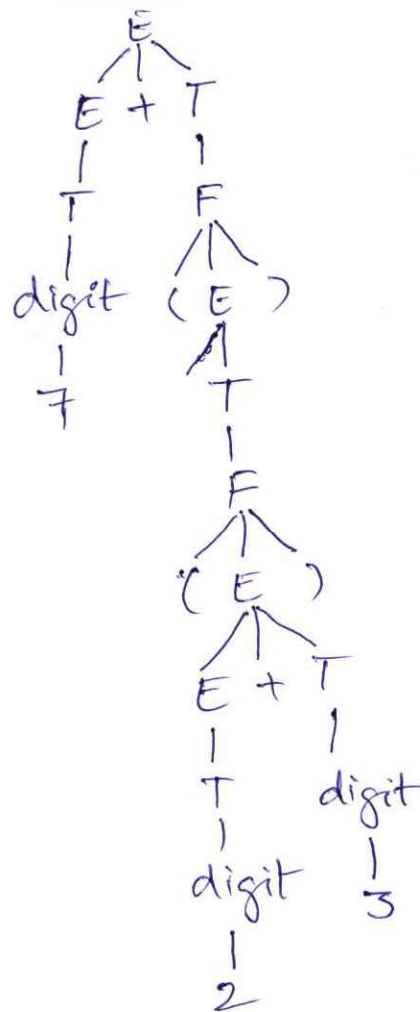


② For expression $7 + ((2+3))$

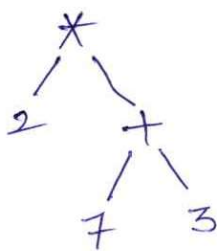
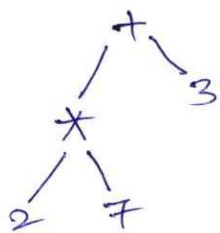
AST



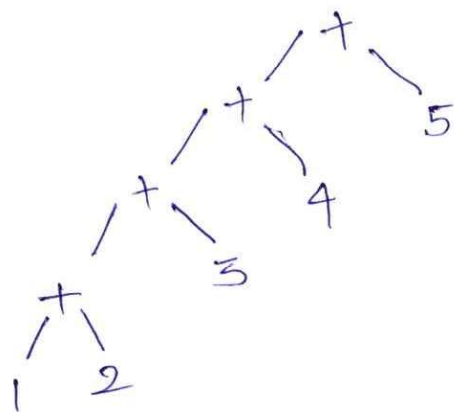
Parse Tree



③ Expressions $2 * 7 + 3$, $2 * (7 + 3)$

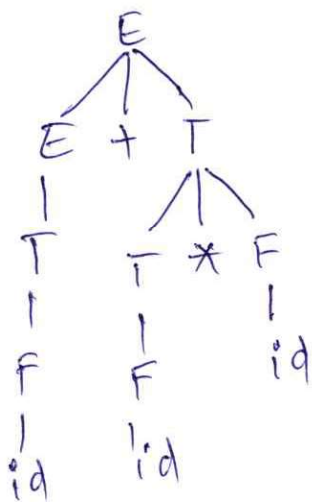


④ $1 + 2 + 3 + 4 + 5$

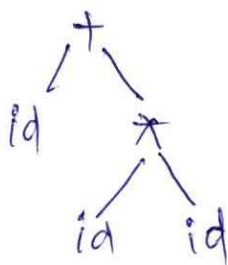


⑤ $id + id * id$

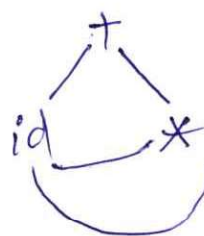
Parse Tree



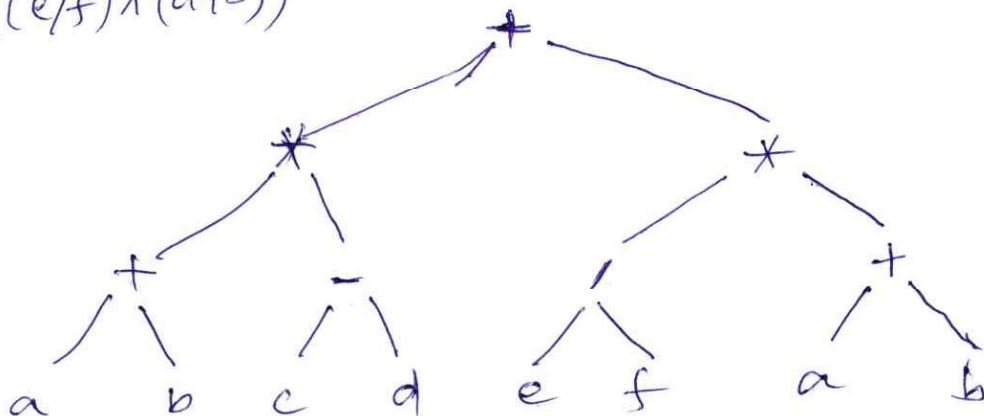
Syntax Tree



DAg



⑥ $(a+b) * (c-d) + ((e/f) * (a+b))$



Polish Notation (Prefix Notation)

- Polish notation refers to a notation in which the operator is placed before its two operands.
- Polish notation is used to express arithmetic, logic and algebraic expressions in parenthesis free notation that makes each equation shorter and easier to parse in terms of defining the evaluation priority of operators.
- Polish notations can be easily (or) readily parsed into an abstract syntax tree and stored in a stack.

Examples

① Polish notation for $(3+2) \times (5-1)$ is

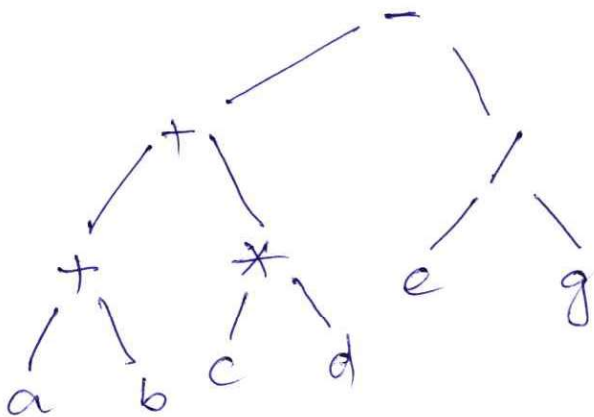
Sol: $\times + 3 2 - 5 1$

② Prefix notation for $a+b+c \times d - e/g$ is

Sol:

Syntax Tree

Prefix Notation



$- + + a b \times c d / e g$

Postfix Notation

- Postfix notation is ^{One of} the useful form of intermediate code, if the given language is expressions.
- Postfix notation is also called "suffix notation" and "reverse polish" notation.
- Postfix notation is a linear representation of Syntax tree.
- The postfix notation for an expression E can be defined inductively as follows.

- (1) If E is a variable or Constant, then the postfix notation for E is E itself.
- (2) If E is an expression of the form $E_1 \text{ op } E_2$, where op is any binary operator, then the postfix notation for E is $E_1' E_2' \text{ op}$, where E_1' and E_2' are the postfix notations for E_1 and E_2 respectively.
- (3) If E is a parenthesized expression of the form (E_1) , then the postfix notation for E is the same as the postfix notation for E_1 .

Eg:- (1) $(A+B \times C) / (D \times E - (F+G) / (H+I))$

$$ABC \times + DE \times FG + HI + / - /$$

(2) $(a-b) \times (c+d) + (a-b)$

$$ab - cd + ab - \times +$$

Three Address Code

- Three address code is an intermediate code, which is easy to generate and can be easily converted to machine code.
- Each three address code instruction has at most three operands and one operator on the right hand side of an instruction.

Advantages of Intermediate Code

- (1) A Compiler for different machines can be created by attaching different backend to the existing front ends of each machine.
 - (2) A Compiler for different source languages (on the same machine) can be created by providing different front ends for corresponding source languages to existing back end.
 - (3) A machine independent code optimizer can be applied to intermediate code in order to optimize the code generation.
- Three address code will help in code optimization.

General form of three address code

$$a = b \text{ op } c$$

where

→ a, b, c are the operands that can be names, constants or compiler generated temporary variables.

→ 'op' represents operator.

Eg:-

(1) $a = b + c$

(2) $c = a * b$

Common Three address Instruction Forms

(1) Assignment Statement:-

$$\boxed{x = y \text{ op } z \quad \text{and} \quad x = \text{op } y}$$

Where x, y, z are operands.

'op' represents operator.

→ It assigns the result obtained after solving the right side expression of the assignment operator to the left side operand.

(2) Copy Statement:-

$$\boxed{x = y}$$

Where x and y are operands.

$=$ is an assignment operator.

→ It Copy and assigns the value of operand 'y' to operand 'x'.

(3) Conditional Jump:-

$$\boxed{\text{if } x \text{ relop } y \text{ goto } Z}$$

Where x, y are operands.

Z is the tag (or) label of the target statement.

'relop' is a relational operator.

'if' is a keyword.

→ If Condition " $x \text{ relop } y$ " gets satisfied then the control will be transferred to the location specified by label 'Z'. Otherwise the next statement appearing in usual sequence is executed.

(4) Unconditional Jump:-

$$\boxed{\text{goto } X}$$

where 'x' is the tag or label of the target statement.

→ On execution of the statement the Control is sent to the location specified by label 'x'. All statements in b/w are skipped.

(5) Procedure Call:-

param x Call P return y

where 'P' is a function which takes 'x' as parameter and returns 'y'.

(6) Indexed assignments:-

$x = y[i] \text{ or } x[i] = y$

where 'x' and 'y' are Variables.

'i' is an indexed variable.

→ The instruction $x = y[i]$ sets 'x' to the value in the location 'i' memory units beyond location 'y'.

The instruction $x[i] = y$ sets the contents of location 'i' units beyond 'x' to the value 'y'.

(7) Address and pointer assignments:-

$x = \&y, x = *y \text{ and } *x = y$

→ where 'x' and 'y' are Variables.

→ The instruction $x = \&y$ sets r-value of 'x' to the location (l-value) of y. The instruction $x = *y$, sets r-value of 'x' to r-value of an object pointed by 'y'.

→ The instruction $*x = *y$ sets r-value of object pointed by 'x' to the r-value of 'y'.

Problems on Three address Code (TAC)

① Write three address code for $a + b * c$.

Sol:-

$$t_1 = b * c$$

$$t_2 = a + t_1$$

② Write TAC for $-(a * b) + (c + d) - (a + b + c + d)$

Sol:-

$$t_1 = a * b$$

$$t_2 = -t_1$$

$$t_3 = c + d$$

$$t_4 = a + b$$

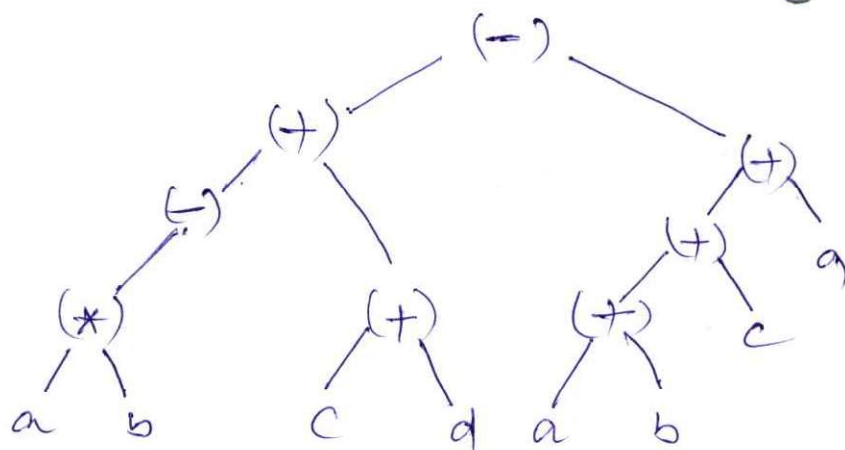
$$t_5 = t_4 + c$$

$$t_6 = t_5 + d$$

$$t_7 = t_2 + t_3$$

$$t_8 = t_7 - t_6$$

Syntax tree



③ Write TAC for $a = (-c * b) + (-c * d)$.

Sol:-

$$t_1 = -c$$

$$t_2 = t_1 * b$$

$$t_3 = -c$$

$$t_4 = t_3 * d$$

$$t_5 = t_2 + t_4$$

$$a = t_5$$

④ Write TAC for $\text{if } A < B \text{ then } 1 \text{ else } 0$

Sol:-

1. $\text{if } A < B \text{ goto } (4)$

2. $T1 = 0$

3. $\text{goto } (5)$

4. $T1 = 1$

5.

⑤ Write TAC for $\text{"if } A < B \text{ and } C < D \text{ then } t = 1 \text{ else } t = 0$ "

Sol:-

1. $\text{if } A < B \text{ goto } (3)$

2. $\text{goto } (4)$

3. $\text{if } C < D \text{ goto } (6)$

4. $t = 0$

5. $\text{goto } 7.$

6. $t = 1.$

7.

⑥ Write TAC for $C = 0 \text{ do } \{ \text{if } (a < b) \text{ then } x++ \text{ else } x--; C++ \}$
 $\text{while } (C < 5)$

Sol:-

1. $C = 0$

2. $\text{if } (a < b) \text{ goto } (4)$

3. $\text{goto } (7)$

4. $T1 = x + 1$

5. $x = T1$

6. $\text{goto } (9)$

7. $T2 = x - 1$

8. $x = T2$

9. $T3 = C + 1$

10. $C = T3$

11. $\text{if } (C < 5) \text{ goto } (2)$

12.

Implementation of Three address Code

→ Three address code is implemented in three ways, namely.

- ① Quadruple
- ② Triples
- ③ Indirect Triples.

① Quadruple:-

In Quadruple representation, each ^{TAC} instruction is splitted into the following 4 fields.

OP, arg1, arg2, result.

Here:-

- 'Op' field is used for storing the internal code of the operator.
- 'arg1', 'arg2' fields are used for storing two operands used.
- 'result' field is used for storing the result of the expression.

Advantages:-

- Easy to rearrange code for global optimization.
- One can quickly access value of temporary values using symbol table.

Disadvantages:-

- Contain lot of temporaries.
- Temporary variable creation increases time and space complexity.

Eg:- Quadruple representation for the expression $a = b * -c + b * -c$

Sol:- Three address Code

Quadruple representation

$$t_1 = -c$$

$$t_2 = b * t_1$$

$$t_3 = -c$$

$$t_4 = b * t_3$$

$$t_5 = t_2 + t_4$$

$$a = t_5$$

#	OP	arg1	arg2	result
0	Uminus	c		t ₁
1	*	b	t ₁	t ₂
2	Uminus	c		t ₃
3	*	b	t ₃	t ₄
4	+	t ₂	t ₄	t ₅
5	=	t ₅		a

② Triples:-

→ Triple representation does not make use of extra temporary variable to represent single operation, instead a pointer is used to refer another triple.

→ Triple uses only three fields namely

op, arg1, arg2.

Advantage:-

→ It saves memory by eliminating temporary variables.

Disadvantages:-

→ Difficult to rearrange the code.

→ Difficult to optimize the code.

Eg: Triple representation for $a = b * -c + b * -c$ is

Sol:-

#	op	Arg1	Arg2
0	Uminus	c	
1	*	b	(0)
2	Uminus	c	
3	*	b	(2)
4	+	(1)	(3)
5	=	a	(4)

③ Indirect Triple:-

→ Indirect Triple representation makes use of pointer to the listing of all references to Computations which is made separately and stored.

→ Temporaries are implicit and easier to rearrange code.

Eg: Indirect triple representation for $a = b * -c + b * -c$ is

#	op	arg1	arg2
10	Uminus	c	
11	*	b	(10)
12	Uminus	c	
13	*	b	(12)
14	+	(11)	(13)
15	=	a	(14)

List of pointers to table

#	Pointers
0	(10)
1	(11)
2	(12)
3	(13)
4	(14)
5	(15)

Directed Acyclic Graph (DAG)

- DAG is an abstract Syntax tree, with a unique node for each value.
- DAG is a directed graph that contains no cycles, but some nodes may have more than one parent.

Applications of DAG

- ① DAG is used in determining the Common Subexpressions.
- ② DAG is used in determining which names are used in the block and computed outside the block.
- ③ It is used for finding which statements of the block could have their computed value outside the block.
- ④ In simplifying the list of quadruples by eliminating the Common Subexpressions and not performing the assignment of the form $x = y$ unless it is must.

Rules for Construction of DAG

Rule 1: In a DAG leaf nodes represent identifiers, names or constants. Interior nodes represent operators.

Rule 2: Before creating a node in DAG, there is a check made to find, Is there any existing node with same children. A new node is created only when such node does not exist.

Rule 3:- The assignment of the form $x = y$ must not be performed until unless it is compulsory.

Problems on DAG Construction

① Construct DAG for the given expression $(a+b) \times (a+b+c)$

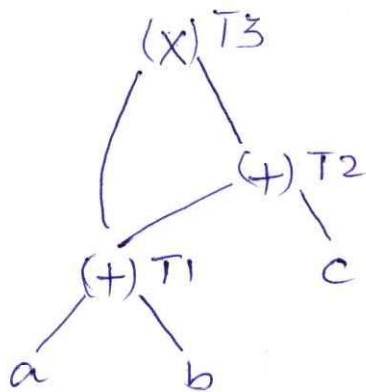
Sol: Three address code

$$T_1 = a + b$$

$$T_2 = T_1 + c$$

$$T_3 = T_1 \times T_2$$

Now DAG is



② Construct DAG for $((a+a) + (a+a)) + ((a+a) + (a+a))$

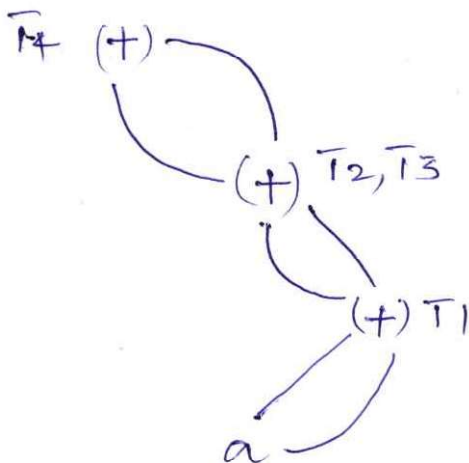
Sol: Three address code

$$T_1 = a + a$$

$$T_2 = T_1 + T_1$$

$$T_3 = T_1 + T_1$$

$$T_4 = T_2 + T_3$$



DAG for the given expression.

③ Consider ~~for~~ the following block and construct a DAG for it.

(1) $a = b \times c$

(2) $d = b$

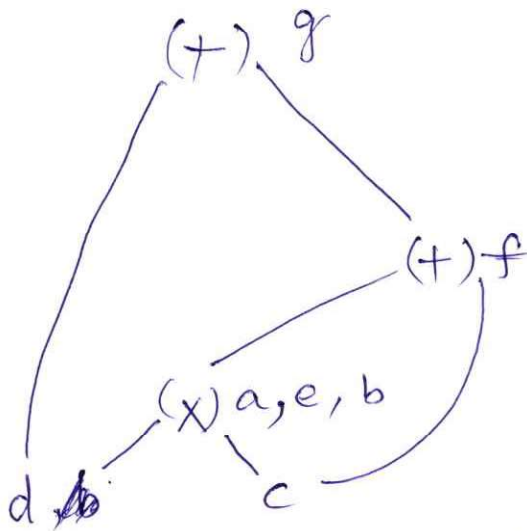
(3) $e = d \times c$

(4) $b = e$

(5) $f = b + c$

(6) $g = f + d$

Sol:- DAG for the given block



④ Construct DAG for the given block.

(1) $a = b + c$

(2) $t_1 = a \times a$

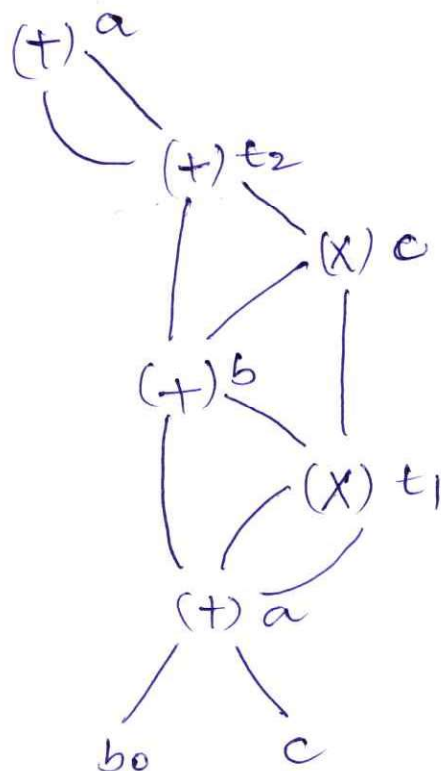
(3) $b = t_1 + a$

(4) $c = t_1 \times b$

(5) $t_2 = c + b$

(6) $a = t_2 + t_2$

Sol:-



⑤ Construct DAG for the given block.

(1) $S1 = 4 * i$

(2) $S2 = a[S1]$

(3) $S3 = 4 * i$

(4) $S4 = b[S3]$

(5) $S5 = S2 * S4$

(6) $S6 = prod + S5$

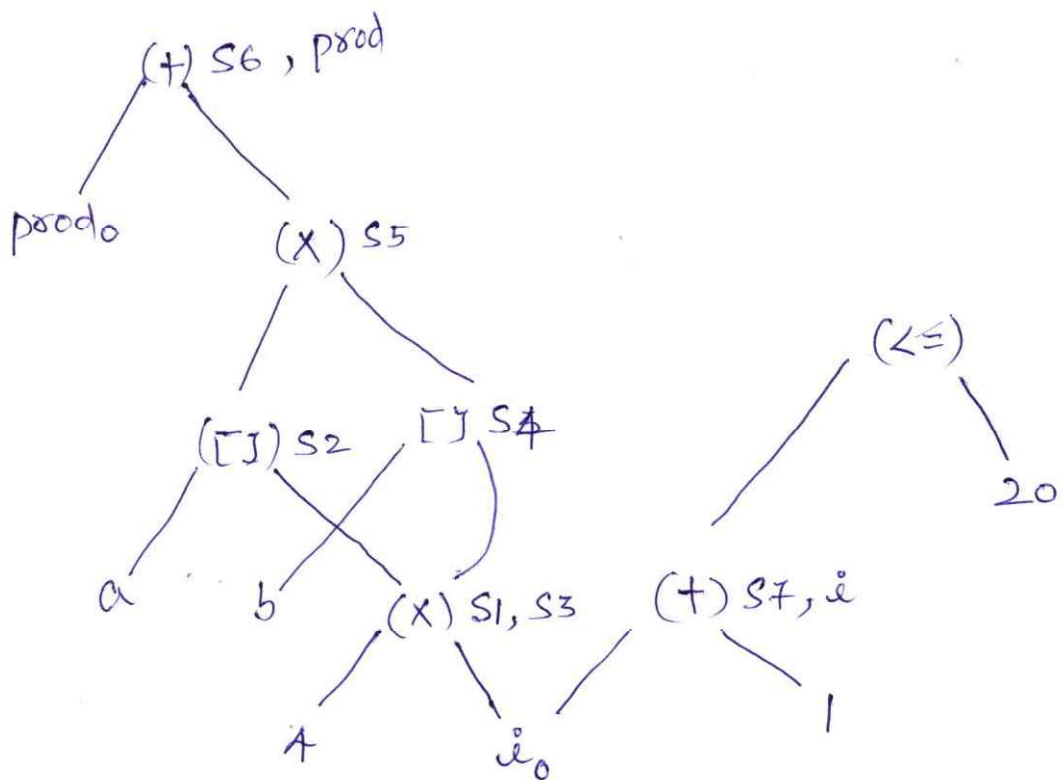
(7) $prod = S6$

(8) $S7 = i + 1$

(9) $i = S7$

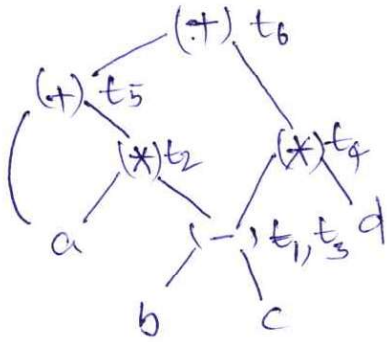
(10) if $i \leq 20$ goto (1)

Sol:-



⑥ Construct DAG for $a + a * (b - c) + (b - c) * d$

Sol:



Three address code

$$t_1 = b - c$$

$$t_2 = a * t_1$$

$$t_3 = b - c$$

$$t_4 = t_3 * d$$

$$t_5 = a + t_2$$

$$t_6 = t_5 + t_4$$

⑦ Construct DAG for

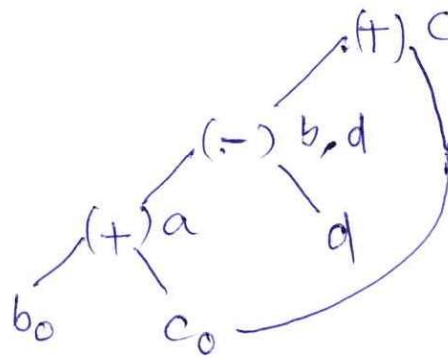
$$a = b + c$$

$$b = a - d$$

$$c = b + c$$

$$d = a - d$$

Sol:



⑧ Construct DAG for

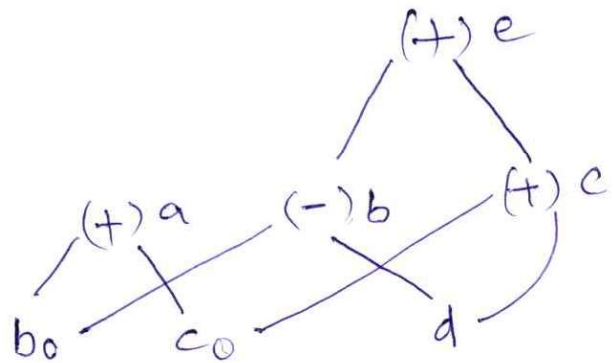
$$a = b + c$$

$$b = b - d$$

$$c = c + d$$

$$e = b + c$$

Sol:



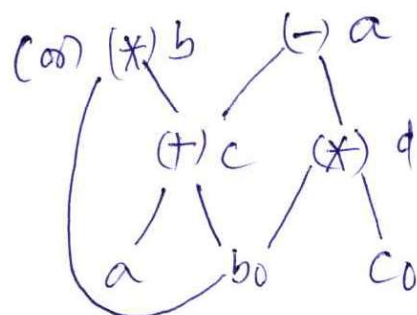
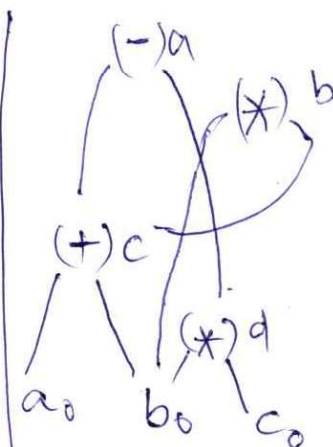
⑨ Construct DAG for

$$d = b * c$$

$$c = a + b$$

$$b = b * c$$

$$a = c - d$$

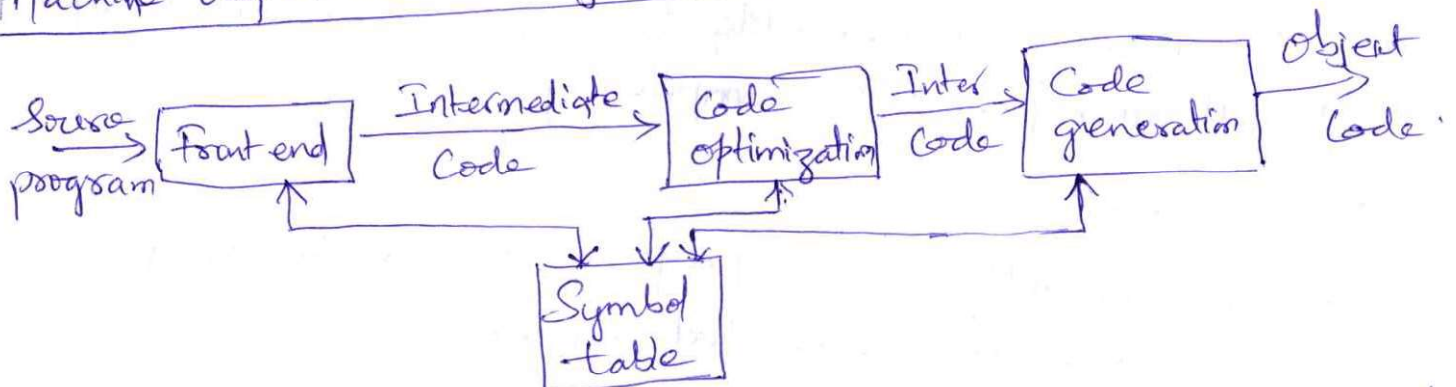


→ It is a process of creating assembly language/machine language statements which will perform the operations specified by the source program.

Properties of Object Code generation phase

- ① Correctness:- It should produce a high correct code, and do not alter the purpose of source code.
- ② Efficient Use of resources of the target machine:- It is necessary to know the target machine on which it is going to run. It also should consider into account the resources available at target machine. Eg:- Memory, registers, special cts designed to perform desired operations efficiently.
- ③ Quick Code generation:- It is necessary that code generation phase should produce the code quickly, without taking much time.

Machine dependant Code generation:-



→ Code generator takes intermediate code as an i/p and generates target code as o/p.

Object Code forms

→ The object code that gets generated will be in one of the following forms.

① Absolute Code ② Relocatable machine code ③ Assembler code.

① Absolute Code:-

- It is a machine code that contains reference to actual memory addresses within program's address space.
- The generated code can be placed directly in the memory and execute immediately.
- Small programs can be compiled and executed quickly because of absolute code.
- Disadvantage is if that memory space is not free, it has to wait.

② Relocatable Code:-

- It allows subprograms to be compiled separately.
- A set of relocatable object modules can be linked together and loaded for execution with the help of 'linking loader'.
- The advantage of generating relocatable m/c code is that we have great flexibility to compile subroutines separately and call other previously compiled programs.

③ Assembler Code:-

- Producing assembly language program as o/p makes the process of code generation somewhat easier.
- ~~It~~ It uses symbolic instructions and use of macro facilities of the assembler to help in generation of code.
- But it is slower because of assembling, linking and loading is required.

Issues in Code Generation

- ① → Input to the Code generator.
- ② → Target programs.
- ③ → Memory Management.
- ④ → Instruction Selection.
- ⑤ → Register allocation.
- ⑥ → Choice of ~~exp~~ evaluation order
- ⑦ → Approaches to Code generation.

① → Takes 3 address Code as i/p.

→ Intermediate Code may be 3 address code, quadruple, postfix notation, (or) graphical rep such as syntax tree (or) DAG etc.

→ front end should take care of Syntax & Semantic ~~check~~ errors before submitting the i/p to the Code generator.

→ Code generation requires Complete error free intermediate Code as i/p.

② → absolute, relocatable & assembly lang.

③ → Mapping the names in the source program to addresses to the data objects in run time memory.

→ type determines the amount of storage needed.

→ By using symbol table information, the Code generator determines the addresses in the target Code.

- ④ → Uniformity and Completeness of Instruction set is an important factor for the code generator
 → selection of instruction depends upon the instruction set of target m/c.

eg: $x = a + b$

mov a, R0
 add b, R0
 mov R0, x

→ line by line code generation leads to poor code generation. because of redundancies.

$x = y + z$

$a = x + t$

Registers

move y, R0 ✓

add z, R0 ✓

mov R0, x

mov x, R0

add t, R0 ✓

mov R0, a ✓

⑤ Register allocation: select appropriate set of variables that will reside in registers.

② Register assignment: pick up the specific register in which corresponding variable will reside

eg: $t_1 = a + b$
 $t_1 = t_1 \times c$
 $t_1 = t_1 / d$

mov a, R0
 add b, R0
 mul c, R0
 div d, R0
 mov R0, t1

address	M	M
register	R	R
indexed	$C(R)$	$C + \text{Constant}(R)$
indirect register	$*R$	$\text{Constant}(R)$
indirect indirect	$*C(R)$	$\text{Constant}(C + \text{Constant}(R))$
literal	$\#C$	C

Pass 3

	in	out
1	x	m, x
5	m, x, z	x, y, z

Pass 4

	in	out
3	m, x	m, x, z

	in	out
1	{x}	{m, x}
2	{m, x}	{m, x}
3	{m, x}	{m, x, z}
4	{x}	ϕ
5	{m, x, z}	{x, y, z}
6	{x, y, z}	{x, y, z}
7	{m, x}	{x, z}
8	{x, y}	{m, x}

$$(a+b) + (e + (c-d))$$

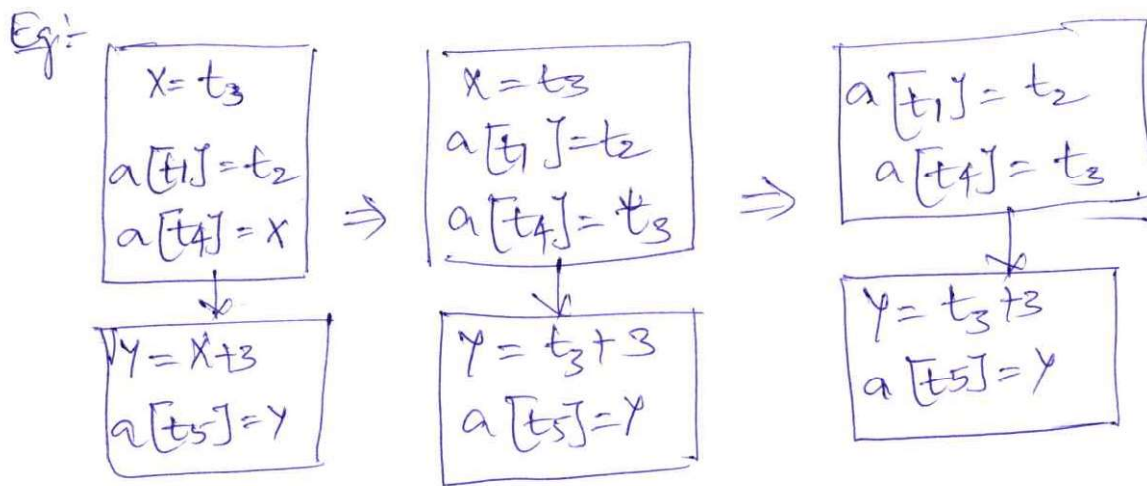
Move R_0, a Move t_1, R_1

Add R_0, b

Move R_1, c

Sub R_1, d

Add



Induction variable

→ A variable 'i' is called induction variable of loop, iff value of variable 'i' changes everytime either gets incremented (or) decremented.

Eg:- Generally induction variables are in the form.

$$a = i * b$$

$$a = b * i$$

$$a = i \pm b$$

$$a = b \pm i$$

where 'b' is a basic Constant and 'i' is an induction variable.

→ If b is a basic then a is in family of 'j'. The 'a' depends on the definition of 'i'.

Algorithm:-

1) Find the induction variable 'i' with triple (i, c, d). Consider a test form.

if i loop x goto B where 'i' is an induction variable and 'x' is not an induction variable.

$$t = c * x$$

$$t = t + d.$$

if j loop t goto B where t is a new temporary.

Finally delete all assignments to the eliminated induction variables from loop L, because these induction variables will be useless.

④ Graph Colouring for register assignment.

Registers interface graph is Constructed.

→ nodes are symbolic registers and edges connects two nodes if one is live at a point where the other is defined.

Registers descriptor :- keeps track of what value is stored in each register.

addresses descriptor :- keeps track of the location that holds the current ~~variable~~ value of a variable.

$$x = (a+b) * (c-d) + ((e/f) * (a+b))$$

$$t_1 = a+b, t_2 = c-d, t_3 = e/f, t_4 = t_1 * t_2, t_5 = t_3 * 1, t_6 = t_4 + t_5$$

Mov a, R0, ~~Now~~ Add b, R0, Mov c, R1, Sub d, R1, Move e, R2
div f, R2, Mul R0, R1, MUL R2, R0, Add R1, R0.

Generic Code Algorithm (using pattern matching or Code generator Concept)

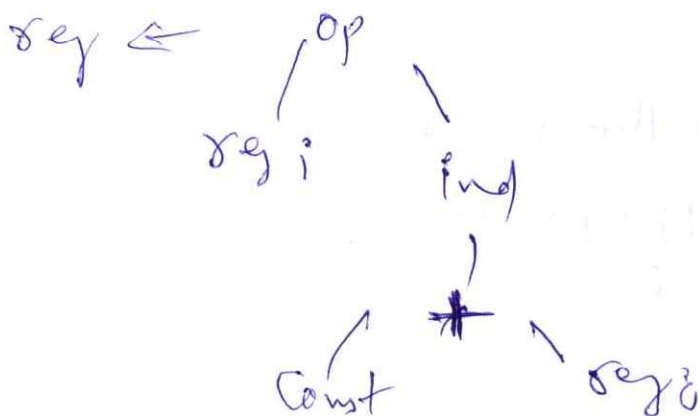
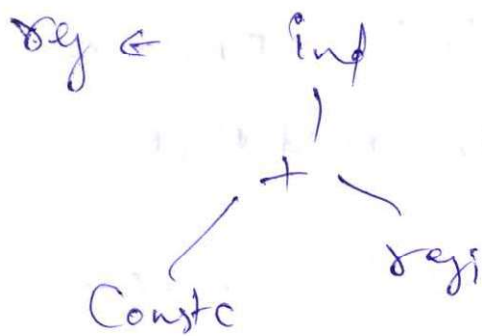
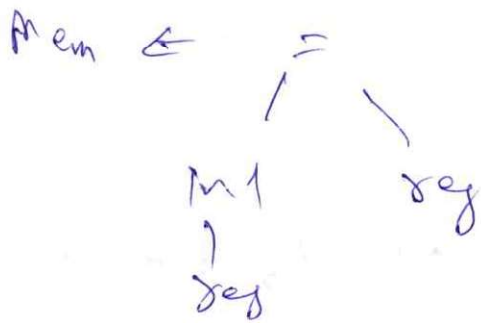
- ① Draw the structure for the complete expression.
- ② Traverse the tree in bottom up fashion and match the sub trees with the appropriate template from the tree translation scheme.
- ③ The corresponding code for the matched template is stored in a buffer.

④ When we reach to the root node, buffer contains the complete m/c code for the given expressions.

Rule

$reg \leftarrow Const_c$

$reg_i \leftarrow mem_a$



Code

Move #c, R_i

Move a, R_i

Move R_i, a

MOV R_j * R_i

MOV c(R_j), R_i

OP c(R_j), R_i

Code generation Using DAG

→ It is much simpler than code generation from 3 add code.

→ 3-Types of algorithms are used to generate Code from DAG.

- ① Rearranging order
- ② Heuristic Ordering
- ③ Labeling algorithm.

① Rearranging Order :-

→ The order of 3-add code affects the Cost of the object code being generated.

→ In changing the order in which Computations are done we can obtain object code with min cost.

Eg:- $(a+b) + (e + (c-d))$

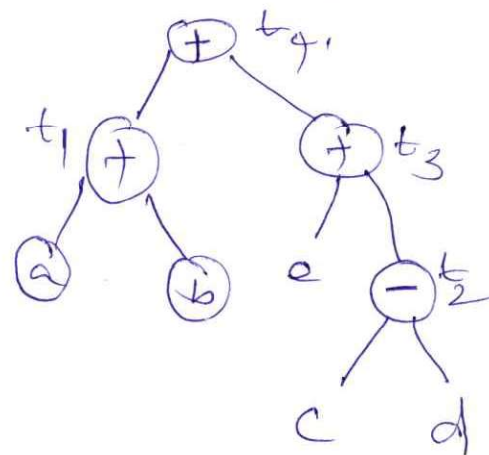
$t_1 = a + b$
 $t_2 = c - d$
 $t_3 = e + t_2$
 $t_4 = t_1 + t_3$

Mov a, R0
Add b, R0
Mov c, R1
Sub d, R1
Mov R0, t1

Mov e, R0
Add R0, R1
Mov t1, R0
Add R1, R0
Mov R0, t4

$t_2 = c - d$
 $t_3 = e + t_2$
 $t_1 = a + b$
 $t_4 = t_1 + t_3$

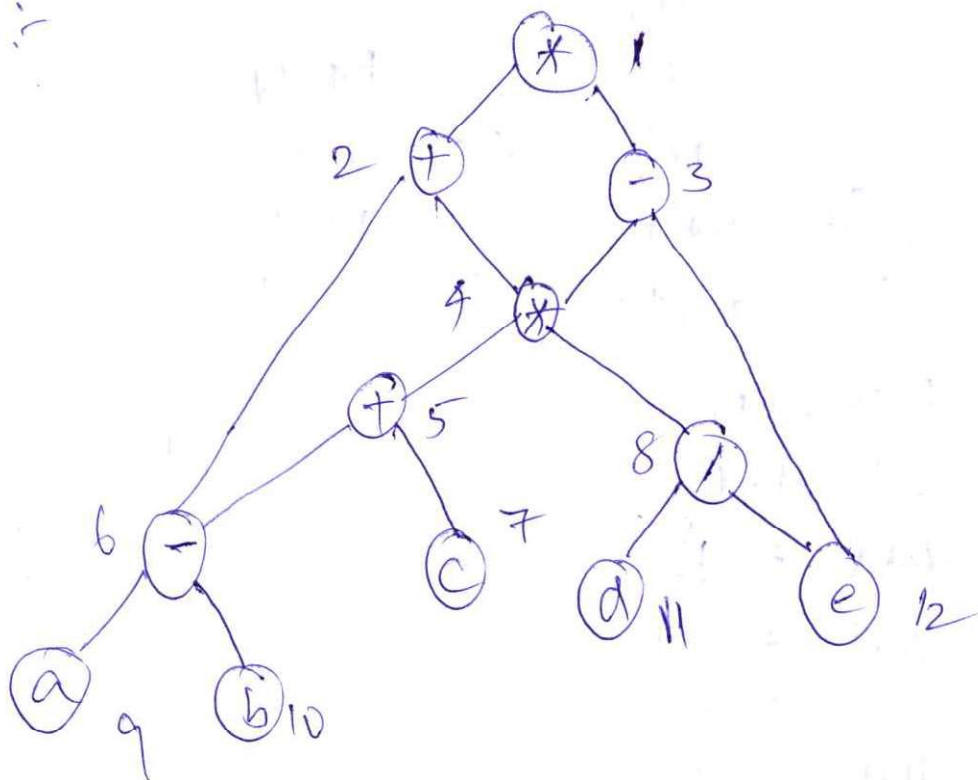
Mov c, R0
Sub d, R0
~~Mov~~ e, R1
Add R0, R1
Mov a, R0
Add b, R0
Add R1, R0
Mov R0, t4



② Heuristic Ordering

- ① Obtain all the interior nodes. Consider them as unlisted interior nodes.
- ② while (unlisted interior nodes remain).
- ③ Pick up an unlisted node n , whose parents have been listed.
- ④ list node ' n '.
- ⑤ while the leftmost child m of n has no (unlisted) parent AND is not leaf.
 - {
 - ⑥ list m ;
 - ⑦ $n = m$;
 - }
- }

eg:-



→ Unlisted interior nodes 1, 2, 3, 4, 5, 6, 8

→ Initially only node with unlisted parent is 1

∴ set $n=1$ by line 14)

→ Now left of 1 is 2 which is unlisted and parent of 2, is listed ^{which is 1}

∴ list 2.

∴ set $n=2$

→ left of 2 is 6 which has unlisted parent 5, ∴ we can't list it, ∴ switch to node 3 whose parent is 1 & listed

∴ list 3 and set $n=3$

→ left of 3 is 4

∴ resulting list is

→ Hence, 1, 2, 3, 4, 5, 6, 8.

→ Then the order of Computation is decided by reversing this list.

We get 8, 6, 5, 4, 3, 2, 1.

→ This also means that we have to perform the computations at these nodes in the given order.

• $t_8 = d/e$, $t_6 = a-b$, $t_5 = t_6 + c$, $t_4 = t_5 * t_8$, $t_3 = t_4 - e$,

$t_2 = t_6 + t_4$, $t_1 = t_2 * t_3$

③ ~~Label~~ → This gives optimized code for DAG

③ Labeling algorithm :-

→ It gives the optimal code for given expression in which min registers are required.

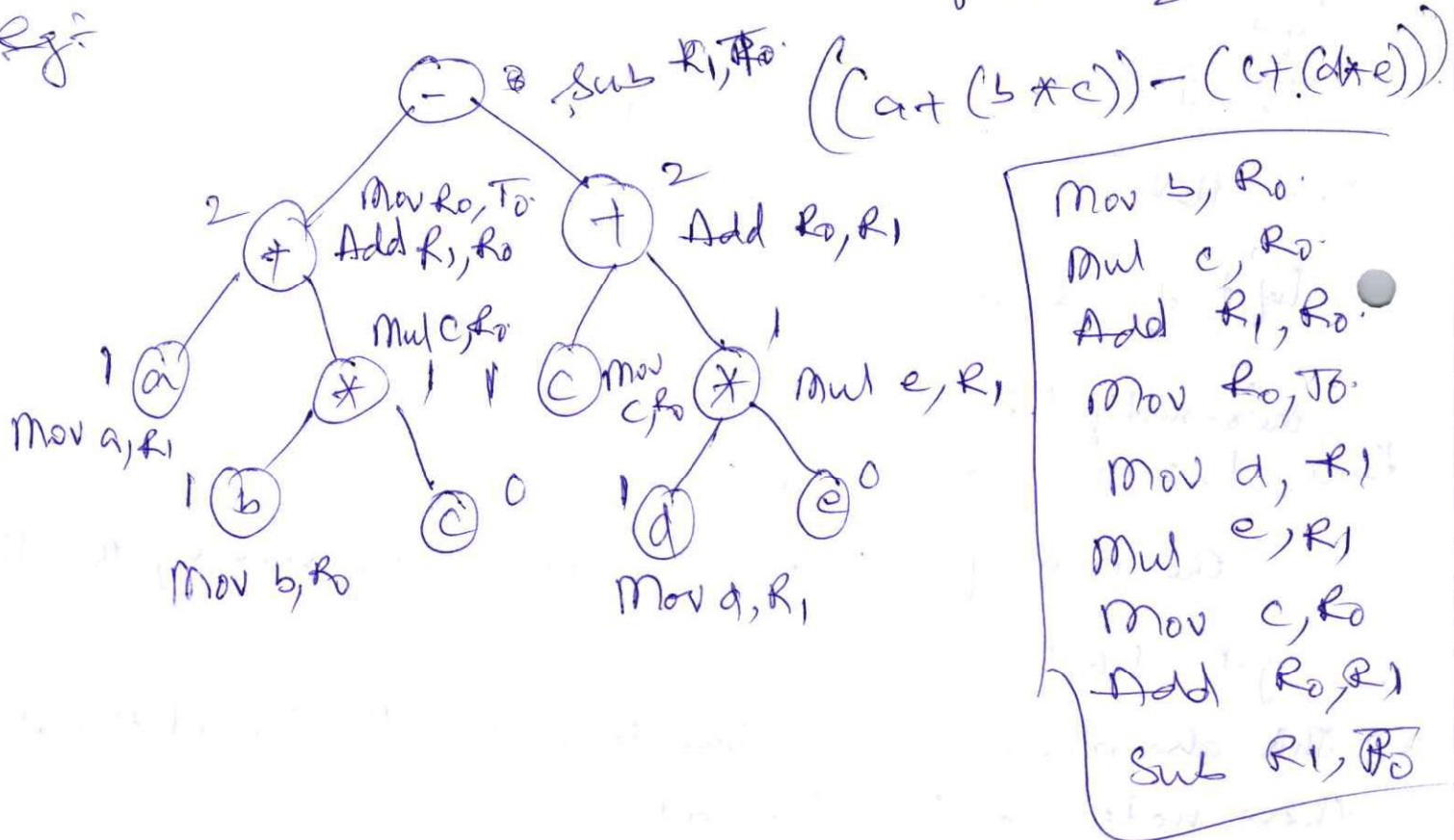
→ Using this algorithm labeling can be done to the tree by visiting nodes in bottomup order. By this all child nodes will be labelled before its parent nodes.

→ we start in bottom-up fashion and label left leaf as 1 and right leaf as 0.

→ If labels of the children of a node n are L_1 & L_2 respectively then

$$\text{Label}(n) = \begin{cases} \max(L_1, L_2) & \text{if } L_1 \neq L_2 \\ L_1 + 1 & \text{if } L_1 = L_2 \end{cases}$$

Eg:



Features of DAG

- Useful data structure for analysing Basic block.
- It is directed with no cycle.
- Depicts how values are calculated and used in next subsequent statements.
- Determine Common Subexpressions in basic block.
- Determines which names are listed inside but evaluated outside of the block.
- Determines which statements of the block could have their values outside the block.

for Algorithm Constructing a DAG

Input:- A basic block

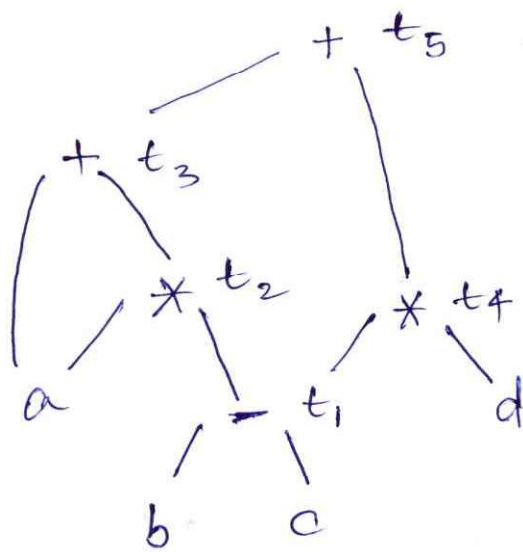
Output:- A DAG with following information.

- A label for each node for leaves the label is an Identifier (constants permitted) and for interior nodes an operator symbol.
- For each node a (possibly empty) list of attached identifiers (constants are not permitted here).

Method:- We assume availability of data structure to create nodes with one or two children with left and right nodes labelling should also available facility to create linked list of attached identifiers for each node.

Directed Acyclic Graph (DAG)

(1) $a + a * (b - c) + (b - c) * d$



$$t_1 = b - c$$

$$t_2 = a * t_1$$

$$t_3 = a + t_2$$

$$t_4 = t_1 * d$$

$$t_5 = t_3 + t_4$$

(2) $((x + y) - ((x + y) * (x - y))) + ((x + y) * (x - y))$

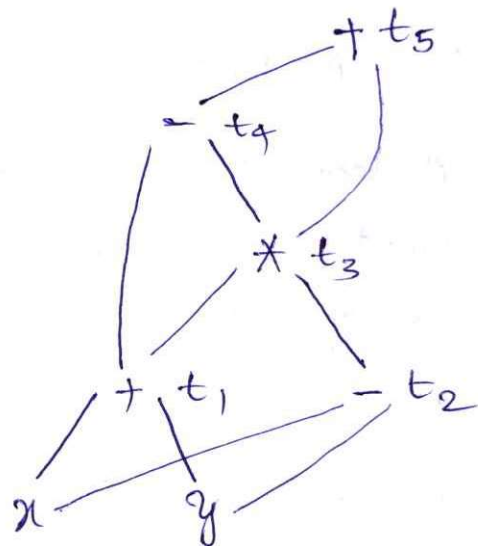
$$t_1 = x + y$$

$$t_2 = x - y$$

$$t_3 = t_1 * t_2$$

$$t_4 = t_1 - t_3$$

$$t_5 = t_4 + t_3$$



$S \rightarrow id = E$ ~~$S \rightarrow id = E$~~ $\{ \text{gen}(id.name = E.place); \}$
 $E \rightarrow E_1 * T$ $\{ E.place = \text{new temp}(); \text{gen}(E.place =$
 $E \rightarrow T$ $\{ E.place = T.place; \}$
 $T \rightarrow T * F$ $\{ T.place = \text{new temp}(); \text{gen}(T.place =$
 $T \rightarrow F$ $T.place *.$
 $F \rightarrow id$ $\{ F.place = id.name; \}$

$S \rightarrow id = E$

$x = a + b + c + d$

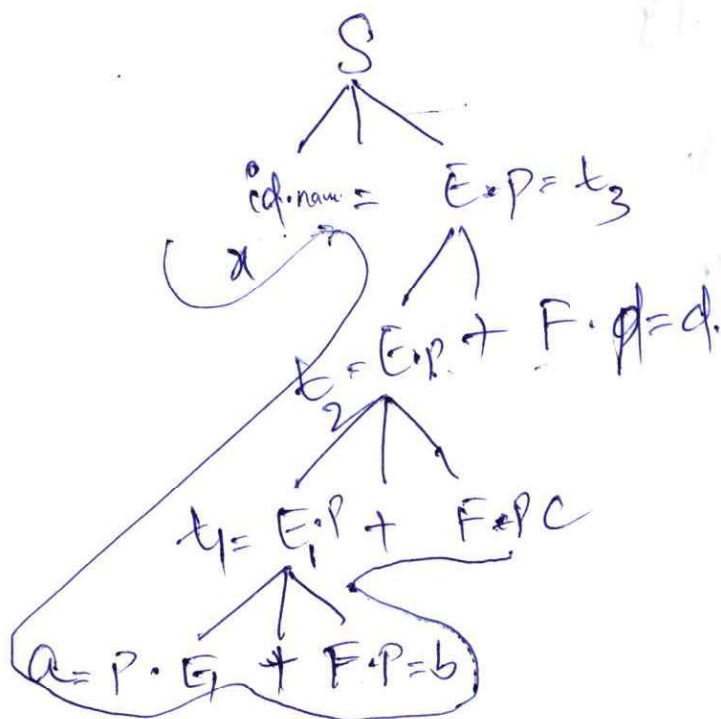
$E \rightarrow E + F / F$

$t_1 = a + b$

$F \rightarrow id.$

$t_2 = t_1 + c$

$x = t_2 + d.$



~~$E.p = t_1 = a + b.$~~

$t_2 = t_1 + c.$

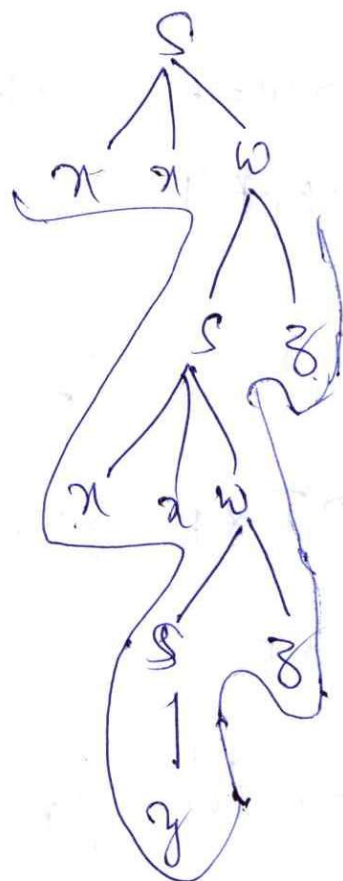
$t_3 = t_2 + d.$

$x = t_3.$

① $S \rightarrow x x w \quad \{ \text{printf}("1"); \}$
 $S \rightarrow y \quad \{ \text{printf}("2"); \}$
 $w \rightarrow s z \quad \{ \text{printf}("3"); \}$

i/p = x x x x y z z

o/p = 2 3 1 3 1



② find the o/p for i/p = 4 - 2 - 4 * 2 for a given SDT

$E \rightarrow E * T \quad \{ E.val = E.val * T.val \}$

$E \rightarrow T \quad \{ E.val = T.val \}$

$T \rightarrow F - T \quad \{ F.val = F.val - T.val \}$

$T \rightarrow F \quad \{ T.val = F.val \}$

$F \rightarrow 2 \quad \{ F.val = 2 \}$

$F \rightarrow 4 \quad \{ F.val = 4 \}$

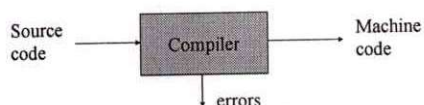
Terminology

- **Compiler:**
 - a program that translates an *executable* program in one language into an *executable* program in another language
 - we expect the program produced by the compiler to be better, in some way, than the original
- **Interpreter:**
 - a program that reads an *executable* program and produces the results of running that program
 - usually, this involves executing the source program in some fashion
- Our course is mainly about compilers but many of the same issues arise in interpreters

Disciplines involved

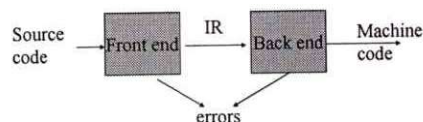
- Algorithms
- Languages and machines
- Operating systems
- Computer architectures

Abstract view



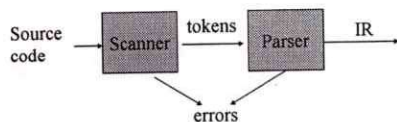
- Recognizes legal (and illegal) programs
- Generate correct code
- Manage storage of all variables and code
- Agreement on format for object (or assembly) code

Front-end, Back-end division



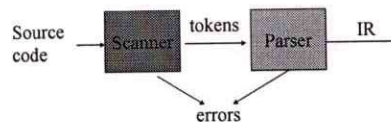
- Front end maps legal code into IR
- Back end maps IR onto target machine
- Simplify retargeting
- Allows multiple front ends
- Multiple passes -> better code

Front end



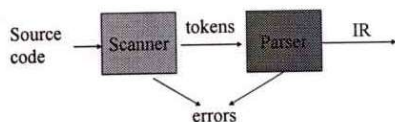
- Recognize legal code
- Report errors
- Produce IR
- Preliminary storage maps

Front end



- **Scanner:**
 - Maps characters into tokens – the basic unit of syntax
 - $x = x + y$ becomes $\langle id, x \rangle = \langle id, x \rangle + \langle id, y \rangle$
 - Typical tokens: number, id, +, -, *, /, do, end
 - Eliminate white space (tabs, blanks, comments)
- A key issue is speed so instead of using a tool like LEX it sometimes needed to write your own scanner

Front end



- Parser:
 - Recognize context-free syntax
 - Guide context-sensitive analysis
 - Construct IR
 - Produce meaningful error messages
 - Attempt error correction
- There are parser generators like YACC which automates much of the work

Front end

- Context free grammars are used to represent programming language syntaxes:

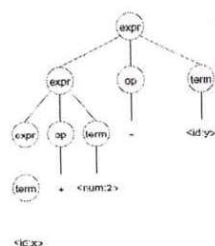
$\langle \text{expr} \rangle ::= \langle \text{expr} \rangle \langle \text{op} \rangle \langle \text{term} \rangle \mid \langle \text{term} \rangle$

$\langle \text{term} \rangle ::= \langle \text{number} \rangle \mid \langle \text{id} \rangle$

$\langle \text{op} \rangle ::= + \mid -$

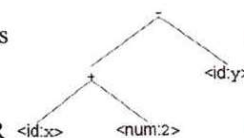
Front end

- A parser tries to map a program to the syntactic elements defined in the grammar
- A parse can be represented by a tree called a parse or syntax tree

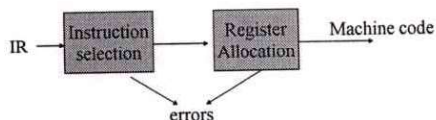


Front end

- A parse tree can be represented more compactly referred to as Abstract Syntax Tree (AST)
- AST is often used as IR between front end and back end

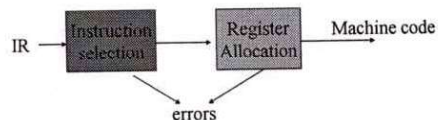


Back end



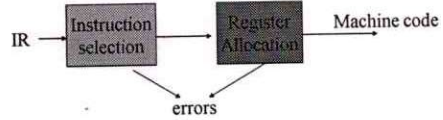
- Translate IR into target machine code
- Choose instructions for each IR operation
- Decide what to keep in registers at each point
- Ensure conformance with system interfaces

Back end



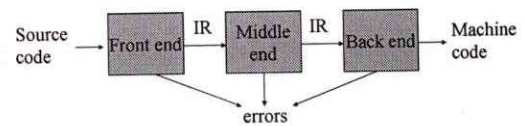
- Produce compact fast code
- Use available addressing modes

Back end



- Have a value in a register when used
- Limited resources
- Optimal allocation is difficult

Traditional three pass compiler



- Code improvement analyzes and change IR
- Goal is to reduce runtime

Middle end (optimizer)

- Modern optimizers are usually built as a set of passes
- Typical passes
 - Constant propagation
 - Common sub-expression elimination
 - Redundant store elimination
 - Dead code elimination

Compiler course

Chapter 3
Lexical Analysis

Why to separate Lexical analysis and parsing

1. Simplicity of design
2. Improving compiler efficiency
3. Enhancing compiler portability

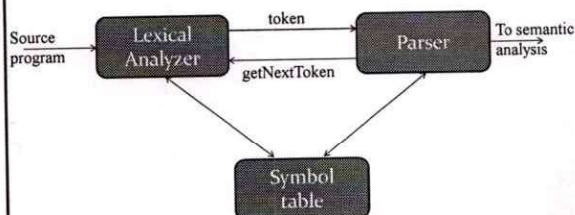
Outline

- Role of lexical analyzer
- Specification of tokens
- Recognition of tokens
- Lexical analyzer generator
- Finite automata
- Design of lexical analyzer generator

Tokens, Patterns and Lexemes

- A token is a pair a token name and an optional token value
- A pattern is a description of the form that the lexemes of a token may take
- A lexeme is a sequence of characters in the source program that matches the pattern for a token

The role of lexical analyzer



Example

Token	Informal description	Sample lexemes
if	Characters i, f	if
else	Characters e, l, s, e	else
comparison	< or > or <= or >= or == or !=	<=, !=
id	Letter followed by letter and digits	pi, score, D2
number	Any numeric constant	3.14159, 0, 6.02e23
literal	Anything but " surrounded by "	"core dumped"

```
printf("total = %d\n", score);
```

Attributes for tokens

- $E = M * C ** 2$
 - <id, pointer to symbol table entry for E>
 - <assign-op>
 - <id, pointer to symbol table entry for M>
 - <mult-op>
 - <id, pointer to symbol table entry for C>
 - <exp-op>
 - <number, integer value 2>

Input buffering

- Sometimes lexical analyzer needs to look ahead some symbols to decide about the token to return
 - In C language: we need to look after -, = or < to decide what token to return
 - In Fortran: DO 5 I = 1.25
- We need to introduce a two buffer scheme to handle large look-aheads safely

... E := M * C ** 2 eof

Lexical errors

- Some errors are out of power of lexical analyzer to recognize:
 - $fi(a == f(x)) \dots$
- However it may be able to recognize errors like:
 - $d = 2r$
- Such errors are recognized when no pattern for tokens matches a character sequence

Sentinels

... E := M eof * C ** 2 eof eof

```
Switch ("forward++") {
  case eof:
    if (forward is at end of first buffer) {
      reload second buffer;
      forward = beginning of second buffer;
    }
    else if (forward is at end of second buffer) {
      reload first buffer;
      forward = beginning of first buffer;
    }
    else /* eof within a buffer marks the end of input */
      terminate lexical analysis;
    break;
  cases for the other characters:
}
```

Error recovery

- Panic mode: successive characters are ignored until we reach to a well formed token
- Delete one character from the remaining input
- Insert a missing character into the remaining input
- Replace a character by another character
- Transpose two adjacent characters

Specification of tokens

- In theory of compilation regular expressions are used to formalize the specification of tokens
- Regular expressions are means for specifying regular languages
- Example:
 - $\text{Letter_}(\text{letter_} | \text{digit})^*$
- Each regular expression is a pattern specifying the form of strings

Regular expressions

- ϵ is a regular expression, $L(\epsilon) = \{\epsilon\}$
- If a is a symbol in Σ then a is a regular expression, $L(a) = \{a\}$
- $(r) \mid (s)$ is a regular expression denoting the language $L(r) \cup L(s)$
- $(r)(s)$ is a regular expression denoting the language $L(r)L(s)$
- $(r)^*$ is a regular expression denoting $(L(r))^*$
- (r) is a regular expression denoting $L(r)$

Recognition of tokens

- Starting point is the language grammar to understand the tokens:

```

stmt -> if expr then stmt
      | if expr then stmt else stmt
      |  $\epsilon$ 
expr -> term relop term
      | term
term -> id
      | number
  
```

Regular definitions

```

d1 -> r1
d2 -> r2
...
dn -> rn
  
```

- Example:

```

letter_ -> A | B | ... | Z | a | b | ... | z | _
digit   -> 0 | 1 | ... | 9
id      -> letter_ (letter_ | digit)*
  
```

Recognition of tokens (cont.)

- The next step is to formalize the patterns:

```

digit   -> [0-9]
Digits  -> digit+
number  -> digit (digits)? (E[+-]? Digit)?
letter  -> [A-Za-z_]
id       -> letter (letter|digit)*
If       -> if
Then     -> then
Else     -> else
Relop   -> < | > | <= | >= | = | <>
  
```

- We also need to handle whitespaces:

```
ws -> (blank | tab | newline)+
```

Extensions

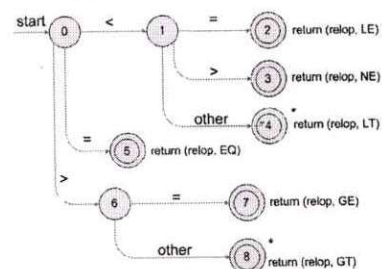
- One or more instances: $(r)^+$
- Zero of one instances: $r?$
- Character classes: $[abc]$

- Example:

- $\text{letter_} \rightarrow [A-Za-z_]$
- $\text{digit} \rightarrow [0-9]$
- $\text{id} \rightarrow \text{letter_}(\text{letter_}|\text{digit})^*$

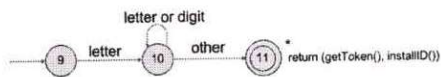
Transition diagrams

- Transition diagram for relop



Transition diagrams (cont.)

- Transition diagram for reserved words and identifiers



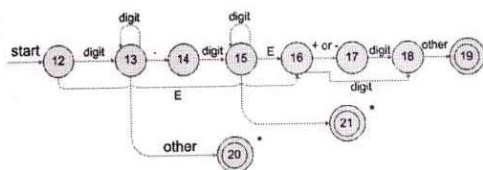
Architecture of a transition-diagram-based lexical analyzer

```

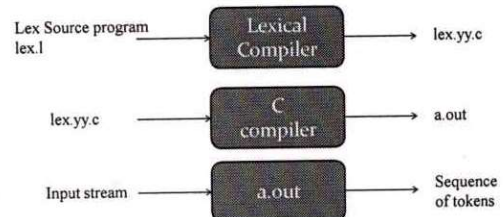
TOKEN getRelop()
{
    TOKEN retToken = new (RELOP)
    while (1) {
        /* repeat character processing until a
           return or failure occurs */
        switch(state) {
            case 0: c = nextchar();
                if (c == '<') state = 1;
                else if (c == '=') state = 5;
                else if (c == '>') state = 6;
                else fail(); /* lexeme is not a relop */
                break;
            case 1: ...
            ...
            case 8: retract();
                retToken.attribute = GT;
                return(retToken);
        }
    }
}
  
```

Transition diagrams (cont.)

- Transition diagram for unsigned numbers

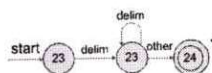


Lexical Analyzer Generator - Lex



Transition diagrams (cont.)

- Transition diagram for whitespace



Structure of Lex programs

```

declarations
%%
translation rules  ----->  Pattern {Action}
%%
auxiliary functions
  
```

Example

```
%
/* definitions of manifest constants
LT, LE, EQ, NE, GT, GE,
IF, THEN, ELSE, ID, NUMBER, RELOP */
%

/* regular definitions
delim  [ \t\n ]
ws     {delim}+
letter {A-Za-z}
digit  {0-9}
id     {letter}({letter}|{digit})*
number {digit}+(\.{digit}+)?([Ee-+]?[digit]+)?

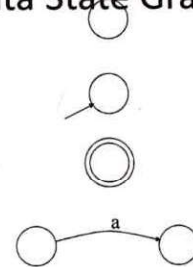
%
{ws}    [/* no action and no return */]
if      {return(IF);}
then    {return(THEN);}
else    {return(ELSE);}
[id]    {yyval = (int) installID(); return(ID);}
[number]{yyval = (int) installNum(); return(NUMBER);}
...
```

```
Int installID() /* funtion to install the
lexeme, whose first character is
pointed to by yytext, and whose
length is yyfeng, into the symbol
table and return a pointer thereto
*/
```

```
Int installNum() { /* similar to
installID, but puts numerical
constants into a separate table */
```

Finite Automata State Graphs

- A state
- The start state
- An accepting state
- A transition



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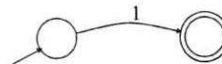
Finite Automata

- Regular expressions = specification
- Finite automata = implementation
- A finite automaton consists of
 - An input alphabet Σ
 - A set of states S
 - A start state n
 - A set of accepting states $F \subseteq S$
 - A set of transitions $\text{state} \rightarrow_{\text{input}} \text{state}$

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A Simple Example

- A finite automaton that accepts only "1"



- A finite automaton accepts a string if we can follow transitions labeled with the characters in the string from the start to some accepting state

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Finite Automata

- Transition

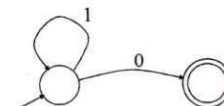
$$s_1 \xrightarrow{a} s_2$$
- Is read

In state s_1 on input "a" go to state s_2
- If end of input
 - If in accepting state => accept, otherwise => reject
 - If no transition possible => reject

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Another Simple Example

- A finite automaton accepting any number of 1's followed by a single 0
- Alphabet: $\{0,1\}$

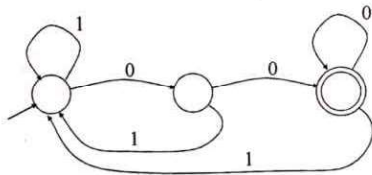


- Check that "110" is accepted but "110..." is not

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And Another Example

- Alphabet $\{0,1\}$
- What language does this recognize?



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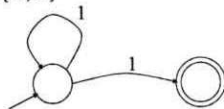
Deterministic and Nondeterministic Automata

- Deterministic Finite Automata (DFA)
 - One transition per input per state
 - No ϵ -moves
- Nondeterministic Finite Automata (NFA)
 - Can have multiple transitions for one input in a given state
 - Can have ϵ -moves
- Finite automata have *finite* memory
 - Need only to encode the current state

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And Another Example

- Alphabet still $\{0,1\}$



- The operation of the automaton is not completely defined by the input
 - On input "1" the automaton could be in either state

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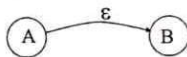
Execution of Finite Automata

- A DFA can take only one path through the state graph
 - Completely determined by input
- NFAs can choose
 - Whether to make ϵ -moves
 - Which of multiple transitions for a single input to take

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Epsilon Moves

- Another kind of transition: ϵ -moves

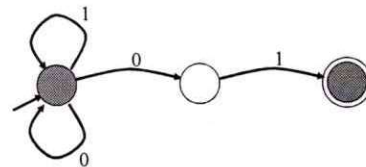


- Machine can move from state A to state B without reading input

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Acceptance of NFAs

- An NFA can get into multiple states



- Input: 1 0 1
- Rule: NFA accepts if it can get in a final state

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NFA vs. DFA (1)

- NFAs and DFAs recognize the same set of languages (regular languages)
- DFAs are easier to implement
 - There are no choices to consider

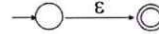
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Regular Expressions to NFA (1)

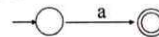
- For each kind of rexp, define an NFA
 - Notation: NFA for rexp A



- For ϵ



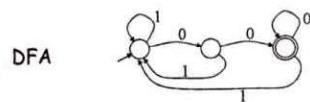
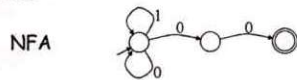
- For input a



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NFA vs. DFA (2)

- For a given language the NFA can be simpler than the DFA

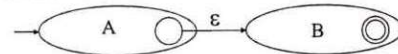


- DFA can be exponentially larger than NFA

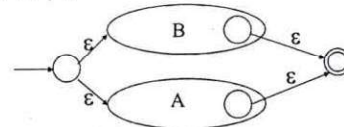
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Regular Expressions to NFA (2)

- For AB



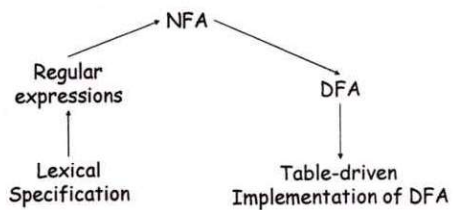
- For $A \mid B$



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Regular Expressions to Finite Automata

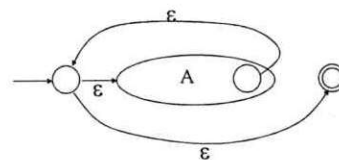
- High-level sketch



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Regular Expressions to NFA (3)

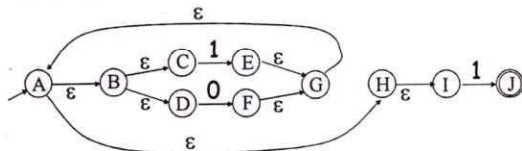
- For A^*



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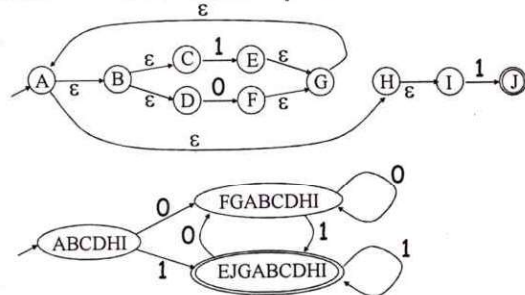
Example of RegExp $\rightarrow$ NFA conversion

- Consider the regular expression $(1 | 0)^* 1$
- The NFA is



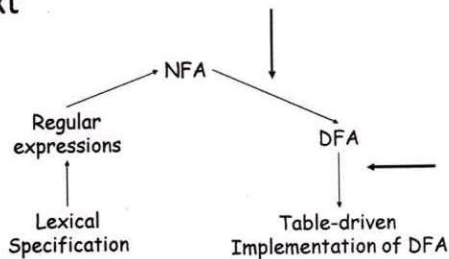
43

NFA $\rightarrow$ DFA Example



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Next



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NFA to DFA. Remark

- An NFA may be in many states at any time
- How many different states?
- If there are N states, the NFA must be in some subset of those N states
- How many non-empty subsets are there?
 - $2^N - 1$ = finitely many, but exponentially many

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NFA to DFA. The Trick

- Simulate the NFA
- Each state of resulting DFA
 - = a non-empty subset of states of the NFA
- Start state
 - = the set of NFA states reachable through ϵ -moves from NFA start state
- Add a transition $S \rightarrow^a S'$ to DFA iff
 - S' is the set of NFA states reachable from the states in S after seeing the input a
 - considering ϵ -moves as well

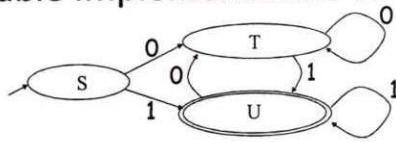
45

Implementation

- A DFA can be implemented by a 2D table T
 - One dimension is "states"
 - Other dimension is "input symbols"
 - For every transition $S_i \rightarrow^a S_k$ define $T[i, a] = k$
- DFA "execution"
 - If in state S_i and input a , read $T[i, a] = k$ and skip to state S_k
 - Very efficient

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Table Implementation of a DFA



	0	1
S	T	U
T	T	U
U	T	U

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Implementation (Cont.)

- NFA → DFA conversion is at the heart of tools such as flex or jflex
- But, DFAs can be huge
- In practice, flex-like tools trade off speed for space in the choice of NFA and DFA representations

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Readings

- Chapter 3 of the book

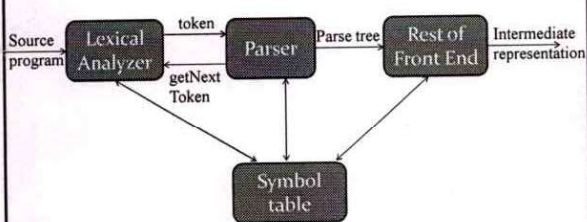
Compiler course

Chapter 4
Syntax Analysis

Outline

- Role of parser
- Context free grammars
- Top down parsing
- Bottom up parsing
- Parser generators

The role of parser



Uses of grammars

$$E \rightarrow E + T \mid T$$

$$T \rightarrow T * F \mid F$$

$$F \rightarrow (E) \mid id$$

$$E \rightarrow TE'$$

$$E' \rightarrow +TE' \mid \varepsilon$$

$$T \rightarrow FT'$$

$$T' \rightarrow *FT' \mid \varepsilon$$

$$F \rightarrow (E) \mid id$$

Error handling

- Common programming errors
 - Lexical errors
 - Syntactic errors
 - Semantic errors
 - Lexical errors
- Error handler goals
 - Report the presence of errors clearly and accurately
 - Recover from each error quickly enough to detect subsequent errors
 - Add minimal overhead to the processing of correct programs

Error-recover strategies

- Panic mode recovery
 - Discard input symbol one at a time until one of designated set of synchronization tokens is found
- Phrase level recovery
 - Replacing a prefix of remaining input by some string that allows the parser to continue
- Error productions
 - Augment the grammar with productions that generate the erroneous constructs
- Global correction
 - Choosing minimal sequence of changes to obtain a globally least-cost correction

Context free grammars

- Terminals
- Nonterminals
- Start symbol
- productions

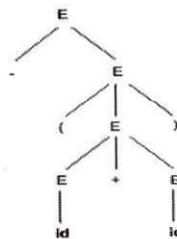
$\text{expression} \rightarrow \text{expression} + \text{term}$
 $\text{expression} \rightarrow \text{expression} - \text{term}$
 $\text{expression} \rightarrow \text{term}$
 $\text{term} \rightarrow \text{term} * \text{factor}$
 $\text{term} \rightarrow \text{term} / \text{factor}$
 $\text{term} \rightarrow \text{factor}$
 $\text{factor} \rightarrow (\text{expression})$
 $\text{factor} \rightarrow \text{id}$

Derivations

- Productions are treated as rewriting rules to generate a string
- Rightmost and leftmost derivations
 - $E \rightarrow E + E \mid E * E \mid -E \mid (E) \mid \text{id}$
 - Derivations for $-(\text{id}+\text{id})$
 - $E \Rightarrow -E \Rightarrow -(E) \Rightarrow -(E+E) \Rightarrow -(\text{id}+E) \Rightarrow -(\text{id}+\text{id})$

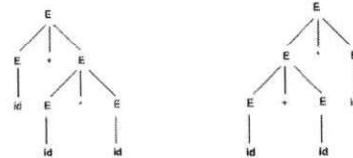
Parse trees

- $-(\text{id}+\text{id})$
- $E \Rightarrow -E \Rightarrow -(E) \Rightarrow -(E+E) \Rightarrow -(\text{id}+E) \Rightarrow -(\text{id}+\text{id})$



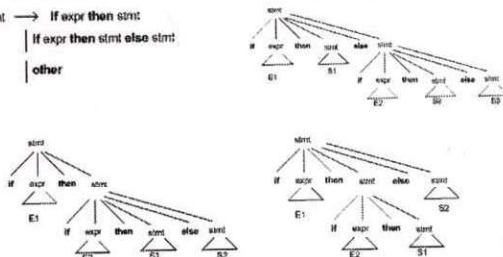
Ambiguity

- For some strings there exist more than one parse tree
- Or more than one leftmost derivation
- Or more than one rightmost derivation
- Example: $\text{id}+\text{id}*\text{id}$



Elimination of ambiguity

$\text{stmt} \rightarrow \text{if expr then stmt}$
 $\quad \mid \text{if expr then stmt else stmt}$
 $\quad \mid \text{other}$



Elimination of ambiguity (cont.)

- Idea:
 - A statement appearing between a **then** and an **else** must be matched

$\text{stmt} \rightarrow \text{matched_stmt} \mid \text{open_stmt}$
 $\text{matched_stmt} \rightarrow \text{if expr then matched_stmt else matched_stmt} \mid \text{other}$
 $\text{open_stmt} \rightarrow \text{if expr then stmt} \mid \text{if expr then matched_stmt else open_stmt}$

Recursive descent parsing

- Consists of a set of procedures, one for each nonterminal
- Execution begins with the procedure for start symbol
- A typical procedure for a non-terminal

```
void A() {
    choose an A-production, A->X1X2...Xk
    for (i=1 to k) {
        if (Xi is a nonterminal
            call procedure Xi();
        else if (Xi equals the current input symbol a)
            advance the input to the next symbol;
        else /* an error has occurred */
    }
}
```

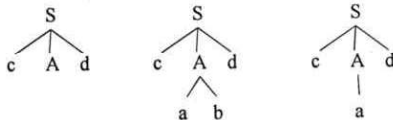
Recursive descent parsing (cont)

- General recursive descent may require backtracking
- The previous code needs to be modified to allow backtracking
- In general form it can't choose an A-production easily.
- So we need to try all alternatives
- If one failed the input pointer needs to be reset and another alternative should be tried
- Recursive descent parsers can't be used for left-recursive grammars

Example

S->cAd
A->ab | a

Input: cad



First and Follow

- First() is set of terminals that begins strings derived from
- If $\alpha \xRightarrow{*} \epsilon$ then ϵ is also in First(ϵ)
- In predictive parsing when we have $A \rightarrow \alpha \mid \beta$, if First(α) and First(β) are disjoint sets then we can select appropriate A-production by looking at the next input
- Follow(A), for any nonterminal A, is set of terminals that can appear immediately after A in some sentential form
 - If we have $S \Rightarrow \alpha A \beta$ for some α and β then a is in Follow(A)
- If A can be the rightmost symbol in some sentential form, then $\$$ is in Follow(A)

Computing First

- To compute First(X) for all grammar symbols X, apply following rules until no more terminals or ϵ can be added to any First set:
 1. If X is a terminal then First(X) = {X}.
 2. If X is a nonterminal and $X \rightarrow Y_1 Y_2 \dots Y_k$ is a production for some $k \geq 1$, then place a in First(X) if for some i a is in First(Y_i) and ϵ is in all of First(Y_1), ..., First(Y_{i-1}) that is $Y_1 \dots Y_{i-1} \Rightarrow \epsilon$. If ϵ is in First(Y_j) for $j=1, \dots, k$ then add ϵ to First(X).
 3. If $X \rightarrow \epsilon$ is a production then add ϵ to First(X)
- Example!

Computing follow

- To compute First(A) for all nonterminals A, apply following rules until nothing can be added to any follow set:
 1. Place \$ in Follow(S) where S is the start symbol
 2. If there is a production $A \rightarrow \alpha B \beta$ then everything in First(β) except ϵ is in Follow(B).
 3. If there is a production $A \rightarrow B$ or a production $A \rightarrow \alpha B \beta$ where First(β) contains ϵ , then everything in Follow(A) is in Follow(B)
- Example!

LL(1) Grammars

- Predictive parsers are those recursive descent parsers needing no backtracking
- Grammars for which we can create predictive parsers are called LL(1)
 - The first L means scanning input from left to right
 - The second L means leftmost derivation
 - And 1 stands for using one input symbol for lookahead
- A grammar G is LL(1) if and only if whenever $A \rightarrow \alpha \mid \beta$ are two distinct productions of G, the following conditions hold:
 - For no terminal a do α and β both derive strings beginning with a
 - At most one of α or β can derive empty string
 - If $\alpha \Rightarrow \epsilon$ then β does not derive any string beginning with a terminal in Follow(A).

Construction of predictive parsing table

- For each production $A \rightarrow \alpha$ in grammar do the following:
 - For each terminal a in First(α) add $A \rightarrow \alpha$ in $M[A,a]$
 - If ϵ is in First(α), then for each terminal b in Follow(A) add $A \rightarrow \epsilon$ to $M[A,b]$. If ϵ is in First(α) and $\$$ is in Follow(A), add $A \rightarrow \epsilon$ to $M[A,\$]$ as well
- If after performing the above, there is no production in $M[A,a]$ then set $M[A,a]$ to error

Example

	First	Follow
$E \rightarrow TE'$	{(, id}	{+, *,), \$}
$E' \rightarrow +TE' \mid \epsilon$	{(, id}	{+,), \$}
$T \rightarrow FT'$	{(, id}	{), \$}
$T' \rightarrow *FT' \mid \epsilon$	{+, e}	{), \$}
$F \rightarrow (E) \mid id$	{*, e}	{+,), \$}

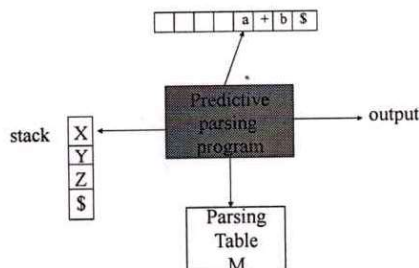
Non-terminal	Input Symbol					
	id	+	*	(	)	\$
E	$E \rightarrow TE'$			$E \rightarrow TE'$		
E'		$E' \rightarrow +TE'$			$E' \rightarrow \epsilon$	$E' \rightarrow \epsilon$
T	$T \rightarrow FT'$			$T \rightarrow FT'$		
T'		$T' \rightarrow \epsilon$	$T' \rightarrow *FT'$		$T' \rightarrow \epsilon$	$T' \rightarrow \epsilon$
F	$F \rightarrow id$			$F \rightarrow (E)$		

Another example

$S \rightarrow iEtSS' \mid a$
 $S' \rightarrow eS \mid \epsilon$
 $E \rightarrow b$

Non-terminal	Input Symbol					
	a	b	e	i	t	\$
S	$S \rightarrow a$			$S \rightarrow iEtSS'$		
S'			$S' \rightarrow \epsilon$ $S' \rightarrow eS$			$S' \rightarrow \epsilon$
E		$E \rightarrow b$				

Non-recursive predicting parsing



Predictive parsing algorithm

```

Set ip point to the first symbol of w;
Set X to the top stack symbol;
While (X <> $) { /* stack is not empty */
  if (X is a) pop the stack and advance ip;
  else if (X is a terminal) error();
  else if (M[X,a] is an error entry) error();
  else if (M[X,a] = X -> Y1Y2...Yk) {
    output the production X -> Y1Y2...Yk;
    pop the stack;
    push Yk,...,Y2,Y1 on to the stack with Y1 on top;
  }
  set X to the top stack symbol;
}

```

Example

- $id+id*ids$

Matched	Stack	Input	Action
	E\$	$id+id*id\$$	

Error recovery in predictive parsing

- Panic mode
 - Place all symbols in $Follow(A)$ into synchronization set for nonterminal A: skip tokens until an element of $Follow(A)$ is seen and pop A from stack.
 - Add to the synchronization set of lower level construct the symbols that begin higher level constructs
 - Add symbols in $First(A)$ to the synchronization set of nonterminal A
 - If a nonterminal can generate the empty string then the production deriving can be used as a default
 - If a terminal on top of the stack cannot be matched, pop the terminal, issue a message saying that the terminal was inserted

Example

Non-terminal	Input Symbol					
	id	+	*	(	)	\$
E	$E \rightarrow TE'$			$E \rightarrow TE$	synch	synch
E'	$E' \rightarrow +TE$			$E' \rightarrow \epsilon$	synch	$E' \rightarrow \epsilon$
T	$T \rightarrow FT'$	synch		$T \rightarrow FT'$	synch	synch
T'	$T' \rightarrow \epsilon$		$T' \rightarrow *FT'$		$T' \rightarrow \epsilon$	$T' \rightarrow \epsilon$
F	$F \rightarrow id$	synch	synch	$F \rightarrow (E)$	synch	synch

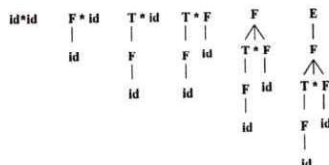
Stack	Input	Action
E\$	$)id*+id\$$	Error, Skip)
E\$	$id*+id\$$	id is in $First(E)$
TE'\$	$id*+id\$$	
FT'E'\$	$id*+id\$$	
idT'E'\$	$id*+id\$$	
T'E'\$	$*+id\$$	
*FT'E'\$	$+id\$$	
FT'E'\$	$+id\$$	Error, $M[F,+] = synch$
T'E'\$	$+id\$$	F has been popped

Bottom-up Parsing

Introduction

- Constructs parse tree for an input string beginning at the leaves (the bottom) and working towards the root (the top)
- Example: $id*id$

$E \rightarrow E + T \mid T$
 $T \rightarrow T * F \mid F$
 $F \rightarrow (E) \mid id$



Shift-reduce parser

- The general idea is to shift some symbols of input to the stack until a reduction can be applied
- At each reduction step, a specific substring matching the body of a production is replaced by the nonterminal at the head of the production
- The key decisions during bottom-up parsing are about when to reduce and about what production to apply
- A reduction is a reverse of a step in a derivation
- The goal of a bottom-up parser is to construct a derivation in reverse:
 - $E \Rightarrow T \Rightarrow T * F \Rightarrow T * id \Rightarrow F * id \Rightarrow id * id$

Handle pruning

- A Handle is a substring that matches the body of a production and whose reduction represents one step along the reverse of a rightmost derivation

Right sentential form	Handle	Reducing production
id*id	id	F->id
F*id	F	T->F
T*id	id	F->id
T*F	T*F	E->T*F

Shift reduce parsing

- A stack is used to hold grammar symbols
- Handle always appear on top of the stack
- Initial configuration:

Stack Input
\$ w\$

- Acceptance configuration

Stack Input
s\$ s

Shift reduce parsing (cont.)

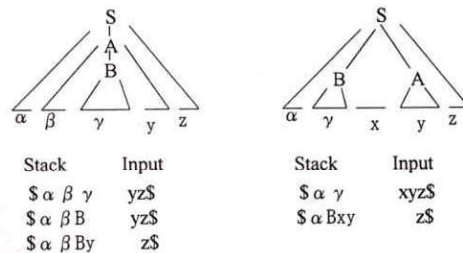
- Basic operations:

- Shift
- Reduce
- Accept
- Error

- Example: id*id

Stack	Input	Action
\$	id*id\$	shift
\$id	*id\$	reduce by F->id
\$F	*id\$	reduce by T->F
\$T	*id\$	shift
\$T*	id\$	shift
\$T*id	\$	reduce by F->id
\$T*F	\$	reduce by T->T*F
\$T	\$	reduce by E->T
\$E	\$	accept

Handle will appear on top of the stack



Conflicts during shift reduce parsing

- Two kind of conflicts
 - Shift/reduce conflict
 - Reduce/reduce conflict
- Example:

```
stmt -> if expr then stmt
      | if expr then stmt else stmt
      | other
```

Stack	Input
... if expr then stmt	else ...\$

Reduce/reduce conflict

```
stmt -> id(parameter_list)
stmt -> expr:=expr
parameter_list -> parameter_list, parameter
parameter_list -> parameter
parameter -> id
expr -> id(expr_list)
expr -> id
expr_list -> expr_list, expr
expr_list -> expr
```

Stack	Input
... id(id	,id) ...\$

LR Parsing

- The most prevalent type of bottom-up parsers
- LR(k), mostly interested on parsers with $k \leq 1$
- Why LR parsers?
 - Table driven
 - Can be constructed to recognize all programming language constructs
 - Most general non-backtracking shift-reduce parsing method
 - Can detect a syntactic error as soon as it is possible to do so
 - Class of grammars for which we can construct LR parsers are superset of those which we can construct LL parsers

States of an LR parser

- States represent set of items
- An LR(0) item of G is a production of G with the dot at some position of the body:
 - For $A \rightarrow XYZ$ we have following items
 - $A \rightarrow \cdot XYZ$
 - $A \rightarrow X \cdot YZ$
 - $A \rightarrow XY \cdot Z$
 - $A \rightarrow XYZ \cdot$
 - In a state having $A \rightarrow \cdot XYZ$ we hope to see a string derivable from XYZ next on the input.
 - What about $A \rightarrow X \cdot YZ$?

Constructing canonical LR(0) item sets

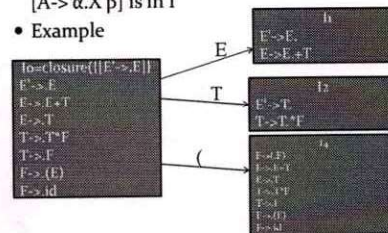
- Augmented grammar:
 - G with addition of a production: $S' \rightarrow S$
- Closure of item sets:
 - If I is a set of items, $\text{closure}(I)$ is a set of items constructed from I by the following rules:
 - Add every item in I to $\text{closure}(I)$
 - If $A \rightarrow \alpha \cdot B \beta$ is in $\text{closure}(I)$ and $B \rightarrow \gamma$ is a production then add the item $B \rightarrow \gamma \cdot$ to $\text{closure}(I)$.
- Example:

$E' \rightarrow E$
 $E \rightarrow E + T \mid T$
 $T \rightarrow T * F \mid F$
 $F \rightarrow (E) \mid \text{id}$

$\text{I} = \text{closure}(\{[E' \rightarrow \cdot E]\})$
 $E' \rightarrow \cdot E$
 $E \rightarrow \cdot E + T$
 $E \rightarrow \cdot T$
 $T \rightarrow \cdot T * F$
 $T \rightarrow \cdot F$
 $F \rightarrow \cdot (E)$
 $F \rightarrow \cdot \text{id}$

Constructing canonical LR(0) item sets (cont.)

- Goto (I, X) where I is an item set and X is a grammar symbol is closure of set of all items $[A \rightarrow \alpha X \cdot \beta]$ where $[A \rightarrow \alpha \cdot X \beta]$ is in I
- Example



Closure algorithm

```

SetOfItems CLOSURE(I) {
  J = I;
  repeat
    for (each item  $A \rightarrow \alpha \cdot B \beta$  in J)
      for (each production  $B \rightarrow \gamma$  of G)
        if ( $B \rightarrow \gamma$  is not in J)
          add  $B \rightarrow \gamma \cdot$  to J;
  until no more items are added to J on one round;
  return J;
}

```

GOTO algorithm

```

SetOfItems GOTO(I, X) {
  J = empty;
  if ( $A \rightarrow \alpha \cdot X \beta$  is in I)
    add CLOSURE( $A \rightarrow \alpha X \cdot \beta$ ) to J;
  return J;
}

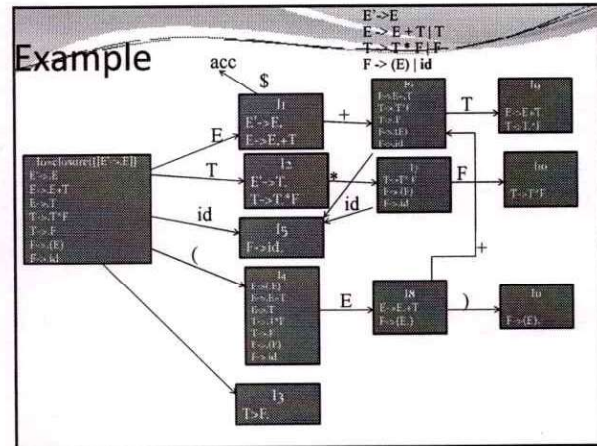
```


Canonical LR(0) items

```

Void items(G') {
  C = CLOSURE({S' -> S});
  repeat
    for (each set of items I in C)
      for (each grammar symbol X)
        if (GOTO(I, X) is not empty and not in C)
          add GOTO(I, X) to C;
  until no new set of items are added to C on a round;
}

```

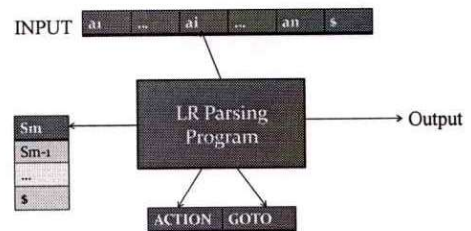


Use of LR(0) automaton

- Example: $id*id$

Line	Stack	Symbols	Input	Action
(1)	0	\$	$id*id\$$	Shift to 5
(2)	05	id	$*id\$$	Reduce by $F \rightarrow id$
(3)	03	id	$*id\$$	Reduce by $T \rightarrow F$
(4)	02	T	$*id\$$	Shift to 7
(5)	027	$T*$	$id\$$	Shift to 5
(6)	0275	$T*id$	$\$$	Reduce by $F \rightarrow id$
(7)	02710	$T*F$	$\$$	Reduce by $T \rightarrow T*F$
(8)	02	T	$\$$	Reduce by $E \rightarrow T$
(9)	01	E	$\$$	accept

LR-Parsing model



LR parsing algorithm

```

let a be the first symbol of w$;
while(i) { /*repeat forever */
  let s be the state on top of the stack;
  if (ACTION[s,a] = shift t) {
    push t onto the stack;
    let a be the next input symbol;
  } else if (ACTION[s,a] = reduce A->β) {
    pop |β| symbols of the stack;
    let state t now be on top of the stack;
    push GOTO[t,A] onto the stack;
    output the production A->β;
  } else if (ACTION[s,a]=accept) break; /* parsing is done */
  else call error-recovery routine;
}

```

Example

STATE	id	+	*	(	)	\$	E	T	F
0	S5			S4				1	3
1		S6					Acc		
2		R3	S7	R3	R3				
3		R	R7	R4	R4				
4	S5			S4			R	3	
5		R	R	R6	R6				
6	S5			S4				9	3
7	S5			S4					10
8		S6			S1				
9		R3	S7	R3	R3				
10		R3	R3	R3	R3				
11		R5	R5	R5	R5				

(0) $E \rightarrow E$
 (1) $E \rightarrow E + T$
 (2) $E \rightarrow T$
 (3) $T \rightarrow T * F$
 (4) $T \rightarrow F$
 (5) $F \rightarrow (E)$
 (6) $F \rightarrow id$

$id*id+id\$$

Line	Stack	Symbol	Input	Action
(1)	0	\$	$id*id+id\$$	Shift to 5
(2)	05	id	$*id+id\$$	Reduce by $F \rightarrow id$
(3)	03	F	$*id+id\$$	Reduce by $T \rightarrow F$
(4)	02	T	$*id+id\$$	Shift to 7
(5)	027	$T*$	$id+id\$$	Shift to 5
(6)	0275	$T*id$	$+id\$$	Reduce by $F \rightarrow id$
(7)	02710	$T*F$	$+id\$$	Reduce by $T \rightarrow T*F$
(8)	02	T	$+id\$$	Reduce by $E \rightarrow T$
(9)	01	E	$+id\$$	Shift
(10)	015	$E+$	$id\$$	Shift
(11)	0153	$E+F$	$\$$	Reduce by $T \rightarrow F$
(12)	0156	$E+T$	$\$$	Reduce by $E \rightarrow E+T$
(13)	01	E	$\$$	accept

Constructing SLR parsing table

- Method
- Construct $C = \{I_0, I_1, \dots, I_n\}$, the collection of LR(0) items for G'
- State i is constructed from state li :
 - If $[A \rightarrow \alpha \cdot \beta]$ is in li and $\text{Goto}(li, a) = lj$, then set $\text{ACTION}[i, a]$ to "shift j "
 - If $[A \rightarrow \alpha \cdot]$ is in li , then set $\text{ACTION}[i, a]$ to "reduce $A \rightarrow \alpha$ " for all a in $\text{follow}(A)$
 - If $[S' \rightarrow \cdot S]$ is in li , then set $\text{ACTION}[i, \$]$ to "Accept"
- If any conflicts appears then we say that the grammar is not SLR(1).
- If $\text{GOTO}(li, A) = lj$ then $\text{GOTO}[i, A] = j$
- All entries not defined by above rules are made "error"
- The initial state of the parser is the one constructed from the set of items containing $[S' \rightarrow \cdot S]$

Example grammar which is not SLR(1)

$S \rightarrow L=R \mid R$ $L \rightarrow *R \mid id$ $R \rightarrow L$				
I0 $S' \rightarrow \cdot S$ $S \rightarrow \cdot L=R$ $S \rightarrow \cdot R$ $L \rightarrow \cdot *R \mid$ $L \rightarrow \cdot id$ $R \rightarrow \cdot L$	I1 $S' \rightarrow S \cdot$ $S \rightarrow L \cdot =R$ $S \rightarrow R \cdot$ $L \rightarrow *L \cdot =R$ $R \rightarrow L \cdot$	I3 $S \rightarrow R \cdot$	I5 $L \rightarrow id \cdot$	I7 $L \rightarrow *R \cdot$
	I12 $S \rightarrow L \cdot =R$ $R \rightarrow L \cdot$	I14 $L \rightarrow *R \cdot$ $R \rightarrow L \cdot$ $L \rightarrow *R \cdot$ $L \rightarrow id \cdot$	I6 $S \rightarrow L \cdot =R$ $R \rightarrow L \cdot$ $L \rightarrow *R \cdot$ $L \rightarrow id \cdot$	I8 $R \rightarrow L \cdot$
				I9 $S \rightarrow L \cdot =R$
Action = Shift 6 Reduce $R \rightarrow L$				

More powerful LR parsers

- Canonical-LR or just LR method
 - Use lookahead symbols for items: LR(1) items
 - Results in a large collection of items
- LALR: lookaheads are introduced in LR(0) items

Canonical LR(1) items

- In LR(1) items each item is in the form: $[A \rightarrow \alpha \cdot \beta, a]$
- An LR(1) item $[A \rightarrow \alpha \cdot \beta, a]$ is valid for a viable prefix γ if there is a derivation $S \xRightarrow{*} \delta A w \xRightarrow{m} \delta \alpha \beta w$, where
 - $\Gamma = \delta \alpha$
 - Either a is the first symbol of w , or $w = \epsilon$ and a is $\$$
- Example:
 - $S \rightarrow BB$
 - $B \rightarrow aB \mid b$

$S \xRightarrow{*} aaBab \xRightarrow{m} aaaBab$
 Item $[B \rightarrow a \cdot B, a]$ is valid for $\gamma = aaa$
 and $w = ab$

Constructing LR(1) sets of items

```

SetOfItems Closure(I) {
  repeat
    for (each item  $[A \rightarrow \alpha \cdot \beta, a]$  in I)
      for (each production  $B \rightarrow \gamma$  in  $G'$ )
        for (each terminal  $b$  in  $\text{First}(\beta a)$ )
          add  $[B \rightarrow \gamma \cdot b, b]$  to set I;
  until no more items are added to I;
  return I;
}

SetOfItems Goto(I, X) {
  initialize J to be the empty set;
  for (each item  $[A \rightarrow \alpha \cdot X \beta, a]$  in I)
    add item  $[A \rightarrow \alpha X \cdot \beta, a]$  to set J;
  return closure(J);
}

void items( $G'$ ) {
  initialize C to Closure( $\{[S' \rightarrow \cdot S, \$]\}$ );
  repeat
    for (each set of items I in C)
      for (each grammar symbol X)
        if ( $\text{Goto}(I, X)$  is not empty and not in C)
          add  $\text{Goto}(I, X)$  to C;
  until no new sets of items are added to C;
}
  
```

Example

$S' \rightarrow S$
 $S \rightarrow CC$
 $C \rightarrow cC$
 $C \rightarrow d$

Canonical LR(1) parsing table

- Method
 - Construct $C = \{I_0, I_1, \dots, I_n\}$, the collection of LR(1) items for G'
 - State i is constructed from state l_i :
 - If $[A \rightarrow \alpha \cdot a\beta, b]$ is in l_i and $\text{Goto}(l_i, a) = l_j$, then set $\text{ACTION}[i, a]$ to "shift j "
 - If $[A \rightarrow \alpha \cdot, a]$ is in l_i , then set $\text{ACTION}[i, a]$ to "reduce $A \rightarrow \alpha$ "
 - If $[S' \rightarrow \cdot S, \$]$ is in l_i , then set $\text{ACTION}[i, \$]$ to "Accept"
 - If any conflicts appears then we say that the grammar is not LR(1).
 - If $\text{GOTO}(l_i, A) = l_j$ then $\text{GOTO}[i, A] = j$
 - All entries not defined by above rules are made "error"
 - The initial state of the parser is the one constructed from the set of items containing $[S' \rightarrow \cdot S, \$]$

Example

$S' \rightarrow S$
 $S \rightarrow CC$
 $C \rightarrow cC$
 $C \rightarrow d$

LALR Parsing Table

- For the previous example we had:



- State merges cant produce Shift-Reduce conflicts. Why?
- But it may produce reduce-reduce conflict

Example of RR conflict in state merging

$S' \rightarrow S$
 $S \rightarrow aAd \mid bBd \mid aBe \mid bAe$
 $A \rightarrow c$
 $B \rightarrow c$

An easy but space-consuming LALR table construction

- Method:
 1. Construct $C = \{I_0, I_1, \dots, I_n\}$ the collection of LR(1) items.
 2. For each core among the set of LR(1) items, find all sets having that core, and replace these sets by their union.
 3. Let $C' = \{J_0, J_1, \dots, J_m\}$ be the resulting sets. The parsing actions for state i , is constructed from J_i as before. If there is a conflict grammar is not LALR(1).
 4. If J is the union of one or more sets of LR(1) items, that is $J = I_1 \cup I_2 \cup \dots \cup I_k$ then the cores of $\text{Goto}(I_1, X)$, ..., $\text{Goto}(I_k, X)$ are the same and is a state like K , then we set $\text{Goto}(J, X) = K$.
- This method is not efficient, a more efficient one is discussed in the book

Compaction of LR parsing table

- Many rows of action tables are identical
 - Store those rows separately and have pointers to them from different states
 - Make lists of (terminal-symbol, action) for each state
 - Implement Goto table by having a link list for each nonterminal in the form (current state, next state)

Using ambiguous grammars

 $E \rightarrow E + E$
 $E \rightarrow E * E$
 $E \rightarrow (E)$
 $E \rightarrow id$
10: $E' \rightarrow E$ $E \rightarrow E + E$ $E \rightarrow E * E$ $E \rightarrow (E)$ $E \rightarrow id$ 13: $E \rightarrow id$ 11: $E' \rightarrow E$ $E \rightarrow E + E$ $E \rightarrow E * E$ 14: $E \rightarrow E + E$ $E \rightarrow E + E$ $E \rightarrow E * E$ $E \rightarrow (E)$ $E \rightarrow id$ 12: $E \rightarrow (E)$ $E \rightarrow E + E$ $E \rightarrow E * E$ 15: $E \rightarrow E * E$ $E \rightarrow E + E$ $E \rightarrow E * E$ $E \rightarrow (E)$ $E \rightarrow id$

STATE	ACTION							GO TO
	id	+	*	(	)	\$	E	
0	S ₀			S ₁				1
1		S ₄	S ₅			Acc		
2	S ₃		S ₂					6
3		R ₄	R ₄		R ₄	R ₄		
4	S ₃			S ₂				7
5	S ₃			S ₂				8
6		S ₄	S ₅					
7		R ₁	S ₅		R ₁	R ₁		
8		R ₂	R ₂		R ₂	R ₂		
9		R ₃	R ₃		R ₃	R ₃		

16: $E \rightarrow (E)$ $E \rightarrow E + E$ $E \rightarrow E * E$ 18: $E \rightarrow E * E$ $E \rightarrow E + E$ $E \rightarrow E * E$ 17: $E \rightarrow E + E$ $E \rightarrow E + E$ $E \rightarrow E * E$ 19: $E \rightarrow (E)$

Readings

- Chapter 4 of the book

Compiler course

Chapter 5
Syntax Directed Translation

Outline

- Syntax Directed Definitions
- Evaluation Orders of SDD's
- Applications of Syntax Directed Translation
- Syntax Directed Translation Schemes

Introduction

- We can associate information with a language construct by attaching attributes to the grammar symbols.
- A syntax directed definition specifies the values of attributes by associating semantic rules with the grammar productions.

Production	Semantic Rule
$E \rightarrow E1 + T$	$E.code = E1.code T.code '+'$

- We may alternatively insert the semantic actions inside the grammar

$E \rightarrow E1 + T \{ \text{print '+'} \}$

Syntax Directed Definitions

- A SDD is a context free grammar with attributes and rules
- Attributes are associated with grammar symbols and rules with productions
- Attributes may be of many kinds: numbers, types, table references, strings, etc.
- Synthesized attributes
 - A synthesized attribute at node N is defined only in terms of attribute values of children of N and at N it
- Inherited attributes
 - An inherited attribute at node N is defined only in terms of attribute values at N's parent, N itself and N's siblings

Example of S-attributed SDD

Production	Semantic Rules
1) $L \rightarrow E n$	$L.val = E.val$
2) $E \rightarrow E1 + T$	$E.val = E1.val + T.val$
3) $E \rightarrow T$	$E.val = T.val$
4) $T \rightarrow T1 * F$	$T.val = T1.val * F.val$
5) $T \rightarrow F$	$T.val = F.val$
6) $F \rightarrow (E)$	$F.val = E.val$
7) $F \rightarrow \text{digit}$	$F.val = \text{digit.lexval}$

Example of mixed attributes

Production	Semantic Rules
1) $T \rightarrow FT'$	$T'.inh = F.val$ $T.val = T'.syn$
2) $T' \rightarrow *FT'$	$T'.inh = T'.inh * F.val$ $T'.syn = T'.syn$
3) $T' \rightarrow \epsilon$	$T'.syn = T'.inh$
1) $F \rightarrow \text{digit}$	$F.val = F.val - \text{digit.lexval}$

Evaluation orders for SDD's

- A dependency graph is used to determine the order of computation of attributes
- Dependency graph
 - For each parse tree node, the parse tree has a node for each attribute associated with that node
 - If a semantic rule defines the value of synthesized attribute A.b in terms of the value of X.c then the dependency graph has an edge from X.c to A.b
 - If a semantic rule defines the value of inherited attribute B.c in terms of the value of X.a then the dependency graph has an edge from X.c to B.c
- Example!

Ordering the evaluation of attributes

- If dependency graph has an edge from M to N then M must be evaluated before the attribute of N
- Thus the only allowable orders of evaluation are those sequence of nodes $N_1, N_2, \dots, N_k$ such that if there is an edge from N_i to N_j then $i < j$
- Such an ordering is called a topological sort of a graph
- Example!

S-Attributed definitions

- An SDD is S-attributed if every attribute is synthesized
- We can have a post-order traversal of parse-tree to evaluate attributes in S-attributed definitions

```
postorder(N) {
    for (each child C of N, from the left) postorder(C);
    evaluate the attributes associated with node N;
}
```

- S-Attributed definitions can be implemented during bottom-up parsing without the need to explicitly create parse trees

L-Attributed definitions

- A SDD is L-Attributed if the edges in dependency graph goes from Left to Right but not from Right to Left.
- More precisely, each attribute must be either
 - Synthesized
 - Inherited, but if there is a production $A \rightarrow X_1 X_2 \dots X_n$ and there is an inherited attribute $X_i.a$ computed by a rule associated with this production, then the rule may only use:
 - Inherited attributes associated with the head A
 - Either inherited or synthesized attributes associated with the occurrences of symbols $X_1, X_2, \dots, X_{i-1}$ located to the left of X_i
 - Inherited or synthesized attributes associated with this occurrence of X_i itself, but in such a way that there is no cycle in the graph

Application of Syntax Directed Translation

- Type checking and intermediate code generation (chapter 6)
- Construction of syntax trees
 - Leaf nodes: Leaf(op, val)
 - Interior node: Node(op, c1, c2, ..., ck)
- Example:

Production	Semantic Rules
1) $E \rightarrow E1 + T$	$E.node = \text{new node}('+', E1.node, T.node)$
2) $E \rightarrow E1 - T$	$E.node = \text{new node}('-', E1.node, T.node)$
3) $E \rightarrow T$	$E.node = T.node$
4) $T \rightarrow (E)$	$T.node = E.node$
5) $T \rightarrow id$	$T.node = \text{new Leaf}(id, id.entry)$
6) $T \rightarrow num$	$T.node = \text{new Leaf}(num, num.val)$

Syntax tree for L-attributed definition

Production	Semantic Rules
1) $E \rightarrow TE'$	$E.node = E'.syn$ $E'.inh = T.node$
2) $E' \rightarrow + TE1'$	$E1'.inh = \text{new node}('+', E'.inh, T.node)$ $E'.syn = E1'.syn$
3) $E' \rightarrow - TE1'$	$E1'.inh = \text{new node}('-', E'.inh, T.node)$ $E'.syn = E1'.syn$
4) $E' \rightarrow \epsilon$	$E'.syn = E'.inh$
5) $T \rightarrow (E)$	$T.node = E.node$
6) $T \rightarrow id$	$T.node = \text{new Leaf}(id, id.entry)$
7) $T \rightarrow num$	$T.node = \text{new Leaf}(num, num.val)$

Syntax directed translation schemes

- An SDT is a Context Free grammar with program fragments embedded within production bodies
- Those program fragments are called semantic actions
- They can appear at any position within production body
- Any SDT can be implemented by first building a parse tree and then performing the actions in a left-to-right depth first order
- Typically SDT's are implemented during parsing without building a parse tree

Postfix translation schemes

- Simplest SDDs are those that we can parse the grammar bottom-up and the SDD is s-attributed
- For such cases we can construct SDT where each action is placed at the end of the production and is executed along with the reduction of the body to the head of that production
- SDT's with all actions at the right ends of the production bodies are called postfix SDT's

Example of postfix SDT

```

1) L -> E n      {print(E.val);}
2) E -> E1 + T   {E.val=E1.val+T.val;}
3) E -> T        {E.val = T.val;}
4) T -> T1 * F    {T.val=T1.val*T.val;}
5) T -> F        {T.val=F.val;}
6) F -> (E)      {F.val=E.val;}
7) F -> digit    {F.val=digit.lexval;}

```

Parse-Stack implementation of postfix SDT's

- In a shift-reduce parser we can easily implement semantic action using the parser stack
- For each nonterminal (or state) on the stack we can associate a record holding its attributes
- Then in a reduction step we can execute the semantic action at the end of a production to evaluate the attribute(s) of the non-terminal at the leftside of the production
- And put the value on the stack in replace of the rightside of production

Example

```

L -> E n      {print(stack[top-1].val);
               top=top-1;}
E -> E1 + T   {stack[top-2].val=stack[top-2].val+stack.val;
               top=top-2;}
E -> T
T -> T1 * F    {stack[top-2].val=stack[top-2].val*stack.val;
               top=top-2;}
T -> F
F -> (E)      {stack[top-2].val=stack[top-1].val
               top=top-2;}
F -> digit

```

SDT's with actions inside productions

- For a production $B \rightarrow X \{a\} Y$
 - If the parse is bottom-up then we perform action "a" as soon as this occurrence of X appears on the top of the parser stack
 - If the parser is top down we perform "a" just before we expand Y
- Sometimes we cant do things as easily as explained above
- One example is when we are parsing this SDT with a bottom-

```

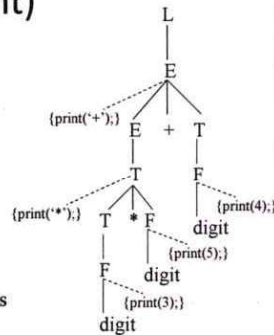
1) L -> E n
2) E -> {print('+');} E1 + T
3) E -> T
4) T -> {print('*');} T1 * F
5) T -> F
6) F -> (E)
7) F -> digit {print(digit.lexval);}

```


SDT's with actions inside productions (cont)

- Any SDT can be implemented as follows

 1. Ignore the actions and produce a parse tree
 2. Examine each interior node N and add actions as new children at the correct position
 3. Perform a postorder traversal and execute actions when their nodes are visited



SDT's for L-Attributed definitions

- We can convert an L-attributed SDD into an SDT using following two rules:
 - Embed the action that computes the inherited attributes for a nonterminal A immediately before that occurrence of A. if several inherited attributes of A are dependent on one another in an acyclic fashion, order them so that those needed first are computed first
 - Place the action of a synthesized attribute for the head of a production at the end of the body of the production

Example

```
S -> while (C) S1      L1=new();
                        L2=new();
                        S1.next=L1;
                        C.false=S.next;
                        C.true=L2;

                        S.code=label||L1||C.code||label||L2||S1.code
```

```
S -> while ( {L1=new();L2=new();C.false=S.next;C.true=L2;}
C) {S1.next=L1;}
S1 {S.code=label||L1||C.code||label||L2||S1.code;}
```

Readings

- Chapter 5 of the book

Compiler course

Chapter 8
Code Generation

Outline

- Code Generation Issues
- Target language Issues
- Addresses in Target Code
- Basic Blocks and Flow Graphs
- Optimizations of Basic Blocks
- A Simple Code Generator
- Peephole optimization
- Register allocation and assignment
- Instruction selection by tree rewriting

Introduction

- The final phase of a compiler is code generator
- It receives an intermediate representation (IR) with supplementary information in symbol table
- Produces a semantically equivalent target program
- Code generator main tasks:
 - Instruction selection
 - Register allocation and assignment
 - Instruction ordering



Issues in the Design of Code Generator

- The most important criterion is that it produces correct code
- Input to the code generator
 - IR + Symbol table
 - We assume front end produces low-level IR, i.e. values of names in it can be directly manipulated by the machine instructions.
 - Syntactic and semantic errors have been already detected
- The target program
 - Common target architectures are: RISC, CISC and Stack based machines
 - In this chapter we use a very simple RISC-like computer with addition of some CISC-like addressing modes

complexity of mapping

- the level of the IR
- the nature of the instruction-set architecture
- the desired quality of the generated code.

$x=y+z$	$a=b+c$ $d=a+e$
LD R0, y	LD R0, b
ADD R0, R0, z	ADD R0, R0, c
ST x, R0	ST a, R0
	LD R0, a
	ADD R0, R0, e
	ST d, R0

Register allocation

- Two subproblems
 - Register allocation: selecting the set of variables that will reside in registers at each point in the program
 - Register assignment: selecting specific register that a variable reside in
- Complications imposed by the hardware architecture
 - Example: register pairs for multiplication and division

$t=a+b$ $t=t*c$ $T=t/d$	$t=a+b$ $t=t*c$ $T=t/d$
L R1, a	L R0, a
A R1, b	A R0, b
M R0, c	M R0, c
D R0, d	SRDA R0, 32
ST R1, t	D R0, d
	ST R1, t

A simple target machine model

- Load operations: LD r,x and LD r1, r2
- Store operations: ST x,r
- Computation operations: OP dst, src1, src2
- Unconditional jumps: BR L
- Conditional jumps: Bcond r, L like BLTZ r, L

Addressing Modes

- variable name: x
- indexed address: a(r) like LD R1, a(R2) means
 $R1 = \text{contents}(a + \text{contents}(R2))$
- integer indexed by a register : like LD R1, 100(R2)
- Indirect addressing mode: *r and *100(r)
- immediate constant addressing mode: like LD R1, #100

b = a [i]

```
LD R1, i           //R1 = i
MUL R1, R1, 8       //R1 = R1 * 8
LD R2, a(R1)
//R2=contents(a+contents(R1))
ST b, R2           //b = R2
```

a[j] = c

```
LD R1, c           //R1 = c
LD R2, j           // R2 = j
MUL R2, R2, 8       //R2 = R2 * 8
ST a(R2), R1
//contents(a+contents(R2))=R1
```

x=*p

```
LD R1, p           //R1 = p
LD R2, o(R1)       // R2 =
// contents(o+contents(R1))
ST x, R2           // x=R2
```

conditional-jump three-address instruction

If x<y goto L

```
LD R1, x           // R1 = x
LD R2, y           // R2 = y
SUB R1, R1, R2     // R1 = R1 - R2
BLTZ R1, M         // if R1 < 0 jump to M
```

costs associated with the addressing modes

- LD R₀, R₁ cost = 1
- LD R₀, M cost = 2
- LD R₁, *100(R₂) cost = 3

Addresses in the Target Code

- A statically determined area Code
- A statically determined data area Static
- A dynamically managed area Heap
- A dynamically managed area Stack

three-address statements for procedure calls and returns

- call callee
- Return
- Halt
- action

Target program for a sample call and return

```

// code for c
100: ACTION;           // code for c
120: ST 364, #140      // save return address 140 in location 364
132: BR 200            // call p
140: ACTION;           // return to operating system
160: HALT              // return to operating system

// code for p
200: ACTION;           // code for p
220: BR #364           // return to address saved in location 364

// 300-363 hold activation record for c
300:                    // return address
304:                    // local data for c
...
// 364-451 hold activation record for p
364:                    // return address
368:                    // local data for p

```

Stack Allocation

```

LD SP, #stackStart      // initialize the stack
code for the first procedure
HALT                    // terminate execution

ADD SP, SP, #caller.recordSize // increment stack pointer
ST *SP, #here + 16       // save return address
BR callee.codeArea       // return to caller
                          // Branch to called procedure

Return to caller
in Callee: BR *0(SP)
in caller: SUB SP, SP, #caller.recordsize

```

Target code for stack allocation

```

100: LD SP, #000        // initialize the stack
108: ACTION;           // code for action1
128: ADD SP, SP, #size   // call sequence begins
136: ST *SP, #152        // push return address
144: BR 300              // call q
152: SUB SP, SP, #size   // restore SP
160: ACTION;           // return
180: HALT

// code for m
200: ACTION;           // code for m
220: BR #0(SP)          // return

// code for p
300: ACTION;           // code for p
320: ADD SP, SP, #size   // contains a unconditional jump to 456
328: ST *SP, #364        // push return address
336: BR 300              // call p
344: ACTION;           // return
360: SUB SP, SP, #size   // push return address
368: BR 300              // call q
376: SUB SP, SP, #size   // restore SP
384: ACTION;           // return
400: BR #0(SP)          // return
416: SUB SP, SP, #size   // push return address
424: ST *SP, #440        // call q
432: BR #0(SP)          // return
440: SUB SP, SP, #size   // restore SP
448: BR #0(SP)          // return
456: BR #0(SP)          // return
600:                    // stack starts here

```


Basic blocks and flow graphs

- Partition the intermediate code into basic blocks
 - The flow of control can only enter the basic block through the first instruction in the block. That is, there are no jumps into the middle of the block.
 - Control will leave the block without halting or branching, except possibly at the last instruction in the block.
- The basic blocks become the nodes of a flow graph

rules for finding leaders

- The first three-address instruction in the intermediate code is a leader.
- Any instruction that is the target of a conditional or unconditional jump is a leader.
- Any instruction that immediately follows a conditional or unconditional jump is a leader.

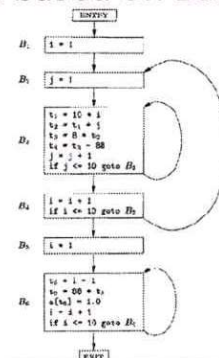
Intermediate code to set a 10*10 matrix to an identity matrix

```

for i from 1 to 10 do
  for j from 1 to 10 do
    a[i,j] = 0.0;
  for i from 1 to 10 do
    a[i,i] = 1.0;
1) i = 1
2) j = 1
3) t1 = 10 * i
4) t2 = t1 + j
5) t3 = 8 * t2
6) t4 = t3 - 88
7) a[t4] = 0.0
8) j = j + 1
9) if j <= 10 goto (3)
10) i = i + 1
11) if i <= 10 goto (2)
12) i = 1
13) t5 = i - 1
14) t6 = 88 * t5
15) a[t6] = 1.0
16) i = i + 1
17) if i <= 10 goto (13)

```

Flow graph based on Basic Blocks



liveness and next-use information

- We wish to determine for each three address statement $x=y+z$ what the next uses of x , y and z are.
- Algorithm:
 - Attach to statement i the information currently found in the symbol table regarding the next use and liveness of x , y , and z .
 - In the symbol table, set x to "not live" and "no next use."
 - In the symbol table, set y and z to "live" and the next uses of y and z to i .

DAG representation of basic blocks

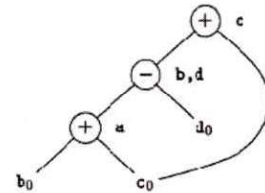
- There is a node in the DAG for each of the initial values of the variables appearing in the basic block.
- There is a node N associated with each statement s within the block. The children of N are those nodes corresponding to statements that are the last definitions, prior to s , of the operands used by s .
- Node N is labeled by the operator applied at s , and also attached to N is the list of variables for which it is the last definition within the block.
- Certain nodes are designated *output nodes*. These are the nodes whose variables are *live on exit* from the block.

Code improving transformations

- We can eliminate *local common subexpressions*, that is, instructions that compute a value that has already been computed.
- We can eliminate *dead code*, that is, instructions that compute a value that is never used.
- We can reorder statements that do not depend on one another; such reordering may reduce the time a temporary value needs to be preserved in a register.
- We can apply algebraic laws to reorder operands of three-address instructions, and sometimes thereby simplify the computation.

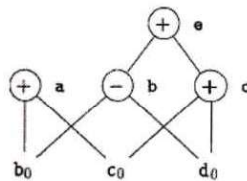
DAG for basic block

$a = b + c$
 $b = a - d$
 $c = b + c$
 $d = a - d$



DAG for basic block

$a = b + c;$
 $b = b - d$
 $c = c + d$
 $e = b + c$

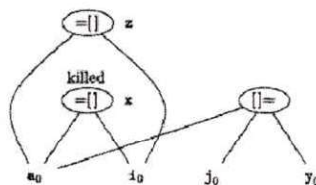


array accesses in a DAG

- An assignment from an array, like $x = a[i]$, is represented by creating a node with operator $=[]$ and two children representing the initial value of the array, a_0 in this case, and the index i . Variable x becomes a label of this new node.
- An assignment to an array, like $a[j] = y$, is represented by a new node with operator $[]=$ and three children representing a_0 , j and y . There is no variable labeling this node. What is different is that the creation of this node *kills* all currently constructed nodes whose value depends on a_0 . A node that has been killed cannot receive any more labels; that is, it cannot become a common subexpression.

DAG for a sequence of array assignments

$x = a[i]$
 $a[j] = y$
 $z = a[i]$



Rules for reconstructing the basic block from a DAG

- The order of instructions must respect the order of nodes in the DAG. That is, we cannot compute a node's value until we have computed a value for each of its children.
- Assignments to an array must follow all previous assignments to, or evaluations from, the same array, according to the order of these instructions in the original basic block.
- Evaluations of array elements must follow any previous (according to the original block) assignments to the same array. The only permutation allowed is that two evaluations from the same array may be done in either order, as long as neither crosses over an assignment to that array.
- Any use of a variable must follow all previous (according to the original block) procedure calls or indirect assignments through a pointer.
- Any procedure call or indirect assignment through a pointer must follow all previous (according to the original block) evaluations of any variable.

principal uses of registers

- In most machine architectures, some or all of the operands of an operation must be in registers in order to perform the operation.
- Registers make good temporaries - places to hold the result of a subexpression while a larger expression is being evaluated, or more generally, a place to hold a variable that is used only within a single basic block.
- Registers are often used to help with run-time storage management, for example, to manage the run-time stack, including the maintenance of stack pointers and possibly the top elements of the stack itself.

Descriptors for data structure

- For each available register, a register descriptor keeps track of the variable names whose current value is in that register. Since we shall use only those registers that are available for local use within a basic block, we assume that initially, all register descriptors are empty. As the code generation progresses, each register will hold the value of zero or more names.
- For each program variable, an address descriptor keeps track of the location or locations where the current value of that variable can be found. The location might be a register, a memory address, a stack location, or some set of more than one of these. The information can be stored in the symbol-table entry for that variable name.

Machine Instructions for Operations

- Use $\text{getReg}(x = y + z)$ to select registers for x , y , and z . Call these R_x , R_y and R_z .
- If y is not in R_y (according to the register descriptor for R_y), then issue an instruction $\text{LD } R_y, y'$, where y' is one of the memory locations for y (according to the address descriptor for y).
- Similarly, if z is not in R_z , issue an instruction $\text{LD } R_z, z'$, where z' is a location for z .
- Issue the instruction $\text{ADD } R_x, R_y, R_z$.

Rules for updating the register and address descriptors

- For the instruction LD R, x
 - Change the register descriptor for register R so it holds only x.
 - Change the address descriptor for x by adding register R as an additional location.
- For the instruction ST x, R_x, change the address descriptor for x to include its own memory location.
- For an operation such as ADD R_x, R_y, R_z implementing a three-address instruction $x = y + x$
 - Change the register descriptor for R_x so that it holds only x.
 - Change the address descriptor for x so that its only location is R_x. Note that the memory location for x is *not* now in the address descriptor for x.
 - Remove R_y from the address descriptor of any variable other than x.
- When we process a copy statement $x = y$, after generating the load for y into register R_y, if needed, and after managing descriptors as for all load statements (per rule 1):
 - Add x to the register descriptor for R_y.
 - Change the address descriptor for x so that its only location is R_y.

Instructions generated and the changes in the register and address descriptors

	R1	R2	R3	a	b	c	d	t	u	v	
a = d LD R2, d											
	u	a	d	v	R2	b	c	d	R2	R1	R3
d = v + u ADD R1, R3, R1											
	d	a	v	R2	b	c	R1			R3	
exit											
ST a, R2 ST d, R1											
	d	a	v	a, R2	b	c	d, R1			R3	
MOV R3, R2, R1											
	u	t	v	a	b	c	d	R2	R1	R3	

Rules for picking register R_v for y

- If y is currently in a register, pick a register already containing y as R_y . Do not issue a machine instruction to load this register, as none is needed.
- If y is not in a register, but there is a register that is currently empty, pick one such register as R_y .
- The difficult case occurs when y is not in a register, and there is no register that is currently empty. We need to pick one of the allowable registers anyway, and we need to make it safe to reuse.

Possibilities for value of R

- If the address descriptor for v says that v is somewhere besides R , then we are OK.
- If v is x , the value being computed by instruction I , and x is not also one of the other operands of instruction I (z in this example), then we are OK. The reason is that in this case, we know this value of x is never again going to be used, so we are free to ignore it.
- Otherwise, if v is not used later (that is, after the instruction I , there are no further uses of v , and if v is live on exit from the block, then v is recomputed within the block), then we are OK.
- If we are not OK by one of the first two cases, then we need to generate the store instruction **ST** v, R to place a copy of v in its own memory location. This operation is called a spill.

Selection of the register R_x

1. Since a new value of x is being computed, a register that holds only x is always an acceptable choice for R_x .
2. If y is not used after instruction I , and R_y holds only y after being loaded, R_y can also be used as R_x . A similar option holds regarding z and R_x .

Possibilities for value of R

- If the address descriptor for v says that v is somewhere besides R , then we are OK.
- If v is x , the value being computed by instruction I , and x is not also one of the other operands of instruction I (z in this example), then we are OK. The reason is that in this case, we know this value of x is never again going to be used, so we are free to ignore it.
- Otherwise, if v is not used later (that is, after the instruction I , there are no further uses of v , and if v is live on exit from the block, then v is recomputed within the block), then we are OK.
- If we are not OK by one of the first two cases, then we need to generate the store instruction **ST** v, R to place a copy of v in its own memory location. This operation is called a spill.

Characteristic of peephole optimizations

- Redundant-instruction elimination
- Flow-of-control optimizations
- Algebraic simplifications
- Use of machine idioms

Redundant-instruction elimination

- LD a, R_0
ST R_0, a
- if $debug == 1$ goto L_1
goto L_2
L1: print debugging information
L2:

Flow-of-control optimizations

goto L_1	if $a < b$ goto L_1
...	...
L1: goto L_2	L1: goto L_2

Can be replaced by:	Can be replaced by:
goto L_2	if $a < b$ goto L_2
...	...
L1: goto L_2	L1: goto L_2

Algebraic simplifications

- $X = X + 0$
- $X = X * 1$

Register Allocation and Assignment

- Global Register Allocation
- Usage Counts
- Register Assignment for Outer Loops
- Register Allocation by Graph Coloring

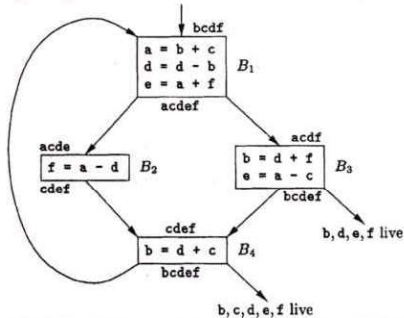
Global register allocation

- Previously explained algorithm does local (block based) register allocation
- This resulted that all live variables be stored at the end of block
- To save some of these stores and their corresponding loads, we might arrange to assign registers to frequently used variables and keep these registers consistent across block boundaries (globally)
- Some options are:
 - Keep values of variables used in loops inside registers
 - Use graph coloring approach for more globally allocation

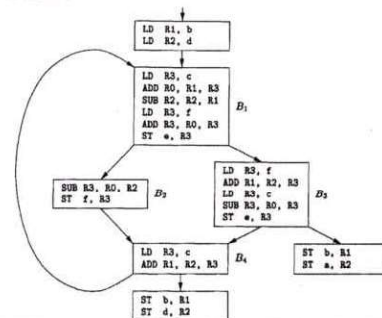
Usage counts

- For the loops we can approximate the saving by register allocation as:
 - Sum over all blocks (B) in a loop (L)
 - For each uses of x before any definition in the block we add one unit of saving
 - If x is live on exit from B and is assigned a value in B, then we ass 2 units of saving

Flow graph of an inner loop



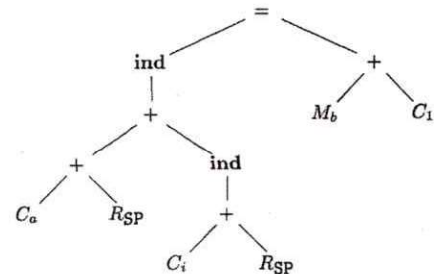
Code sequence using global register assignment



Register allocation by Graph coloring

- Two passes are used
 - Target-machine instructions are selected as though there are an infinite number of symbolic registers
- Assign physical registers to symbolic ones
 - Create a register-interference graph
 - Nodes are symbolic registers and edges connects two nodes if one is live at a point where the other is defined.
 - For example in the previous example an edge connects a and d in the graph
 - Use a graph coloring algorithm to assign registers.

Intermediate-code tree for $a[i]=b+1$



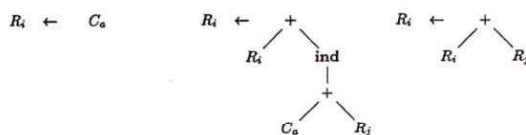
Tree-rewriting rules

1)	$R_i \leftarrow C_a$	{ LD $R_i, \#a$ }
2)	$R_i \leftarrow M_x$	{ LD R_i, x }
3)	$M \leftarrow M_x$	{ ST x, R_i }
4)	$M \leftarrow \text{ind } R_i$	{ ST $*R_i, R_j$ }
5)	$R_i \leftarrow \text{ind } + C_a R_j$	{ LD $R_i, a(R_j)$ }
6)	$R_i \leftarrow + R_i \text{ ind } + C_a R_j$	{ ADD $R_i, R_i, a(R_j)$ }
7)	$R_i \leftarrow + R_i R_j$	{ ADD R_i, R_i, R_j }
8)	$R_i \leftarrow + R_i C_1$	{ INC R_i }

Syntax-directed translation scheme

- | | | |
|-----|--|----------------------------|
| 1) | $R_i \rightarrow c_a$ | { LD $R_i, \#a$ } |
| 2) | $R_i \rightarrow M_x$ | { LD R_i, x } |
| 3) | $M \rightarrow = M_x R_i$ | { ST x, R_i } |
| 4) | $M \rightarrow = \text{ind } R_i R_j$ | { ST $*R_i, R_j$ } |
| 5) | $R_i \rightarrow \text{ind } + c_a R_j$ | { LD $R_i, a(R_j)$ } |
| 6) | $R_i \rightarrow + R_i \text{ ind } + c_a R_j$ | { ADD $R_i, R_i, a(R_j)$ } |
| 7) | $R_i \rightarrow + R_i R_j$ | { ADD R_i, R_i, R_j } |
| 8) | $R_i \rightarrow + R_i c_1$ | { INC R_i } |
| 9) | $R \rightarrow \text{sp}$ | |
| 10) | $M \rightarrow \text{m}$ | |

An instruction set for tree matching

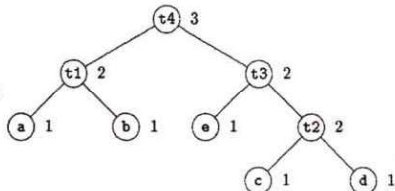


Ershov Numbers

- Label any leaf 1.
- The label of an interior node with one child is the label of its child.
- The label of an interior node with two children is
 - The larger of the labels of its children, if those labels are different.
 - One plus the label of its children if the labels are the same.

A tree labeled with Ershov numbers

$t1 = a - b$
 $t2 = c + d$
 $t3 = e * t2$
 $t4 = t1 + t3$



Generating code from a labeled expression tree

- To generate machine code for an interior node with label k and two children with equal labels (which must be $k-1$) do the following:
 - Recursively generate code for the right child, using base $b+1$. The result of the right child appears in register R_{b+k} .
 - Recursively generate code for the left child, using base b ; the result appears in R_{b+k-1} .
 - Generate the instruction $OP\ R_{b+k-1}, R_{b+k-1}, R_{b+k}$, where OP is the appropriate operation for the interior node in question.
- Suppose we have an interior node with label k and children with unequal labels. Then one of the children, which we'll call the "big" child, has label k , and the other child, the "little" child, has some label $m < k$. Do the following to generate code for this interior node, using base b :
 - Recursively generate code for the big child, using base b ; the result appears in register R_{b+k-1} .
 - Recursively generate code for the small child, using base b ; the result appears in register R_{b+m-1} . Note that since $m < k$, neither R_{b+k-1} nor any higher-numbered register is used.
 - Generate the instruction $OP\ R_{b+k-1}, R_{b+m-1}, R_{b+k-1}$, or the instruction $OP\ R_{b+k-1}, R_{b+k-1}, R_{b+m-1}$, depending on whether the big child is the right or left child, respectively.
- For a leaf representing operand x , if the base is b generate the instruction $LD\ R_b, x$.

Optimal three-register code

```

LD R3, d
LD R2, c
ADD R3, R2, R3
LD R2, e
MUL R3, R2, R3
LD R2, b
LD R1, a
SUB R2, R1, R2
ADD R3, R2, R3
  
```

Evaluating Expressions with an Insufficient Supply of Registers

- Node N has at least one child with label r or greater. Pick the larger child (or either if their labels are the same) to be the "big" child and let the other child be the "little" child.
- Recursively generate code for the big child, using base $b = 1$. The result of this evaluation will appear in register R_r .
- Generate the machine instruction $ST\ t_k, R_r$, where t_k is a temporary variable used for temporary results used to help evaluate nodes with label k .
- Generate code for the little child as follows. If the little child has label r or greater, pick base $b=1$. If the label of the little child is $j < r$, then pick $b=r-j$. Then recursively apply this algorithm to the little child; the result appears in R_r .
- Generate the instruction $LD\ R_r-l, t_k$.
- If the big child is the right child of N , then generate the instruction $OP\ R_r, R_r-l, R_r$. If the big child is the left child, generate $OP\ R_r, R_r, R_r-l$.

Optimal three-register code using only two registers

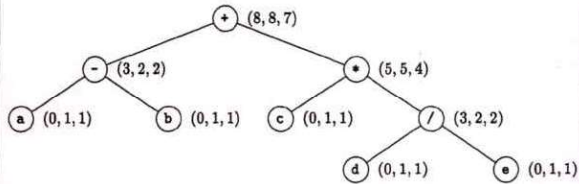
```

LD R2, d
LD R1, c
ADD R2, R1, R2
LD R1, e
MUL R2, R1, R2
ST t3, R2
LD R2, b
LD R1, a
SUB R2, R1, R2
LD R1, t3
ADD R2, R2, R1
  
```

Dynamic Programming Algorithm

- Compute bottom-up for each node n of the expression tree T an array C of costs, in which the i th component $C[i]$ is the optimal cost of computing the subtree S rooted at n into a register, assuming i registers are available for the computation, for
- Traverse T , using the cost vectors to determine which subtrees of T must be computed into memory.
- Traverse each tree using the cost vectors and associated instructions to generate the final target code. The code for the subtrees computed into memory locations is generated first.

Syntax tree for $(a-b)+c*(d/e)$ with cost vector at each node



minimum cost of evaluating the root with two registers available

- Compute the left subtree with two registers available into register R_0 , compute the right subtree with one register available into register R_1 , and use the instruction $\text{ADD } R_0, R_0, R_1$ to compute the root. This sequence has cost $2+5+1=8$.
- Compute the right subtree with two registers available into R_1 , compute the left subtree with one register available into R_0 , and use the instruction $\text{ADD } R_0, R_0, R_1$. This sequence has cost $4+2+1=7$.
- Compute the right subtree into memory location M , compute the left subtree with two registers available into register R_0 , and use the instruction $\text{ADD } R_0, R_0, M$. This sequence has cost $5+2+1=8$.

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Semester End Question Papers

COMPILER DESIGN



Vidya Jyothi Institute of Technology (Autonomous)

(Accredited by NAAC & NBA, Approved By A.I.C.T.E., New Delhi, Permanently Affiliated to JNTU, Hyderabad)

(Aziz Nagar, C.B.Post, Hyderabad -500075)

Subject code: A16523

III B. Tech II SEM REGULAR EXAMINATION - MAY 2018

COMPILER DESIGN

(FOR CSE)

Time: 3hrs

Max.Marks:75

Note: This question paper contains two PARTS A and B.

PART A is compulsory which carries 25 marks. Answer all questions.

PART B consists of 5 Units. Answer all full question.

PART - A

ANSWER ALL THE QUESTIONS

25 M

1. What do you mean by LL(1) grammar? Give example. 3M
2. What are the problems in top down parsing? 2M
3. what are the various conflicts that occur during shift reduce parsing? 2M
4. Draw the model of an LR parser. 3M
5. Generate three address code for the given pseudo code 3M
while($i \leq 100$) { $A = A/B * 20$; $++i$; print(A value) }
6. How to generate polish notation using translation schemes? 2M
7. At what levels code can be optimized by user and compiler? 2M
8. Describe Loop Optimization. 3M
9. Mention the properties that a code generator should possess. 2M
10. Construct a DAG for the expression $a = b * -c + b * -c$. 3M

ANSWER ALL THE QUESTIONS

5X10M=50M

- 11.i) Describe the various phases of a compiler and trace it with the program segment
(position:=initial + rate*60). 10M

OR

- ii) a. What are the preprocessing steps required for predictive parse table construction? 5M
b. Construct the predictive parser for the following grammar. 5M

$S \rightarrow (L)/a$

$L \rightarrow L, S/S$

12. i) a. With neat sketch explain the structure of LR parser and the rules to compute LR item. 5M
b. Consider the following grammar: 5M

$E \rightarrow E + E$

$E \rightarrow E * E$

$E \rightarrow (E)$

$E \rightarrow id$

Show the shift-reduce parser action for the string: $id*(id+id)$.

OR

- ii). a. Differentiate between Top down and Bottom up Parsing methods. 5M
b. Write the steps for the efficient construction of LALR parsing table. 5M

13. i) a. Explain heap storage allocation. With an Example 5M
b. What is an Abstract syntax tree? How to construct it? Explain by writing syntax directed definition. 5M

OR

- ii). a. Can we reuse the symbol table space? Explain through an example. 5M
b. Write short notes on functions of semantic analysis. 5M

14. i) Explain in detail about inter procedural optimization with an example. 10M

OR

- ii) Explain reducible and non-reducible flow graphs with an example. 10M

15. i) Efficient Register allocation and assignment improves the performance of object code-Justify this statement with suitable examples. 10M

OR

- ii). Explain in detail about machine dependent code optimization techniques with their drawbacks. 10M

*** VJTT(A) ***



Vidya Jyothi Institute of Technology(Autonomous)

(Accredited by NAAC & NBA, Approved By A.I.C.T.E., New Delhi, Permanently Affiliated to JNTU, Hyderabad)

(Aziz Nagar, C.B.Post, Hyderabad -500075)

Subject code: A16523

III B. Tech II SEM SUPPLEMENTARY EXAMINATION – DECEMBER 2018

R15

COMPILER DESIGN

(CSE)

Time: 3hrs

Max.Marks:75

Note: This question paper contains two PARTS A and B.

PART A is compulsory which carries 25 marks. Answer all questions.

PART B consists of 5 questions. Answer all the questions.

PART - A

ANSWER ALL THE QUESTIONS

- | | |
|--|------|
| 1. Write the role of preprocessor in language processing. | 25 M |
| 2. What is the role of lexical analyzer? | 2M |
| 3. Discuss the role of Action and Goto functions in LR parser? | 3M |
| 4. Explain the various actions performed by shift-reduce parsers | 3M |
| 5. Write the fields and uses of symbol table. | 2M |
| 6. What is the need for symbol table? | 3M |
| 7. Describe scope of variable? | 2M |
| 8. Give the organization of optimizing compiler. | 2M |
| 9. What are the issues in the design of code generator? | 3M |
| 10. How to allocate registers to instruction? | 3M |
| | 2M |

PART-B

ANSWER ALL THE QUESTIONS

5QX10M=50M

- | | |
|---|-----|
| 11. i) a) What is the role of regular expression in lexical analysis? Explain with examples. | 5M |
| b) Explain bootstrapping a compiler with suitable diagrams. | 5M |
| OR | |
| ii) a) Verify whether the following grammar is LL(1) or not? | 7M |
| $E \rightarrow E + T \mid T$ $T \rightarrow T * F \mid F$ $F \rightarrow (F) \mid a \mid b. $ | |
| b) Why lexical and syntax analyzer are separated out? | 3M |
| 12. i) a) Parse the input string intid,id; using shift reduce parser for the grammar. | 5M |
| $S \rightarrow TL;$ $T \rightarrow \text{int} \mid \text{float}$ $L \rightarrow L, \text{id} \mid \text{id} $ | |
| b) Show that the grammar $S \rightarrow 0S1 \mid SS \mid \epsilon$ is ambiguous. | 5M |
| OR | |
| ii) a) Explain the compaction of LR parsing tables with an example. | 5M |
| b) Explain the process of handling "Dangling-ELSE" ambiguity. | 5M |
| 13. i) a) Translate the assignment $x := a + b * c + d$ into three address statement. | 5M |
| b) Discuss storage allocation for block structured languages. | 5M |
| OR | |
| ii) a) Write down the specification of a simple Type Checker. | 5M |
| b) Explain in detail the symbol table organization for Block-Structured languages. | 5M |
| 14. i) Construct the syntax tree and draw the DAG for the expression. | 10M |
| $(a * b) + (c - d) * (a * b) + b.$ | |
| OR | |
| ii) Explain how loop optimization can be done? How they are different from local optimizations. | 10M |
| 15. i) a) Discuss various object code forms. | 5M |
| b) Write a short note on code generating algorithms. | 5M |
| OR | |
| ii). Explain in detail register allocation and assignment. | 10M |

VJIT(A)

Code No: 136AQ

R16

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B. Tech III Year II Semester Examinations, May - 2019

COMPILER DESIGN

(Common to CSE, IT)

Time: 3 hours

Max. Marks: 75

Note: This question paper contains two parts A and B.
Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions.

PART - A

(25 Marks)

- 1.a) Define regular expression. [2]
- b) Define linker and loader and explain briefly. [3]
- c) Define ambiguous grammar. [2]
- d) Compare SLR, CLR and LACR. [3]
- e) What is coercion? [2]
- f) How to find evaluation order for SDD's? [3]
- g) What are the limitations of static allocation? [2]
- h) Write the fields and uses of symbol table. [3]
- i) What is common sub-expression elimination? Explain. [2]
- j) What are induction variables? What is induction variable elimination? [3]

PART - B

(50 Marks)

- 2.a) Explain the procedure to convert regular expression to Finite automata.
 - b) Explain various phases in the construction of compiler with a neat sketch. [5+5]
- OR**
- 3.a) What is the functionality of preprocessing and input buffering?
 - b) Explain compiler construction tools. [5+5]

- 4.a) What is left recursion? Describe the algorithm used for eliminating left recursion?
- b) Eliminate left recursion in the following: [5+5]
$$E \rightarrow E+T \mid T, T \rightarrow T * F \mid F, F \rightarrow (E) \mid id$$

OR

- 5.a) What is ambiguous grammar? Show that following grammar is ambiguous or not.
$$A \rightarrow A + A \mid A - A \mid A * A \mid a$$
- b) Verify whether the following grammar is LL(1) or not? [5+5]
$$E \rightarrow E+T \mid T$$
$$T \rightarrow T * F \mid F$$
$$F \rightarrow (F) \mid a \mid b.$$

6.a) What are three address codes? Explain different types of representations of three address code.

b) Write three codes for $x := A[y, z]$ [5+5]

OR

7.a) What is type checker? Explain the specification of a simple type checker.

b) Explain translation schema for array elements. [5+5]

8.a) Explain about Heap management.

b) Define reference counting. What is the role of reference counting in garbage collection? [5+5]

OR

9.a) Give the detailed description on DAG.

b) Explain different methods for register allocation and assignment. [5+5]

10.a) Explain redundancy elimination techniques.

b) Write the principal sources of optimization. [5+5]

OR

11.a) Explain loop optimization technique with example.

b) Explain constant propagation with example. [5+5]

---ooOoo---

Code No: 115AP

R13

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B. Tech III Year I Semester Examinations, May/June - 2019

COMPILER DESIGN

(Computer Science and Engineering)

Time: 3 hours

Max. Marks: 75

Note: This question paper contains two parts A and B.
Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions.

PART - A

(25 Marks)

- 1.a) Why lexical and syntax analyzers are separated? [2]
- b) List the various error recovery strategies for a lexical analysis. [3]
- c) Mention the types of LR parser. [2]
- d) Define LR(0) items with examples. [3]
- e) What are the benefits of intermediate code generation? [2]
- f) Explain about hashing. [3]
- g) What is a basic block? [2]
- h) Discuss about common sub expression elimination. [3]
- i) How do you calculate the cost of an instruction? [2]
- j) List out the common issues in the design of code generator. [3]

PART - B

(50 Marks)

2. Explain the various phases of a compiler in detail. Also write down the output for the following expression after each phase a: $=b*cd$. [10]
- OR**
3. What is FIRST and FOLLOW? Explain the steps to compute FIRST and FOLLOW with an example. [10]
- OR**
4. Check whether the following grammar is SLR (1) or not. Explain your answer with Reasons. [10]
 $S \rightarrow L=R \quad S \rightarrow R \quad L \rightarrow *R \quad L \rightarrow id \quad R \rightarrow L$
- OR**
5. Consider the grammar. [10]
 $E \rightarrow E+T \quad E \rightarrow T \quad T \rightarrow T*F \quad T \rightarrow F \quad F \rightarrow (E)/id$
Construct CLR parsing table for the above grammar. Give the moves of the CLR parser on $id * id + id$.
6. What is a three address code? Mention its types. How would you implement the three address statements? Explain with examples. [10]
- OR**
7. Describe in detail the syntax directed translation of case statements. [10]

8. What are steps needed to compute the next use information? [10]
OR
9. Discuss about the following: [10]
a) Copy Propagation
b) Dead code Elimination and
c) Code motion.
10. Write the code generation for the $d := (a-b) + (a-c) + (a-c)$. [10]
OR
11. Write a code generation algorithm. Explain about the descriptor and function `getreg()`. [10]
Give an example.

--ooOoo--



Code No: 115AP

R13

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B. Tech III Year I Semester Examinations, November/December - 2018

COMPILER DESIGN

(Computer Science and Engineering)

Time: 3 hours

Max. Marks: 75

Note: This question paper contains two parts A and B.
Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions.

PART - A

(25 Marks)

- 1.a) Define Boot strapping? [2]
- b) What are the draw backs of predictive parsing? [3]
- c) What are the actions performed by Shift reduce parser? [2]
- d) Differentiate between SLR, LALR and CLR parsers. [3]
- e) What are the applications of DAG? [2]
- f) What are the advantages of stack storage allocation strategy? [3]
- g) Define Basic block. What are the rules for defining a basic block? [2]
- h) What is common sub expression elimination? [3]
- i) Define Dead code elimination? [2]
- j) What is register allocation? Give a brief description. [3]

PART - B

(50 Marks)

- 2.a) Define Regular Expression. Explain about the Properties of Regular Expressions.
- b) Differentiate between Top down and bottom up parsing techniques. [5+5]

OR

- 3.a) Define Compiler? Explain in brief about the types of lexical errors with example.
- b) Construct a Predictive parsing table for the Grammar
 $E \rightarrow E+T/T, T \rightarrow T^*F/F, F \rightarrow (E)/id?$ [4+6]

- 4.a) Construct LALR Parsing table for the grammar
 $S \rightarrow L=R/R$
 $L \rightarrow *R/id$
 $R \rightarrow L$
- b) Define Ambiguous Grammar? Check whether the grammar
 $S \rightarrow aAB,$
 $A \rightarrow bC/cd,$
 $C \rightarrow cd,$
 $B \rightarrow c/d$ Is Ambiguous or not? [6+4]

OR

- 5.a) Construct SLR Parsing table for the grammar
 $S \rightarrow (L)a$
 $L \rightarrow L,s|s$ **WWW.MANARESULTS.CO.IN**
- b) Discuss in brief about model of LR parser. [6+4]

- 6.a) Construct a Quadruple, Triple and Indirect Triple for the statement $a + a * (b - c) + (b - c) * d$?
b) Differentiate between Static and Dynamic Storage allocation Strategies. [5+5]

OR

- 7.a) Construct an annotated parse tree for real id1, id2, id3.
b) Explain in brief about equivalence of type expressions. [5+5]

- 8.a) Explain in brief about different Principal sources of optimization techniques with suitable examples.

- b) Define Flow Graph? Explain how a given program can be converted into flow graph? [5+5]

OR

- 9.a) Explain in brief about function preserving transformations on basic blocks.
b) Explain in brief about optimization of basic blocks. [5+5]

- 10.a) Explain in detail about global common sub expression elimination technique.
b) Explain in brief about Induction variable elimination. [5+5]

OR

- 11.a) Explain in brief about the issues in the design of code generator.
b) Explain in detail about peep hole optimization. [5+5]

---00000---

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Extra Topics Delivered

COMPILER DESIGN

S. No	Name of the Topic
1	LEX program
2	DAG(Directed acyclic graph)

1. LEX program

```
/*lex program to count number of words*/
%{
#include<stdio.h>
#include<string.h>
int i = 0;
%}

/* Rules Section*/
%%
([a-zA-Z0-9])* {i++;} /* Rule for counting
                    number of words*/

"\n" {printf("%d\n", i); i = 0;}
%%

int yywrap(void){}

int main()
{
    // The function that starts the analysis
    yylex();

    return 0;
}
```

Output:

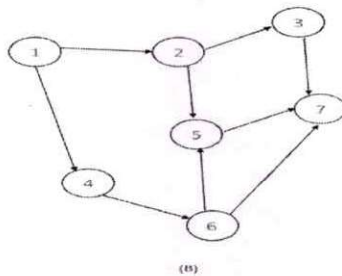
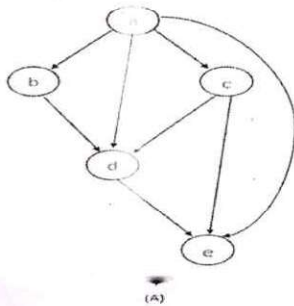
```
shubham@gfg-desktop:~$ lex words.l
shubham@gfg-desktop:~$ cc lex.yy.c -lfl
shubham@gfg-desktop:~$ ./a.out
Hello Everyone
2
This is GeeksforGeeks
3
```

2. Directed Acyclic Graph :

The Directed Acyclic Graph (DAG) is used to represent the structure of basic blocks, to visualize the flow of values between basic blocks, and to provide optimization techniques in the basic block. To apply an optimization technique to a basic block, a DAG is a three-address code that is generated as the result of an intermediate code generation.

- Directed acyclic graphs are a type of data structure and they are used to apply transformations to basic blocks.
- The Directed Acyclic Graph (DAG) facilitates the transformation of basic blocks.
- DAG is an efficient method for identifying common sub-expressions.
- It demonstrates how the statement's computed value is used in subsequent statements.

Examples of directed acyclic graph :



DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Innovation in Teaching and Learning

COMPILER DESIGN



Vidya Jyothi Institute of Technology

(Accredited by NAAC & NBA , Approved By A.I.C.T.E., New Delhi) permanently affiliated JNTUH

(Aziz Nagar, C.B.Post, Hyderabad -500075)

(AUTONOMOUS)

Innovation in Teaching Learning: Role Play

Subject: Compiler Design

Name of the Faculty: Dr.Aruna Kmari

Topic: Phases of compiler

Class/ Section: III B.Tech II-Sem CSE-B

Teaching is an art and science. Teaching is a process of imparting knowledge and skills. It is a systematic process based on some educational objectives to communicate.

Interactive learning is a hands-on, real-world approach to education. 'Interactive learning actively engages the students in wrestling with the material. It reinvigorates the classroom for both students and faculty. Lectures are changed into discussions, and students and teachers become partners in the journey of knowledge acquisition.'

Role-playing is the changing of one's behaviour to assume a role, either unconsciously to fill a social role, or consciously to act out an adopted role.

- To refer to the playing of roles generally such as in a theatre, or educational setting;
- To refer to taking a role of an existing character or person and acting it out with a partner taking someone else's role, often involving different genres of practice

Topic: Phases of compiler

Compiler: A **compiler** is a computer program that transforms computer code written in one programming language (the source language) into another programming language (the target language).

Lexical Analysis. The first phase of scanner works as a text scanner

Syntax Analysis. The next phase is called the syntax analysis or parsing

Semantic Analysis. Semantic analysis checks whether the parse tree constructed follows the rules of language. For example, assignment of values is between compatible data types, and adding string to an integer. ...

Intermediate Code Generation After semantic analysis the compiler generates an intermediate code of the source code for the target machine. It represents a program for some abstract machine.

Code Optimization The next phase does code optimization of the intermediate code.

Code Generation In this phase, the code generator takes the optimized representation of the intermediate code and maps it to the target machine language.

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING




Instructor
Dr.D.Aruna Kumari


CSE-HOD
Head of the Department
Computer Science and Engineering
VJIT, Hyderabad-50075.

COMPILER DESIGN



Vidya Jyothi Institute of Technology

(An Autonomous Institution)

(Accredited by NAAC, Approved by AICTE New Delhi & Permanently Affiliated to JNTU/H)

Aziz Nagar Gate, C.B. Post, Hyderabad-500 075

Department of Computer Science & Engineering

(Accredited by NBA)

Academic Year: 2019-20

BATCH: 2017-21

III B.Tech- II Sem

Course: CD

Branch: CSE

S.No	Reg.No	ASM - I (5)	MID I Threshold 60%						ASM - II (5)	MID II Threshold 60%					End Exam (75M)
			PART-A			PART-B				PART-A	PART-B				
			Q1(2M)(C O1)	Q2(2M)(C O2)	Q3(2M)(C O3)	Q4(5M)(C O1)	Q5(5M)(C O2)	Q6(4M)(C O3)			Q1(6M)(C O3)	Q2(7M)(C O4)	Q3(7M)(C O4)	Q4(7M)(C O5)	
1	17601A0511	5	1	1	1	2	2	1	5	4	7			7	1
2	17601A0575	5	2	2	2	5	5	3	5	5		7		7	41
3	17911A0501	5	0	0	0	0	0	0	5	0	0	0			29
4	17911A0502	5	1	1	1	3	3	2	5	4	7			7	48
5	17911A0503	5	1	1	1	2	2	1	5	4	7			7	54
6	17911A0504	5	2	2	2	5	5	3	5	5		7		7	46
7	17911A0505	5	0	0	0	0	0	0	5	0	0	0			52
8	17911A0506	5	1	1	1	3	3	2	5	4	7			7	45
9	17911A0507	5	1	1	1	2	2	1	5	4	7			7	53
10	17911A0508	5	2	2	2	5	5	3	5	5		7		7	44
11	17911A0509	5	0	0	0	0	0	0	5	0	0	0			52
12	17911A0510	5	1	1	1	3	3	2	5	4	7			7	53
13	17911A0511	5	1	1	1	2	2	1	5	4	7			7	48
14	17911A0512	5	2	2	2	5	5	3	5	5		7		7	49
15	17911A0513	5	0	0	0	0	0	0	5	0	0	0			56
16	17911A0514	5	1	1	1	3	3	2	5	4	7			7	42
17	17911A0515	5	1	1	1	2	2	1	5	4	7			7	40
18	17911A0516	5	2	2	2	5	5	3	5	5		7		7	45
19	17911A0517	5	0	0	0	0	0	0	5	0	0	0			53
20	17911A0518	5	1	1	1	3	3	2	5	4	7			7	37
21	17911A0520	5	1	1	1	2	2	1	5	4	7			7	40
22	17911A0521	5	2	2	2	5	5	3	5	5		7		7	53
23	17911A0523	5	0	0	0	0	0	0	5	0	0	0			39
24	17911A0524	5	1	1	1	3	3	2	5	4	7			7	48
25	17911A0526	5	1	1	1	5	5	1	5	2		6	6		37
26	17911A0527	5	1	1	1	5	5	1	5	1	7			7	44
27	17911A0528	5	2	2	2	5	5	3	5	2		7	7		52
28	17911A0529	5	2	2	2	5	5	2	5	5		7		7	53
29	17911A0530	5	2	2	2	5	5	3	5	5		7		7	53
30	17911A0531	5	2	2	2	5	5	3	5	5		7		7	55
31	17911A0532	5	0	0	0	0	0	0	5	0	0	0			51
32	17911A0533	5	1	1	1	3	3	2	5	4	7			7	33
33	17911A0534	5	0	0	0	0	0	0	5	1		5	5		47
34	17911A0536	5	1	1	2	5	5	2	5	2	5		5		49
35	17911A0537	5	1	1	1	5	5	1	5	2		6	6		47
36	17911A0538	5	1	1	1	5	5	1	5	1	7			7	36
37	17911A0539	5	2	2	2	5	5	3	5	2		7	7		45
38	17911A0540	5	2	2	2	5	5	2	5	5		7		7	43
39	17911A0541	5	2	2	2	5	5	3	5	5		7		7	45
40	17911A0542	5	2	2	2	5	5	3	5	5		7		7	49
41	17911A0543	5	0	0	0	0	0	0	5	0	0	0			46
42	17911A0544	5	1	1	1	3	3	2	5	4	7			7	44
43	17911A0545	5	0	0	0	0	0	0	5	1		5	5		30
44	17911A0546	5	1	1	2	5	5	2	5	2	5		5		49
45	17911A0547	5	1	1	1	5	5	1	5	2		6	6		42
46	17911A0548	5	1	1	1	5	5	1	5	1	7			7	56
47	17911A0549	5	2	2	2	5	5	3	5	2		7	7		47
48	17911A0550	5	2	2	2	5	5	2	5	5		7		7	41
49	17911A0552	5	2	2	2	5	5	3	5	5		7		7	51
50	17911A0553	5	2	2	2	5	5	3	5	5		7		7	50
51	17911A0554	5	0	0	0	0	0	0	5	0	0	0			46
52	17911A0555	5	1	1	1	3	3	2	5	4	7			7	52
53	17911A0556	5	0	0	0	0	0	0	5	1		5	5		52
54	17911A0557	5	1	1	2	5	5	2	5	2	5		5		52
55	17911A0558	5	1	1	1	5	5	1	5	2		6	6		48
56	17911A0559	5	1	1	1	5	5	1	5	1	7			7	53
57	17911A0560	5	2	2	2	5	5	3	5	2		7	7		54
58	17911A0561	5	2	2	2	5	5	2	5	5		7		7	54
59	17911A0562	5	2	2	2	5	5	3	5	5		7		7	51
60	17911A0563	5	2	2	2	5	5	3	5	5		7		7	50
61	17911A0564	5	0	0	0	0	0	0	5	0	0	0			59
62	17911A0565	5	1	1	1	3	3	2	5	4	7			7	31
63	17911A0566	5	0	0	0	0	0	0	5	1		5	5		51
64	17911A0567	5	1	1	2	5	5	2	5	2	5				53
65	17911A0569	5	1	1	1	5	5	1	5	2		6	6		33
66	17911A0570	5	1	1	1	5	5	1	5	1	7			7	40
67	17911A0571	5	2	2	2	5	5	3	5	2		7	7		46
68	17911A0572	5	2	2	2	5	5	2	5	5		7		7	44

69	17911A0573	5	2	2	2	5	5	3	5	5		7		7	36
70	17911A0574	5	2	2	2	5	5	4	5	6		7	7		43
71	17911A0575	5	2	2	2	5	5	3	5	5		7		7	53
72	17911A0576	5	1	1	1	3	3	1	5	2		7	7		34
73	17911A0577	5	2	2	2	5	5	3	5	5		7		7	55
74	17911A0578	5	1	1	2	5	5	2	5	1	7		7		52
75	17911A0579	5	2	2	2	5	5	2	5	6		7	7		61
76	17911A0580	5	1	1	1	5	5	1	5	3		7		7	58
77	17911A0581	5	2	2	2	5	5	4	5	3		7		7	58
78	17911A0582	5	1	1	1	5	5	1	5	4	7			7	62
79	17911A0583	5	1	1	1	3	3	2	5	3		7		7	64
80	17911A0584	5	1	1	1	4	4	2	5	2		7	7		61
81	17911A0585	5	1	1	1	2	2	2	5	3		7		7	26
82	17911A0586	5	1	1	2	5	5	2	5	2	4			4	55
83	17911A0587	5	1	1	2	5	5	2	5	2	5	5			59
84	17911A0589	5	1	1	1	3	3	1	5	5		7		7	67
85	17911A0590	5	2	2	2	5	5	1	5	4	7			7	56
86	17911A0591	5	2	2	2	5	5	1	5	2	5	5			59
87	17911A0594	5	1	1	1	3	3	1	5	2		6	5		31
88	17911A0595	5	2	2	2	5	5	1	5	1		5	5		56
89	17911A0596	5	2	2	2	5	5	1	5	4	7			7	58
90	17911A0597	5	1	1	1	4	4	1	5	2	4			4	11
91	17911A0598	5	1	1	2	5	5	2	5	2	4			4	35
92	17911A0599	5	1	1	1	3	3	2	5	4	7			7	63
93	17911A05A0	5	1	1	1	4	4	2	5	2		7	7		29
94	17911A05A2	5	2	2	2	5	5	2	5	6		7	7		57
95	17911A05A3	5	1	1	2	5	5	2	5	6		7	7		59
96	17911A05A5	5	1	1	1	5	5	1	5	3		7		7	42
97	17911A05A6	5	1	1	1	3	3	2	5	5		7		7	31
98	17911A05A7	5	1	1	1	4	4	2	5	6		7	7		64
99	17911A05A8	5	2	2	2	5	5	1	5	6		7	7		54
100	17911A05A9	5	1	1	1	4	4	1	5	5		7		7	63
101	17911A05B0	5	2	2	2	5	5	2	5	0	0	0			31
102	17911A05B1	5	1	1	1	4	4	2	5	2		6	6		55
103	17911A05B2	5	1	1	1	5	5	1	5	6		7	7		40
104	17911A05B3	5	1	1	1	5	5	2	5	2		6	6		65
105	17911A05B4	5	1	1	1	5	5	1	5	5		7		7	40
106	17911A05B6	5	2	2	2	5	5	1	5	6		7	7		59
107	17911A05B7	5	1	1	1	5	5	2	5	4	7			7	34
108	17911A05B8	5	1	1	2	5	5	2	5	2		6	5		38
109	17911A05B9	5	1	1	1	2	2	2	5	2		6	5		A
110	17911A05C0	5	1	1	1	5	5	1	5	2		6	5		56
111	17911A05C1	5	2	2	2	5	5	2	5	6		7	7		47
112	17911A05C2	5	1	1	1	2	2	2	5	5		7		7	26
113	17911A05C3	5	2	2	2	5	5	1	5	1		5	5		48
114	17911A05C4	5	1	1	2	1	5	2	5	6		7	7		61
115	17911A05C5	5	1	1	1	1	5	2	5	3		7		7	46
116	17911A05C7	5	1	1	1	1	5	1	5	2		6	6		47
117	17911A05C8	5	1	1	1	2	5	1	5	4	7			7	54
118	17911A05C9	5	2	2	2	2	5	1	5	6		7	7		37
119	17911A05D0	5	1	1	2	2	5	2	5	4	7			7	47
120	17911A05D1	5	1	1	2	2	5	2	5	2		2	2		53
121	17911A05D2	5	1	1	2	2	5	2	5	5		7		7	56
122	17911A05D3	5	2	2	2	1	5	1	5	4	7			7	67
123	17911A05D4	5	1	1	1	2	3	2	5	1	7		7		71
124	17911A05D5	5	2	2	2	2	5	4	5	6		7	7		64
125	17911A05D6	5	2	2	2	2	5	2	5	6		7	7		53
126	17911A05D7	5	2	2	2	1	5	4	5	4	7			7	63
127	17911A05D8	5	1	1	1	1	5	2	5	0		0		0	64
128	17911A05D9	5	1	1	1	1	4	2	5	2	5	5			71
129	17911A05E0	5	1	1	1	2	2	2	5	2	5	5			59
130	17911A05E1	5	2	2	2	5	5	1	5	6		7	7		68
131	17911A05E2	5	1	1	1	4	4	2	5	2		7	7		68
132	17911A05E3	5	1	1	1	1	5	2	5	5		7		7	67
133	17911A05E4	5	2	2	2	1	5	1	5	5		7		7	73
134	17911A05E5	5	1	1	2	1	5	2	5	5		7		7	70
135	17911A05E6	5	2	2	2	2	5	1	5	6		7	7		70
136	17911A05E7	5	1	1	1	2	4	2	5	4	7			7	72
137	17911A05E8	5	2	2	2	2	5	1	5	0	0		0		33
138	17911A05E9	5	2	2	2	2	5	4	5	5		7		7	38
139	17911A05F0	5	2	2	2	2	5	1	5	4	7			7	69
140	17911A05F1	5	2	2	2	1	5	3	5	6		7	7		52
141	17911A05F2	5	1	1	2	2	5	2	5	4	7			7	68
142	17911A05F3	5	2	2	2	2	5	1	5	5		7		7	46
143	17911A05F4	5	1	1	1	2	4	1	5	5		7		7	67
144	17911A05F5	5	2	2	2	1	5	2	5	6		7	7		72
145	17911A05F6	5	2	2	2	1	5	3	5	2		6	6		43
146	17911A05F7	5	2	2	2	1	5	3	5	4	7			7	62
147	17911A05F8	5	1	1	1	1	1	0	5	1		4	4		63
148	17911A05F9	5	1	1	1	4	4	1	5	2	5	5			67
149	17911A05G0	5	2	2	2	5	5	3	5	5		7		7	60
150	17911A05G1	5	2	2	2	5	5	4	5	6		7	7		46
151	17911A05G2	5	2	2	2	5	5	4	5	5		7		7	67
152	17911A05G3	5	2	2	2	5	5	1	5	6		7	7		75
153	17911A05G4	5	2	2	2	5	5	3	5	5		7		7	36
154	17911A05G5	5	1	1	1	2	2	1	5	6		7	7		46

155	17911A05G6	5	2	2	2	5	5	4	5	6		7	7		68
156	17911A05G7	5	2	2	2	5	5	3	5	4	7		7		49
157	17911A05G8	5	1	1	2	5	5	2	5	2		6	5		45
158	17911A05G9	5	1	1	1	1	5	1	5	4	7			7	39
159	17911A05H0	5	1	1	1	1	3	2	5	1		5	5		68
160	17911A05H1	5	1	1	1	1	5	2	5	4	7			7	69
161	17911A05H2	5	1	1	1	2	3	2	5	3		7		7	60
162	17911A05H3	5	1	1	1	2	5	1	5	5		7		7	57
163	17911A05H4	5	2	2	2	2	5	3	5	4	7			7	38
164	17911A05H5	5	2	2	2	2	5	4	5	2		6	5		45
165	17911A05H6	5	2	2	2	2	5	2	5	5		7		7	50
166	17911A05H7	5	2	2	2	2	1	5	2	5	6		7	7	63
167	17911A05H8	5	1	1	1	2	5	1	5	1		5	5		45
168	17911A05H9	5	2	2	2	2	5	2	5	4	7			7	47
169	17911A05J1	5	1	1	1	2	4	2	5	2	4			4	74
170	17911A05J2	5	2	2	2	2	1	5	3	5	5		7		55
171	17911A05J3	5	2	2	2	1	5	4	5	3		7		7	65
172	17911A05J4	5	1	1	2	1	5	2	5	2		7	7		75
173	17911A05J5	5	2	2	2	5	5	4	5	4	7			7	74
174	17911A05J6	5	2	2	2	5	5	4	5	5		7		7	65
175	17911A05J7	5	1	1	1	1	1	0	5	2		6	6		69
176	17911A05J8	5	1	1	1	4	4	1	5	2	3		3		61
177	17911A05J9	5	2	2	2	5	5	4	5	4	7			7	67
178	17911A05K0	5	2	2	2	5	5	4	5	5		7		7	63
179	17911A05K1	5	1	1	1	5	5	1	5	4	7			7	75
180	17911A05K2	5	2	2	2	5	5	1	5	6		7	7		64
181	17911A05K3	5	1	1	1	5	5	2	5	6		7		7	39
182	17911A05K4	5	1	1	1	3	3	2	5	6		7	7		61
183	17911A05K5	5	2	2	2	5	5	1	5	6		6	6		70
184	17911A05K6	5	1	1	1	5	5	2	5	6		5	5		40
185	17911A05K7	5	1	1	1	5	5	1	5	6	5	5			50
186	17911A05K8	5	2	2	2	5	5	3	5	6		7	7		73
187	17911A05K9	5	1	1	1	5	5	2	5	6	7			7	47
188	17911A05L0	5	1	1	1	5	5	1	5	6		6	5		57
189	17911A05L1	5	2	2	2	5	5	3	5	6		7		7	67
190	17911A05L2	5	2	2	2	5	5	1	5	6		6	6		57
191	17911A05L3	5	1	1	1	2	2	2	5	6	3		3		54
192	17911A05L4	5	2	2	2	5	5	1	5	6	7			7	68
193	17911A05L5	5	2	2	2	5	5	1	5	6	7			7	73
194	17911A05L6	5	1	1	2	5	5	2	5	6		6	6		53
195	17911A05L7	5	2	2	2	5	5	2	5	6	7			7	54
196	17911A05L8	5	2	2	2	5	5	3	5	6	7			7	53
197	17911A05L9	5	1	1	1	5	5	2	5	6	7			7	46
198	17911A05M0	5	2	2	2	5	5	4	5	6		7	7		48
199	17911A05M1	5	2	2	2	5	5	4	5	6	7			7	29
200	17911A05M2	5	1	1	2	5	5	4	5	6	5	5			44
201	17911A05M3	5	1	1	1	3	3	4	5	6	4			4	60
202	17911A05M4	5	1	1	1	3	3	4	5	6		6	5		49
203	17911A05M5	5	1	1	1	5	5	4	5	6		7	7		38
204	17911A05M6	5	1	1	2	5	5	4	5	6		7	7		49
205	17911A05M7	5	1	1	2	5	5	4	5	6		7	7		53
206	17911A05M8	5	1	1	1	4	4	4	5	6	7			7	41
207	17911A05M9	5	2	2	2	5	5	4	5	6	7			7	46
208	17911A05N0	5	1	1	1	4	4	4	5	6		6	5		44
209	17911A05N1	5	1	1	1	3	3	4	5	6	7			7	55
210	17911A05N2	5	1	1	1	2	2	4	5	6		6	5		44
211	17911A05N3	5	1	1	1	4	4	4	5	6		7	7		36
212	17911A05N5	5	2	2	2	5	5	4	5	6	7			7	44
213	17911A05N6	5	1	1	1	5	5	4	5	6	7			7	53
214	17911A05N7	5	1	1	1	5	5	4	5	6	7			7	43
215	17911A05N8	5	2	2	2	5	5	4	5	6	7			7	49
216	17911A05N9	5	2	2	2	5	5	4	5	6	7			7	34
217	17911A05P0	5	1	1	1	5	5	4	5	6	7			7	41
218	17911A05P2	5	1	1	1	4	4	4	5	6	7			7	44
219	17911A05P3	5	2	2	2	5	5	4	5	6	7			7	42
220	17911A05P4	5	2	2	2	5	5	4	5	6		6	6		32
221	17911A05P5	5	1	1	2	5	5	4	5	6	7			7	31
222	17911A05P6	5	1	1	1	0	0	4	5	6		3	3		41
223	17911A05P7	5	1	1	1	3	3	4	5	6		6	5		48
224	17911A05P8	5	1	1	1	4	4	4	5	6		6	6		47
225	17911A05P9	5	2	2	2	5	5	4	5	6		7		7	55
226	17911A05Q0	5	1	1	1	5	5	4	5	6		2	2		42
227	18915A0501	5	1	1	1	3	3	4	5	6	4			4	40
228	18915A0502	5	2	2	2	5	5	4	5	6	7			7	41
229	18915A0503	5	2	2	2	5	5	4	5	6		7		7	46
230	18915A0504	5	1	1	1	5	5	4	5	6		4	4		A
231	18915A0501	5	2	2	2	5	5	4	5	6		7	7		48
232	18915A0502	5	1	1	1	5	5	4	5	6		7	7		38
233	18915A0503	5	1	1	1	4	4	4	5	6	3		3		32
234	18915A0504	5	1	1	2	5	5	4	5	6		7		7	45
Average marks		5	1.346154	1.346154	1.448718	3.559829	4.183761	2.183761	5	4.025641	5.583333	5.9375	6.010526	6.786325	50.76724
No of students attempted		234	234	234	234	234	234	234	234	234	96	160	95	117	232
%of students scored 60%		100	94.017094	94.017094	94.017094	67.948718	87.179487	67.521368	100	70.08547	87.5	91.25	96.842105	99.145299	81.03
CO ATTAINMENT		3	3.0	3.0	3.0	2.0	3.0	2.0	3	3.0	3.0	3.0	3.0	3.0	3.0

ASSESSMENT OF COs FOR THE COURSE

CO	Method		value		CO Attainment (Internal)		CO Attainment (End Exam)	Overall CO Attainment
CO 1	ASM I		3	2.67	2.89		3.00	2.97
	MID I - PART A - Q1		3.0					
	MID I - PART B - Q4		2.0					
CO 2	ASM I		3	3.00				
	MID I - PART A - Q2		3.0					
	MID I - PART B - Q5		3.0					
CO 3	ASM I		3	2.8				
	ASM II		3.0					
	MID I - PART A - Q3		3.0					
	MID I - PART B - Q6		2.0					
	MID II - PART A - Q1		3.0					
CO 4	ASM II		3	3				
	MID II - PART B - Q2		3.0					
	MID II - PART B - Q3		3.0					
CO 5	ASM II		3	3				
	MID II - PART B - Q4		3.0					
	MID II - PART B - Q5		3.0					

COURSE-COORDINATOR

Ans

HOD-CSE

B.H.

Head of the Department
Computer Science and Engineering
VJIT, Hyderabad-50075.

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Course End Survey Form

COMPILER DESIGN



VIDYA JYOTHI INSTITUTE OF TECHNOLOGY

(Accredited by NBA, Approved by AICTE New Delhi & Permanently Affiliated to JNTUH)
Aziz Nagar Gate, C.B. Post, Hyderabad-500 075.

Department of Computer Science & Engineering **Course End Survey Form Academic year: 2019-20**

Name of the student		Year & sem	III – II
Roll number		Regulations	R 15

Dear Student,

We need your help in evaluating the courses offered, by responding the short survey below.

Your feedback is very valuable for us in order to continually improve our program. The aim of this survey is to evaluate how well each of the courses has prepared you to have necessary skills.

Your responses will be kept confidential and will not be revealed to anyone outside the department without your permission.

Please indicate (✓) the level to which you agree with the following criterion:
(3: Strongly agree 2: Agree 1: Strongly disagree)

Name of The Course: COMPILER DESIGN		RATING		
After completing this course the student must demonstrate the knowledge and ability to		3	2	1
CO 1	Differentiate the phases in compilation & parsing.			
CO 2	Identify the process in parsing and semantic analysis.			
CO 3	Explain about symbol tables and code optimization methods.			
CO 4	Explain about code optimization methods.			
CO 5	Analyze data flow and generate object code.			

Any other comments / suggestions: _____

Signature

Assessment Sheet – Co Wise (Direct Attainment)

Vidya Jyothi Institute of Technology(Autonomous)
Department of Computer Science Engineering
BATCH: 2016-20

Academic Year: 2018-19
III B.Tech - II Sem
Course: CD

Faculty:

S.No	Reg.No	MID I Threshold 60%						MID II Threshold 60%						Threshold 60%		
		ASMI - I (5)	PART-A			PART-B			ASMI - II (5)	PART-A			PART-B			
			Q1(2M)	Q2(2M)	Q3 B (2M)	Q4(5M)	Q5(5M)	Q6(4M)		Q1(2M)	Q2(2M)	Q3 A (2M)	Q4(4M)	Q5(5M)	Q6(5M)	End Exam (75M)
1	15911A0521	5	2	2	2	4	4	4	5	2	2	2	4	4	4	51
2	15911A0548	5	2	2	2	2	2	3	5	2	2	2	3	2	2	48
3	15911A0599	5	2	2	2	2	2	2	5	2	2	2	2	2	2	53
4	15911A0589	5	2	2	2	2	2	2	5	2	2	2	2	2	2	8
5	15911A0509	5	2	2	2	2	2	4	5	2	2	2	4	2	4	15
6	15911A0514	5	2	2	2	2	3	3	5	2	2	2	3	3	3	60
7	15911A0516	5	2	2	2	3	3	2	5	2	2	2	2	3	3	52
8	15911A05P9	5	2	2	2	4	4	4	5	2	2	2	4	4	4	48
9	16911A0501	5	2	2	2	5	5	4	5	2	2	2	4	5	5	59
10	16911A0502	5	2	2	2	3	3	3	5	2	2	2	1	3	3	57
11	16911A0503	5	2	2	2	1	3	3	5	2	2	2	3	1	3	45
12	16911A0505	5	2	2	2	3	3	4	5	2	2	2	4	3	3	48
13	16911A0506	5	2	2	2	4	2	4	5	2	2	2	4	4	5	54
14	16911A0507	5	2	2	2	5	3	4	5	2	2	2	4	5	5	61
15	16911A0508	5	2	2	2	3	3	3	5	2	2	2	1	3	3	58
16	16911A0509	5	2	2	2	1	3	3	5	2	2	2	3	1	3	61
17	16911A0510	5	2	2	2	3	3	4	5	2	2	2	4	3	3	60
18	16911A0511	5	2	2	2	4	5	4	5	2	2	2	4	4	5	48
19	16911A0512	5	2	2	2	3	3	3	5	2	2	2	3	3	3	54
20	16911A0513	5	2	2	2	3	3	2	5	2	2	2	2	3	3	47
21	16911A0514	5	2	2	2	4	4	4	5	2	2	2	4	4	4	51
22	16911A0515	5	2	2	2	3	3	3	5	2	2	2	3	3	3	49
23	16911A0516	5	2	2	2	4	4	4	5	2	2	2	4	4	4	51
24	16911A0517	5	2	2	2	2	2	3	5	2	2	2	3	2	2	47
25	16911A0518	5	2	2	2	2	2	2	5	2	2	2	2	2	2	58
26	16911A0519	5	2	2	2	2	2	2	5	2	2	2	2	2	2	55
27	16911A0520	5	2	2	2	2	4	4	5	2	2	2	4	2	4	45
28	16911A0521	5	2	2	2	3	3	3	5	2	2	2	3	3	3	60
29	16911A0522	5	2	2	2	3	3	2	5	2	2	2	2	3	3	49
30	16911A0523	5	2	2	2	4	4	4	5	2	2	2	4	4	4	53
31	16911A0524	5	2	2	2	5	5	4	5	2	2	2	4	5	5	55
32	16911A0525	5	2	2	2	3	3	3	5	2	2	2	1	3	3	48
33	16911A0526	5	2	2	2	1	3	3	5	2	2	2	3	1	3	54
34	16911A0527	5	2	2	2	3	3	4	5	2	2	2	4	3	3	17
35	16911A0528	5	2	2	2	4	2	4	5	2	2	2	4	4	5	49

36	16911A0529	5	2	2	2	2	2	2	2	5	3	4	5	2	2	2	2	2	4	5	5	60
37	16911A0530	5	2	2	2	2	2	2	2	3	3	3	5	2	2	2	2	2	1	3	3	58
38	16911A0532	5	2	2	2	2	2	2	2	1	3	3	5	2	2	2	2	3	1	3	56	
39	16911A0533	5	2	2	2	2	2	2	2	3	3	4	5	2	2	2	2	4	3	3	48	
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41	16911A0535	5	2	2	2	2	2	2	2	3	3	3	5	2	2	2	2	3	3	3	49	
42	16911A0536	5	2	2	2	2	2	2	2	3	3	2	5	2	2	2	2	2	3	3	48	
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44	16911A0538	5	2	2	2	2	2	2	2	3	3	3	5	2	2	2	2	3	3	3	54	
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46	16911A0541	5	2	2	2	2	2	2	2	2	2	3	5	2	2	2	2	3	2	2	56	
47	16911A0542	5	2	2	2	2	2	2	2	2	2	2	5	2	2	2	2	2	2	2	52	
48	16911A0543	5	2	2	2	2	2	2	2	2	2	2	5	2	2	2	2	2	2	2	48	
49	16911A0544	5	2	2	2	2	2	2	2	2	4	4	5	2	2	2	2	4	2	4	52	
50	16911A0545	5	1	1	1	1	3	3	3	3	3	3	5	1	1	1	1	3	3	3	54	
51	16911A0546	5	2	2	2	2	2	2	2	3	3	2	5	2	2	2	2	2	3	3	59	
52	16911A0548	5	0	0	0	0	0	0	0	4	4	4	5	0	0	0	0	4	4	4	45	
53	16911A0549	5	0	0	0	0	0	0	0	5	5	4	5	0	0	0	0	4	5	5	60	
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55	16911A0551	5	0	0	0	0	0	0	0	1	3	3	5	0	0	0	0	3	1	3	40	
56	16911A0552	5	0	0	0	0	0	0	0	3	3	4	5	0	0	0	0	4	3	3	42	
57	16911A0553	5	0	0	0	0	0	0	0	4	4	2	5	0	0	0	0	4	4	5	34	
58	16911A0554	5	2	2	2	2	2	2	2	5	3	4	5	2	2	2	2	4	5	5	28	
59	16911A0555	5	1	1	1	1	3	3	3	3	3	3	5	1	1	1	1	1	3	3	37	
60	16911A0556	5	1	1	1	1	1	1	1	1	3	3	5	1	1	1	1	3	1	3	50	
61	16911A0558	5	1	1	1	1	3	3	3	4	4	4	5	1	1	1	1	4	3	3	47	
62	16911A0559	5	0	0	0	0	0	0	0	4	5	4	5	0	0	0	0	4	4	5	45	
63	16911A0560	5	0	0	0	0	0	0	0	3	3	3	5	0	0	0	0	3	3	3	29	
64	16911A0561	5	0	0	0	0	0	0	0	3	3	2	5	0	0	0	0	2	3	3	32	
65	16911A0562	5	2	2	2	2	2	2	2	4	4	4	5	2	2	2	2	4	4	4	45	
66	16911A0563	5	2	2	2	2	2	2	2	3	3	3	5	2	2	2	2	3	3	3	30	
67	16911A0564	5	2	2	2	2	2	2	2	4	4	4	5	2	2	2	2	4	4	4	45	
68	16911A0565	5	0	0	0	0	0	0	0	2	2	3	5	0	0	0	0	3	2	2	28	
69	16911A0566	5	0	0	0	0	0	0	0	2	2	2	5	0	0	0	0	2	2	2	47	
70	16911A0567	5	2	2	2	2	2	2	2	2	2	2	5	2	2	2	2	2	2	2	33	
71	16911A0568	5	2	2	2	2	2	2	2	2	4	4	5	2	2	2	2	4	2	4	50	
72	16911A0569	5	2	2	2	2	2	2	2	3	3	3	5	2	2	2	2	3	3	3	29	
73	16911A0570	5	2	2	2	2	2	2	2	3	3	2	5	2	2	2	2	2	3	3	45	
74	16911A0572	5	2	2	2	2	2	2	2	4	4	4	5	2	2	2	2	4	4	4	58	
75	16911A0573	5	2	2	2	2	2	2	2	5	5	4	5	2	2	2	2	4	5	5	32	
76	16911A0575	5	2	2	2	2	2	2	2	3	3	3	5	2	2	2	2	1	3	3	15	
77	16911A0576	5	2	2	2	2	2	2	2	1	3	3	5	2	2	2	2	3	1	3	47	
78	16911A0577	5	2	2	2	2	2	2	2	3	3	4	5	2	2	2	2	4	3	3	55	
79	16911A0579	5	2	2	2	2	2	2	2	4	2	4	5	2	2	2	2	4	4	5	42	
80	16911A0580	5	2	2	2	2	2	2	2	5	3	4	5	2	2	2	2	4	5	5	28	
81	16911A0581	5	2	2	2	2	2	2	2	3	3	3	5	2	2	2	2	1	3	3	57	
82	16911A0582	5	2	2	2	2	2	2	2	1	3	3	5	2	2	2	2	3	1	3	12	

[illegible]

130	16911A05D3	5	0	0	0	0	0	3	3	2	5	0	0	0	0	0	0	2	3	3	39
131	16911A05D4	5	0	0	0	0	0	4	4	4	5	0	0	0	0	0	0	4	4	4	48
132	16911A05D5	5	0	0	0	0	0	3	3	3	5	0	0	0	0	0	0	3	3	3	49
133	16911A05D6	5	2	2	2	2	2	4	4	4	5	2	2	2	2	2	4	4	4	4	27
134	16911A05D7	5	2	2	2	2	2	2	2	3	5	2	2	2	2	2	3	2	2	2	33
135	16911A05D8	5	2	2	2	2	2	2	2	2	5	2	2	2	2	2	2	2	2	2	51
136	16911A05D9	5	1	1	1	1	1	2	2	2	5	1	1	1	1	1	2	2	2	2	44
137	16911A05E0	5	1	1	1	1	1	2	4	4	5	1	1	1	1	1	4	2	4	4	42
138	16911A05E1	5	1	1	1	1	1	3	3	3	5	1	1	1	1	1	3	3	3	3	31
139	16911A05E2	5	2	2	2	2	2	3	3	2	5	2	2	2	2	2	2	3	3	3	3
140	16911A05E3	5	2	2	2	2	2	4	4	4	5	2	2	2	2	2	4	4	4	4	41
141	16911A05E4	5	2	2	2	2	2	5	5	4	5	2	2	2	2	2	4	5	5	5	14
142	16911A05E5	5	0	0	0	0	0	3	3	3	5	0	0	0	0	0	1	3	3	3	33
143	16911A05E6	5	0	0	0	0	0	1	3	3	5	0	0	0	0	0	3	1	3	A	
144	16911A05E7	5	0	0	0	0	0	3	3	4	5	0	0	0	0	0	4	3	3	3	33
145	16911A05E8	5	0	0	0	0	0	4	2	4	5	0	0	0	0	0	4	4	5	5	51
146	16911A05E9	5	0	0	0	0	0	5	3	4	5	0	0	0	0	0	4	5	5	5	60
147	16911A05F0	5	0	0	0	0	0	3	3	3	5	0	0	0	0	0	1	3	3	3	48
148	16911A05F1	5	2	2	2	2	2	1	3	3	5	2	2	2	2	2	3	1	3	3	51
149	16911A05F2	5	1	1	1	1	1	3	3	4	5	1	1	1	1	1	4	3	3	3	59
150	16911A05F3	5	1	1	1	1	1	4	5	4	5	1	1	1	1	1	4	4	5	5	65
151	16911A05F4	5	1	1	1	1	1	3	3	3	5	1	1	1	1	1	3	3	3	3	48
152	16911A05F6	5	1	1	1	1	1	3	3	2	5	1	1	1	1	1	2	3	3	3	58
153	16911A05F7	5	1	1	1	1	1	4	4	4	5	1	1	1	1	1	4	4	4	4	52
154	16911A05F8	5	2	2	2	2	2	3	3	3	5	2	2	2	2	2	3	3	3	3	55
155	16911A05F9	5	1	1	1	1	1	4	4	4	5	1	1	1	1	1	4	4	4	4	52
156	16911A05G0	5	1	1	1	1	1	2	2	3	5	1	1	1	1	1	3	2	2	2	60
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158	16911A05G2	5	1	1	1	1	1	2	2	2	5	1	1	1	1	1	2	2	2	2	62
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160	16911A05G4	5	0	0	0	0	0	3	3	3	5	0	0	0	0	0	3	3	3	3	56
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162	16911A05G6	5	0	0	0	0	0	4	4	4	5	0	0	0	0	0	4	4	4	4	54
163	16911A05G7	5	0	0	0	0	0	5	5	4	5	0	0	0	0	0	4	5	5	5	49
164	16911A05G8	5	2	2	2	2	2	3	3	3	5	2	2	2	2	2	1	3	3	3	53
165	16911A05G9	5	2	2	2	2	2	1	3	3	5	2	2	2	2	2	3	1	3	3	45
166	16911A05H0	5	2	2	2	2	2	3	3	3	4	5	2	2	2	2	4	3	3	3	59
167	16911A05H1	5	0	0	0	0	0	4	2	4	5	0	0	0	0	0	4	4	4	5	61
168	16911A05H2	5	0	0	0	0	0	5	3	4	5	0	0	0	0	0	4	5	5	A	
169	16911A05H3	5	0	0	0	0	0	3	3	3	5	0	0	0	0	0	1	3	3	3	48
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171	16911A05H6	5	0	0	0	0	0	3	3	4	5	0	0	0	0	0	4	3	3	3	50
172	16911A05H7	5	2	2	2	2	2	4	5	4	5	2	2	2	2	2	4	4	4	5	48
173	16911A05H8	5	2	2	2	2	2	3	3	3	5	2	2	2	2	2	3	3	3	3	51
174	16911A05J0	5	2	2	2	2	2	3	3	2	5	2	2	2	2	2	2	3	3	3	48
175	16911A05J1	5	2	2	2	2	2	4	4	4	5	2	2	2	2	2	4	4	4	4	58
176	16911A05J2	5	2	2	2	2	2	3	3	3	5	2	2	2	2	2	3	3	3	3	57

177	16911A05J3	5	2	2	2	2	4	4	4	5	2	2	2	2	2	2	4	4	4	4	60
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179	16911A05J5	5	2	2	2	2	2	2	2	2	5	2	2	2	2	2	2	2	2	2	46
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181	16911A05J7	5	2	2	2	2	2	2	4	4	5	2	2	2	2	2	4	2	4	4	51
182	16911A05J8	5	2	2	2	2	3	3	3	3	5	2	2	2	2	2	3	3	3	3	49
183	16911A05J9	5	2	2	2	2	3	3	3	2	5	2	2	2	2	2	2	3	3	2	2
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185	16911A05K2	5	2	2	2	2	5	5	5	4	5	2	2	2	2	2	4	5	5	5	51
186	16911A05K3	5	2	2	2	2	3	3	3	3	5	2	2	2	2	2	1	3	3	3	49
187	16911A05K4	5	2	2	2	2	1	3	3	3	5	2	2	2	2	2	3	1	3	3	58
188	16911A05K5	5	2	2	2	2	3	3	3	4	5	2	2	2	2	2	4	3	3	3	56
189	16911A05K6	5	2	2	2	2	4	2	4	4	5	2	2	2	2	2	4	4	5	5	49
190	16911A05K7	5	2	2	2	2	5	3	4	4	5	2	2	2	2	2	4	5	5	5	66
191	16911A05K8	5	2	2	2	2	3	3	3	3	5	2	2	2	2	2	1	3	3	3	48
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193	16911A05L1	5	2	2	2	2	3	3	3	4	5	2	2	2	2	2	4	3	3	3	54
194	16911A05L2	5	2	2	2	2	4	5	4	5	5	2	2	2	2	2	4	4	5	5	48
195	16911A05L3	5	2	2	2	2	3	3	3	3	5	2	2	2	2	2	3	3	3	3	61
196	16911A05L4	5	2	2	2	2	3	3	3	2	5	2	2	2	2	2	2	3	3	3	54
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198	16911A05L6	5	2	2	2	2	3	3	3	3	5	2	2	2	2	2	3	3	3	3	49
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201	16911A05L9	5	2	2	2	2	2	2	2	2	5	2	2	2	2	2	2	2	2	2	58
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203	16911A05M1	5	2	2	2	2	2	2	4	4	5	2	2	2	2	2	4	2	4	2	46
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205	16911A05M3	5	2	2	2	2	3	3	3	2	5	2	2	2	2	2	2	3	3	3	51
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207	16911A05M6	5	2	2	2	2	5	5	4	5	5	2	2	2	2	2	4	5	5	5	58
208	16911A05M7	5	1	1	1	1	3	3	3	3	5	1	1	1	1	1	1	3	3	3	9
209	16911A05M8	5	2	2	2	2	1	3	3	3	5	2	2	2	2	2	3	1	3	3	54
210	16911A05M9	5	0	0	0	0	3	3	3	4	5	0	0	0	0	0	4	3	3	3	49
211	16911A05N0	5	0	0	0	0	4	2	4	4	5	0	0	0	0	0	4	4	5	5	45
212	16911A05N1	5	0	0	0	0	5	3	4	4	5	0	0	0	0	0	4	5	5	5	52
213	16911A05N2	5	0	0	0	0	3	3	3	3	5	0	0	0	0	0	1	3	3	3	49
214	16911A05N3	5	0	0	0	0	1	3	3	3	5	0	0	0	0	0	3	1	3	3	53
215	16911A05N4	5	0	0	0	0	3	3	3	4	5	0	0	0	0	0	4	3	3	3	55
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217	16911A05N6	5	1	1	1	1	3	3	3	3	5	1	1	1	1	1	3	3	3	3	60
218	16911A05N7	5	1	1	1	1	3	3	3	2	5	1	1	1	1	1	2	3	3	3	45
219	16911A05N8	5	1	1	1	1	4	4	4	4	5	1	1	1	1	1	4	4	4	4	61
220	16911A05N9	5	1	1	1	1	3	3	3	3	5	1	1	1	1	1	3	3	3	3	5
221	16911A05P0	5	1	1	1	1	5	3	4	4	5	1	1	1	1	1	4	5	5	5	54
222	16911A05P1	5	2	2	2	2	3	3	3	3	5	2	2	2	2	2	1	3	3	3	46
223	16911A05P2	5	2	2	2	2	1	3	3	3	5	2	2	2	2	2	3	1	3	3	48

224	16911A05P3	5	2	2	2	2	3	3	4	5	2	2	2	2	2	4	3	3	62
225	16911A05P4	5	0	0	0	0	4	5	4	5	0	0	0	0	0	4	4	5	16
226	16911A05P5	5	0	0	0	0	3	3	3	3	5	0	0	0	0	3	3	3	45
227	16911A05P6	5	0	0	0	0	3	3	2	5	0	0	0	0	0	3	3	3	60
228	16911A05P7	5	2	2	2	2	4	4	4	5	2	2	2	2	4	4	4	4	52
229	16911A05P8	5	2	2	2	2	3	3	3	3	5	2	2	2	3	3	3	3	15
230	16911A05P9	5	2	2	2	2	3	3	3	3	5	2	2	2	3	3	3	3	48
231	16911A05Q0	5	2	2	2	2	3	3	3	3	5	2	2	2	3	3	3	3	65
232	17915A0501	5	2	2	2	2	3	3	3	3	5	2	2	2	3	3	3	3	60
233	17915A0502	5	2	2	2	2	3	3	3	3	5	2	2	2	3	3	3	3	18
234	17915A0503	5	2	2	2	2	3	3	3	3	5	2	2	2	3	3	3	3	48
235	17918A0501	5	2	2	2	2	3	3	3	3	5	2	2	2	3	3	3	3	59
Average marks		5	1.5	1.5	1.5	1.5	3.1	3.2	3.3	3.3	5	1.5	1.5	1.5	3.1	3.1	3.4	3.4	44.7
No of students attempted		235	235	235	235	235	235	235	235	235	235	235	235	235	235	235	235	235	233
No of students scored		235	156.00	156.00	156.00	156.00	174.00	195.00	194.00	194.00	235	156.00	156.00	156.00	173.00	174.00	205.00	205.00	155.00
% of students scored		100	66.38	66.38	66.38	66.38	74.04	82.98	82.55	82.55	100	66.38	66.38	66.38	73.62	74.04	87.23	87.23	66.52
CO ATTAINMENT		3	2.0	2.0	2.0	2.0	3.0	3.0	3.0	3.0	3	2.0	2.0	2.0	3.0	3.0	3.0	3.0	2.0

ASSESSMENT OF COs FOR THE COURSE						
CO	Method	value	Avg	CO Attainment	CO Attainment (End	Overall CO Attainment
CO 1	ASM I	3	2.7			
	MID I - PART A - Q1	2.0				
	MID I - PART B - Q4	3.0				
	ASM I	3				
CO 2	MID I - PART A - Q2	2.0	2.7			
	MID I - PART B - Q5	3.0				
	ASM I	3				
	ASM II	3				
CO 3	MID I - PART A - Q3	2.0	2.7	2.67	2.00	2.17
	MID I - PART B - Q6	3.0				
	MID II - PART A - Q1	2.0				
	MID II - PART B - Q4	3.0				
	ASM II	3				
	MID II - PART A - Q2	2.0				
CO 4	MID II - PART B - Q5	3.0	2.7			
	ASM II	3				
	MID II - PART A - Q3	2.0				
CO 5	MID II - PART B - Q6	3.0	2.7			



Vidya Jyothi Institute of Technology

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Aziz Nagar Gate, C.B. Post, Hyderabad-500 075

Department of Computer Science & Engineering
(Accredited by NBA)

BATCH: 2017-21

Academic Year: 2019-20
III B. Tech- II Sem
Course: CD

Branch: CSE

S.No	Reg. No	MID I Threshold 60%										MID II Threshold 60%				Threshold 60%
		PART-A					PART-B					ASM - II (5)	PART-A			Threshold 60%
		Q1(2M)(C O1)	Q2(2M)(C O2)	Q3(2M)(C O3)	Q4(5M)(C O1)	Q5(5M)(C O2)	Q6(4M)(C O3)	Q1(6M)(C O3)	Q2(7M)(C O4)	Q3(7M)(C O4)	Q4(7M)(C O5)	Q5(7M)(C O5)				
1	14911A0516	5	1	1	2	2	1	5	4	7		7	7		1	
2	16911A0531	5	2	2	5	5	3	5	5		7	7	7		41	
3	16911A0547	5	0	0	0	0	0	5	0	0	0				29	
4	16911A0557	5	1	1	3	3	2	5	4	7		7	7		48	
5	16911A0515	5	1	1	2	2	1	5	4	7		7	7		54	
6	16911A0519	5	2	2	5	5	3	5	5	7		7	7		46	
7	16911A05K9	5	0	0	0	0	0	5	0	0	0				52	
8	16911A05M5	5	1	1	3	3	2	5	4	7		7	7		45	
9	17601A0511	5	1	1	2	2	1	5	4	7		7	7		53	
10	17601A0575	5	2	2	5	5	3	5	5	7		7	7		44	
11	17911A0501	5	0	0	0	0	0	5	0	0	0				52	
12	17911A0502	5	1	1	3	3	2	5	4	7		7	7		53	
13	17911A0503	5	1	1	2	2	1	5	4	7		7	7		48	
14	17911A0504	5	2	2	5	5	3	5	5	7		7	7		49	
15	17911A0505	5	0	0	0	0	0	5	0	0	0				56	
16	17911A0506	5	1	1	3	3	2	5	4	7		7	7		42	
17	17911A0507	5	1	1	2	2	1	5	4	7		7	7		40	
18	17911A0508	5	2	2	5	5	3	5	5	7		7	7		45	
19	17911A0509	5	0	0	0	0	0	5	0	0	0				53	
20	17911A0510	5	1	1	3	3	2	5	4	7		7	7		37	
21	17911A0511	5	1	1	2	2	1	5	4	7		7	7		40	
22	17911A0512	5	2	2	5	5	3	5	5	7		7	7		53	
23	17911A0513	5	0	0	0	0	0	5	0	0	0				39	
24	17911A0514	5	1	1	3	3	2	5	4	7		7	7		48	
25	17911A0515	5	1	1	5	5	1	5	2	6	6				37	
26	17911A0516	5	1	1	5	5	1	5	1	7	7				44	
27	17911A0517	5	2	2	5	5	3	5	2	7	7				52	
28	17911A0518	5	2	2	5	5	2	5	5	7		7	7		53	
29	17911A0520	5	2	2	5	5	3	5	5	7		7	7		53	
30	17911A0521	5	2	2	5	5	3	5	5	7		7	7		55	

[illegible]

78	17911A0574	5	1	1	1	1	1	5	5	5	1	5	4	7		7	62
79	17911A0575	5	1	1	1	1	1	3	3	3	2	5	3			7	64
80	17911A0576	5	1	1	1	1	1	4	4	4	2	5	2		7	61	
81	17911A0577	5	1	1	1	1	1	2	2	2	2	5	3		7	26	
82	17911A0578	5	1	1	1	1	2	5	5	5	2	5	2	4	4	55	
83	17911A0579	5	1	1	1	2	2	5	5	5	2	5	2	5		59	
84	17911A0580	5	1	1	1	1	1	3	3	3	1	5	5		7	67	
85	17911A0581	5	2	2	2	2	2	5	5	5	1	5	4	7	7	56	
86	17911A0582	5	2	2	2	2	2	5	5	5	1	5	2	5		59	
87	17911A0583	5	1	1	1	1	1	3	3	3	1	5	2		5	31	
88	17911A0584	5	2	2	2	2	2	5	5	5	1	5	1		5	56	
89	17911A0585	5	2	2	2	2	2	5	5	5	1	5	4	7	7	58	
90	17911A0586	5	1	1	1	1	1	4	4	4	1	5	2	4	4	11	
91	17911A0587	5	1	1	1	1	2	5	5	5	2	5	2	4	4	35	
92	17911A0589	5	1	1	1	1	1	3	3	3	2	5	4	7	7	63	
93	17911A0590	5	1	1	1	1	1	4	4	4	2	5	2		7	29	
94	17911A0591	5	2	2	2	2	2	5	5	5	2	5	6		7	57	
95	17911A0594	5	1	1	1	1	2	5	5	5	2	5	6		7	59	
96	17911A0595	5	1	1	1	1	1	5	5	5	1	5	3		7	42	
97	17911A0596	5	1	1	1	1	1	3	3	3	2	5	5		7	31	
98	17911A0597	5	1	1	1	1	1	4	4	4	2	5	6		7	64	
99	17911A0598	5	2	2	2	2	2	5	5	5	1	5	6		7	54	
100	17911A0599	5	1	1	1	1	1	4	4	4	1	5	5		7	63	
101	17911A05A0	5	2	2	2	2	2	5	5	5	2	5	0	0		31	
102	17911A05A2	5	1	1	1	1	1	4	4	4	2	5	2		6	55	
103	17911A05A3	5	1	1	1	1	1	5	5	5	1	5	6		7	40	
104	17911A05A5	5	1	1	1	1	1	5	5	5	2	5	2		6	65	
105	17911A05A6	5	1	1	1	1	1	5	5	5	1	5	5		7	40	
106	17911A05A7	5	2	2	2	2	2	5	5	5	1	5	6		7	59	
107	17911A05A8	5	1	1	1	1	1	5	5	5	2	5	4	7	7	34	
108	17911A05A9	5	1	1	1	1	2	5	5	5	2	5	2		6	38	
109	17911A05B0	5	1	1	1	1	1	2	2	2	2	5	2		6	A	
110	17911A05B1	5	1	1	1	1	1	5	5	5	1	5	2		6	56	
111	17911A05B2	5	2	2	2	2	2	5	5	5	2	5	6		7	47	
112	17911A05B3	5	1	1	1	1	1	2	2	2	2	5	5		7	26	
113	17911A05B4	5	2	2	2	2	2	5	5	5	1	5	1		5	48	
114	17911A05B6	5	1	1	1	1	2	1	1	1	2	5	6		7	61	
115	17911A05B7	5	1	1	1	1	1	1	1	1	2	5	3		7	46	
116	17911A05B8	5	1	1	1	1	1	1	1	1	1	5	2		6	47	
117	17911A05B9	5	1	1	1	1	1	2	2	2	1	5	4	7	7	54	
118	17911A05C0	5	2	2	2	2	2	2	2	2	1	5	6		7	37	
119	17911A05C1	5	1	1	1	1	2	2	2	2	2	5	4	7	7	47	
120	17911A05C2	5	1	1	1	1	2	2	2	2	2	5	2		2	53	
121	17911A05C3	5	1	1	1	1	2	2	2	2	2	5	5		7	56	
122	17911A05C4	5	2	2	2	2	2	1	1	1	1	5	4	7	7	67	
123	17911A05C5	5	1	1	1	1	1	2	2	2	2	5	1		7	71	
124	17911A05C7	5	2	2	2	2	2	2	2	2	4	5	6		7	64	

125	17911A05C8	5	2	2	2	2	2	2	5	2	5	2	5	6	7	7	53
126	17911A05C9	5	2	2	2	2	2	2	5	1	5	4	5	4	7	7	63
127	17911A05D0	5	1	1	1	1	1	1	5	1	5	2	5	0	0	0	64
128	17911A05D1	5	1	1	1	1	1	1	4	1	4	2	5	2	5		71
129	17911A05D2	5	1	1	1	1	1	1	2	2	5	2	5	2	5		59
130	17911A05D3	5	2	2	2	2	2	2	5	5	5	1	5	6	7	7	68
131	17911A05D4	5	1	1	1	1	1	4	4	1	5	2	5	2	7	7	68
132	17911A05D5	5	1	1	1	1	1	5	5	1	5	2	5	5	7	7	67
133	17911A05D6	5	2	2	2	2	2	5	5	1	5	1	5	5	7	7	73
134	17911A05D7	5	1	1	1	1	2	5	5	2	5	2	5	5	7	7	70
135	17911A05D8	5	2	2	2	2	2	5	5	1	5	1	5	6	7	7	70
136	17911A05D9	5	1	1	1	1	1	4	4	2	5	2	5	4	7	7	72
137	17911A05E0	5	2	2	2	2	2	5	5	1	5	1	5	0	0		33
138	17911A05E1	5	2	2	2	2	2	5	5	4	5	4	5	5	7	7	38
139	17911A05E2	5	2	2	2	2	2	5	5	1	5	1	5	4	7	7	69
140	17911A05E3	5	2	2	2	2	2	1	5	3	5	3	5	6	7	7	52
141	17911A05E4	5	1	1	1	1	2	5	5	2	5	2	5	4	7	7	68
142	17911A05E5	5	2	2	2	2	2	5	5	1	5	1	5	5		7	46
143	17911A05E6	5	1	1	1	1	2	4	5	1	5	1	5	5		7	67
144	17911A05E7	5	2	2	2	2	2	5	5	2	5	2	5	6	7	7	72
145	17911A05E8	5	2	2	2	2	2	1	5	3	5	3	5	2	6	6	43
146	17911A05E9	5	2	2	2	2	2	1	5	3	5	3	5	4	7	7	62
147	17911A05F0	5	1	1	1	1	1	1	1	0	5	1	5	1	4	4	63
148	17911A05F1	5	1	1	1	1	1	4	4	1	5	1	5	2	5		67
149	17911A05F2	5	2	2	2	2	2	5	5	3	5	3	5	5	7	7	60
150	17911A05F3	5	2	2	2	2	2	5	5	4	5	4	5	6	7	7	46
151	17911A05F4	5	2	2	2	2	2	5	5	4	5	4	5	5	7	7	67
152	17911A05F5	5	2	2	2	2	2	5	5	1	5	1	5	6	7	7	75
153	17911A05F6	5	2	2	2	2	2	5	5	3	5	3	5	5	7	7	36
154	17911A05F7	5	1	1	1	1	1	2	2	1	5	1	5	6	7	7	46
155	17911A05F8	5	2	2	2	2	2	5	5	4	5	4	5	6	7	7	68
156	17911A05F9																
157	17911A05G0																
158	17911A05G1																
159	17911A05G2																
160	17911A05G3																
161	17911A05G4																
162	17911A05G5																
163	17911A05G6																
164	17911A05G7																
165	17911A05G8																
166	17911A05G9	5	2	2	2	2	2	5	5	3	5	3	5	4	7	7	49
167	17911A05H0	5	1	1	1	1	2	5	5	2	5	2	5	2	6	5	45
168	17911A05H1	5	1	1	1	1	1	1	5	1	5	1	5	4	7	7	39
169	17911A05H2	5	1	1	1	1	1	1	3	2	5	2	5	1	5	5	68
170	17911A05H3	5	1	1	1	1	1	1	5	2	5	2	5	4	7	7	69
171	17911A05H4	5	1	1	1	1	1	2	3	2	5	2	5	3	7	7	60

172	17911.A05I15	5	1	1	1	1	1	1	2	2	5	5	1	5	5	7	7	57
173	17911.A05I16	5	2	2	2	2	2	2	2	2	5	5	3	5	4	7	7	38
174	17911.A05I17	5	2	2	2	2	2	2	2	2	5	5	4	5	2	6	5	45
175	17911.A05I18	5	2	2	2	2	2	2	2	2	5	5	2	5	5	7	7	50
176	17911.A05I19	5	2	2	2	2	2	2	1	1	5	5	2	5	6	7	7	63
177	17911.A05I1	5	1	1	1	1	1	1	2	2	5	5	1	5	1	5	5	45
178	17911.A05I2	5	2	2	2	2	2	2	2	2	5	5	2	5	4	7	7	47
179	17911.A05I3	5	1	1	1	1	1	1	2	2	4	5	2	5	2	4	4	74
180	17911.A05I4	5	2	2	2	2	2	2	1	1	5	5	3	5	5	7	7	55
181	17911.A05I5	5	2	2	2	2	2	2	1	1	5	5	4	5	3	7	7	65
182	17911.A05I6	5	1	1	1	1	1	1	1	1	5	5	2	5	2	7	7	75
183	17911.A05I7	5	2	2	2	2	2	2	5	5	5	5	4	5	4	7	7	74
184	17911.A05I8	5	2	2	2	2	2	2	5	5	5	5	4	5	5	7	7	65
185	17911.A05I9	5	1	1	1	1	1	1	1	1	1	5	0	5	2	6	6	69
186	17911.A05K0	5	1	1	1	1	1	1	4	4	4	5	1	5	2	3	3	61
187	17911.A05K1	5	2	2	2	2	2	2	5	5	5	5	4	5	4	7	7	67
188	17911.A05K2	5	2	2	2	2	2	2	5	5	5	5	4	5	5	7	7	63
189	17911.A05K3	5	1	1	1	1	1	1	5	5	5	5	1	5	4	7	7	75
190	17911.A05K4	5	2	2	2	2	2	2	5	5	5	5	1	5	6	7	7	64
191	17911.A05K5	5	1	1	1	1	1	1	5	5	5	5	2	5	6	7	7	39
192	17911.A05K6	5	1	1	1	1	1	1	3	3	3	5	2	5	6	7	7	61
193	17911.A05K7	5	2	2	2	2	2	2	5	5	5	5	1	5	6	6	6	70
194	17911.A05K8	5	1	1	1	1	1	1	5	5	5	5	2	5	6	5	5	40
195	17911.A05K9	5	1	1	1	1	1	1	5	5	5	5	1	5	6	5	5	50
196	17911.A05L0	5	2	2	2	2	2	2	5	5	5	5	3	5	6	7	7	73
197	17911.A05L1	5	1	1	1	1	1	1	5	5	5	5	2	5	6	7	7	47
198	17911.A05L2	5	1	1	1	1	1	1	5	5	5	5	1	5	6	6	5	57
199	17911.A05L3	5	2	2	2	2	2	2	5	5	5	5	3	5	6	7	7	67
200	17911.A05L4	5	2	2	2	2	2	2	5	5	5	5	1	5	6	6	6	57
201	17911.A05L5	5	1	1	1	1	1	1	2	2	5	5	2	5	6	3	3	54
202	17911.A05L6	5	2	2	2	2	2	2	5	5	5	5	1	5	6	7	7	68
203	17911.A05L7	5	2	2	2	2	2	2	5	5	5	5	1	5	6	7	7	73
204	17911.A05L8	5	1	1	1	1	1	1	5	5	5	5	2	5	6	6	6	53
205	17911.A05L9	5	2	2	2	2	2	2	5	5	5	5	2	5	6	7	7	54
206	17911.A05M0	5	2	2	2	2	2	2	5	5	5	5	3	5	6	7	7	53
207	17911.A05M1	5	1	1	1	1	1	1	5	5	5	5	2	5	6	7	7	46
208	17911.A05M2	5	2	2	2	2	2	2	5	5	5	5	4	5	6	7	7	48
209	17911.A05M3	5	2	2	2	2	2	2	5	5	5	5	4	5	6	7	7	29
210	17911.A05M4	5	1	1	1	1	1	1	5	5	5	5	4	5	6	5	5	44
211	17911.A05M5	5	1	1	1	1	1	1	3	3	5	5	4	5	6	4	4	60
212	17911.A05M6	5	1	1	1	1	1	1	3	3	5	5	4	5	6	6	5	49
213	17911.A05M7	5	1	1	1	1	1	1	5	5	5	5	4	5	6	7	7	38
214	17911.A05M8	5	1	1	1	1	1	1	5	5	5	5	4	5	6	7	7	49
215	17911.A05M9	5	1	1	1	1	1	1	5	5	5	5	4	5	6	7	7	53
216	17911.A05N0	5	1	1	1	1	1	1	4	4	4	5	4	5	6	7	7	41
217	17911.A05N1	5	2	2	2	2	2	2	5	5	5	5	4	5	6	7	7	46
218	17911.A05N2	5	1	1	1	1	1	1	4	4	4	5	4	5	6	6	5	44

219	17911A05N3	5	1	1	1	1	3	3	4	5	6	7	7	55		
220	17911A05N5	5	1	1	1	2	2	4	4	5	6	5		44		
221	17911A05N6	5	1	1	1	4	4	4	4	5	6	7		36		
222	17911A05N7	5	2	2	2	5	5	5	4	5	6	7	7	44		
223	17911A05N8	5	1	1	1	5	5	5	4	5	6	7	7	53		
224	17911A05N9	5	1	1	1	5	5	5	4	5	6	7	7	43		
225	17911A05P0	5	2	2	2	5	5	5	4	5	6	7	7	49		
226	17911A05P2	5	2	2	2	5	5	5	4	5	6	7	7	34		
227	17911A05P3	5	1	1	1	5	5	5	4	5	6	7	7	41		
228	17911A05P4	5	1	1	1	4	4	4	4	5	6	7	7	44		
229	17911A05P5	5	2	2	2	5	5	5	4	5	6	7	7	42		
230	17911A05P6	5	2	2	2	5	5	5	4	5	6	6	6	32		
231	17911A05P7	5	1	1	1	5	5	5	4	5	6	7	7	31		
232	17911A05P8	5	1	1	1	0	0	4	4	5	6		3	41		
233	17911A05P9	5	1	1	1	3	3	4	4	5	6	6	5	48		
234	17911A05Q0	5	1	1	1	4	4	4	4	5	6	6	6	47		
235	18915A0501	5	2	2	2	5	5	5	4	5	6	7	7	55		
236	18915A0502	5	1	1	1	5	5	5	4	5	6	2	2	42		
237	18915A0503	5	1	1	1	3	3	3	4	5	6	4	4	40		
238	18915A0504	5	2	2	2	5	5	5	4	5	6	7	7	41		
Average marks		5	1,346/491	1,346/491	1,447/368	3,526/316	4,166/667	2,135/965	5	5	3,973/684	5,610/526	5,922/581	6,043/956	6,782/609	50,964/76
No of students attempted		228	228	228	228	228	228	228	228	228	228	95	155	91	115	227
% of students scored 60%		100	93.859649	93.859649	93.859649	67.105263	86.842105	66.666667	100	100	69.298246	87.368421	90.967742	96.703297	99.130435	81.50
CO ATTAINMENT		3	3.0	3.0	3.0	2.0	3.0	2.0	3	3	2.0	3.0	3.0	3.0	3.0	3.0

ASSESSMENT OF COs FOR THE COURSE								
CO	Method		value		CO Attainment (Internal)		CO Attainment (End Exam)	Overall CO Attainment
CO 1	ASM I		3	2.67				
	MID I - PART A - Q1		3.0					
	MID I - PART B - Q4		2.0					
	ASM I		3					
CO 2	MID I - PART A - Q2		3.0	3.00				
	MID I - PART B - Q5		3.0					
	ASM I		3					
	ASM II		3.0					
CO 3	MID I - PART A - Q3		3.0	2.6			3.00	2.96
	MID I - PART B - Q6		2.0					
	MID II - PART A - Q1		2.0					
	ASM II		3					
	MID II - PART B - Q2		3.0					
	MID II - PART B - Q3		3.0					
CO 4	ASM II		3	3				
	MID II - PART B - Q4		3.0					
	MID II - PART B - Q5		3.0					
	ASM II		3					
CO 5	MID II - PART B - Q4		3.0	3				
	MID II - PART B - Q5		3.0					

COURSE COORDINATOR

HOD-CSE

Faculty:

S.No	Reg.No	MID I Threshold 60%								MID II Threshold 60%								Threshold 60%
		ASM - I (5)	PART-A			PART-B			ASM - II (5)	PART-A				PART-B				
			Q1(2M)	Q2(2M)	Q3 B (2M)	Q4(5M)	Q5(5M)	Q6(4M)		Q1(2M)	Q2(2M)	Q3 A (2M)	Q4(4M)	Q5(5M)	Q6(5M)	End Exam (75M)		
1	16911A0574	5	2	2	2	4	4	5	2	2	1	4	2	4	2	10		
2	17911A0551	5	2	2	2	2	3	5	2	0	1	3	2	3	3	8		
3	17911A0568	5	2	2	2	2	2	5	2	0	1	2	2	2	2	14		
4	17911A0593	5	2	2	0	2	2	5	2	0	1	2	2	2	2	50		
5	17911A05A1	5	2	2	2	2	4	5	2	0	1	4	2	2	2	48		
6	18911A0501	5	2	2	2	3	3	5	2	0	2	3	3	2	2	65		
7	18911A0502	5	2	2	2	3	3	5	2	1	2	2	3	2	2	73		
8	18911A0503	5	2	2	2	4	4	5	2	1	2	4	4	4	4	53		
9	18911A0504	5	2	2	2	5	5	5	1	1	2	4	5	0	4	45		
10	18911A0505	5	2	2	2	3	3	5	1	1	2	1	3	0	6	62		
11	18911A0506	5	2	2	2	1	3	5	1	1	2	3	1	0	7	73		
12	18911A0507	5	2	2	2	3	3	5	2	0	2	4	3	3	3	56		
13	18911A0508	5	2	2	2	4	2	5	2	0	2	4	4	5	5	53		
14	18911A0509	5	2	2	2	5	3	5	2	0	2	4	5	5	5	62		
15	18911A0510	5	2	2	2	3	3	5	2	0	2	2	1	3	3	72		
16	18911A0511	5	2	2	2	1	3	5	2	0	2	3	1	3	3	59		
17	18911A0512	5	2	2	2	3	3	5	2	1	2	4	3	3	3	49		
18	18911A0513	5	2	2	2	4	5	5	2	1	2	4	4	5	5	54		
19	18911A0515	5	2	2	2	3	3	5	1	1	2	3	3	3	3	73		
20	18911A0516	5	2	2	2	3	3	5	1	1	2	2	3	3	3	8		
21	18911A0518	5	2	2	2	4	4	5	2	2	2	4	4	4	4	72		
22	18911A0519	5	2	2	2	4	4	5	2	2	2	4	4	4	4	75		
23	18911A0521	5	2	2	2	5	5	5	1	2	2	4	5	5	5	48		
24	18911A0522	5	2	2	2	3	3	5	1	2	2	1	3	3	3	61		
25	18911A0523	5	2	2	2	1	3	5	1	2	2	3	1	3	3	58		
26	18911A0524	5	2	2	2	3	3	5	2	2	2	4	3	3	3	62		
27	18911A0525	5	2	2	2	4	2	5	2	2	2	4	4	5	5	52		
28	18911A0527	5	2	2	2	5	3	5	2	2	2	4	5	5	5	73		
29	18911A0528	5	2	2	2	3	3	5	2	2	2	1	3	3	3	48		
30	18911A0529	5	2	2	2	1	3	5	2	2	2	3	1	3	3	46		
33	18911A0530	5	2	2	2	3	3	5	2	0	2	4	3	3	3	56		
32	18911A0531	5	2	2	2	4	5	5	2	0	2	4	4	5	5	49		
33	18911A0532	5	2	2	2	3	3	5	1	0	2	3	3	3	3	51		
34	18911A0533	5	2	2	2	3	3	5	1	0	2	2	3	3	3	46		
35	18911A0534	5	2	2	2	4	4	5	2	0	2	4	4	4	4	71		

57	18911A0535	5	2	2	2	2	4	4	4	5	2	1	2	4	4	4	65
59	18911A0536	5	2	2	2	2	5	4	4	5	1	1	2	4	5	5	69
58	18911A0537	5	2	2	2	2	3	3	3	5	1	1	2	1	3	3	56
51	18911A0538	5	2	2	2	2	1	3	3	5	1	1	2	3	1	3	60
40	18911A0539	5	2	2	2	2	3	3	4	5	2	1	2	4	3	3	A
41	18911A0540	5	2	2	2	2	4	2	4	5	2	0	2	4	4	5	59
42	18911A0541	5	2	2	2	2	5	3	4	5	2	0	2	4	5	5	75
43	18911A0542	5	2	2	2	2	3	3	3	5	2	0	2	1	3	3	52
56	18911A0543	5	2	2	2	2	1	3	3	5	2	0	2	3	1	3	75
45	18911A0544	5	2	2	2	2	3	3	4	5	2	0	2	4	3	3	57
46	18911A0546	5	2	2	2	2	4	5	4	5	2	1	2	4	4	5	48
47	18911A0547	5	2	2	2	2	3	3	3	5	1	1	2	3	3	3	62
48	18911A0548	5	2	2	2	2	3	3	2	5	1	1	2	2	3	3	68
49	18911A0549	5	2	2	2	2	4	4	4	5	2	1	2	4	4	4	63
50	18911A0550	5	2	2	2	2	4	4	4	5	2	2	2	4	4	4	57
51	18911A0551	5	2	2	2	2	5	5	4	5	1	2	2	4	5	5	68
52	18911A0552	5	2	2	2	2	3	3	3	5	1	2	2	1	3	3	49
53	18911A0553	5	2	2	2	2	1	3	3	5	1	0	2	3	1	3	61
54	18911A0554	5	2	2	2	2	3	3	4	5	2	0	2	4	3	3	70
55	18911A0555	5	2	2	2	2	4	2	4	5	2	0	2	4	4	5	74
56	18911A0556	5	2	2	2	2	5	3	4	5	2	0	2	4	5	5	59
57	18911A0557	5	2	2	2	2	3	3	3	5	2	0	2	1	3	3	73
58	18911A0559	5	2	2	2	2	1	3	3	5	2	1	2	3	1	3	53
59	18911A0560	5	2	2	2	2	3	3	4	5	2	1	2	4	3	3	55
60	18911A0561	5	2	2	2	2	4	5	4	5	2	1	2	4	4	5	48
61	18911A0562	5	2	2	2	2	3	3	3	5	1	1	2	3	3	3	46
62	18911A0563	5	2	2	2	2	3	3	2	5	1	1	2	2	3	3	50
63	18911A0564	5	2	2	2	2	4	4	4	5	2	0	2	4	4	4	49
64	18911A0565	5	2	2	2	2	4	4	4	5	2	0	2	4	4	4	71
65	18911A0566	5	2	2	2	2	5	5	4	5	1	0	2	4	5	5	52
66	18911A0567	5	2	2	2	2	3	3	3	5	1	0	2	1	3	3	46
67	18911A0568	5	2	2	2	2	1	3	3	5	1	0	2	3	1	3	53
68	18911A0569	5	2	2	2	2	3	3	4	5	2	1	2	4	3	3	56
69	18911A0570	5	2	2	2	2	4	2	4	5	2	1	2	4	4	5	47
70	18911A0571	5	2	2	2	2	5	3	4	5	2	1	2	4	5	5	64
71	18911A0572	5	2	2	2	2	3	3	3	5	2	1	2	1	3	3	56
72	18911A0573	5	2	2	2	2	1	3	3	5	2	2	2	3	1	3	62
73	18911A0574	5	2	2	2	2	3	3	4	5	2	2	2	4	3	3	55
74	18911A0575	5	2	2	2	2	4	5	4	5	2	2	2	4	4	5	61
75	18911A0576	5	2	2	2	2	3	3	3	5	1	2	2	3	3	3	45
76	18911A0577	5	2	2	2	2	3	3	2	5	1	0	2	2	3	3	61
77	18911A0578	5	2	2	2	2	4	4	4	5	2	0	2	4	4	4	69
78	18911A0579	5	2	2	2	2	4	4	4	5	2	0	2	4	4	4	64
79	18911A0580	5	2	2	2	2	5	5	4	5	1	0	2	4	5	5	58
80	18911A0581	5	2	2	2	2	3	3	3	5	1	0	2	1	3	3	51
81	18911A0582	5	2	2	2	2	1	3	3	5	1	1	2	3	1	3	65
82	18911A0583	5	2	2	2	2	3	3	4	5	2	1	2	4	3	3	61

83	18911A0584	5	1	2	2	4	2	4	5	2	1	2	4	4	5	49
84	18911A0585	5	1	2	2	5	3	4	5	2	1	2	4	5	5	49
85	18911A0586	5	1	2	2	3	3	3	5	2	1	2	1	3	3	58
86	18911A0587	5	0	2	2	1	3	3	5	2	0	2	3	1	3	45
87	18911A0588	5	0	2	2	3	3	4	5	2	0	2	4	3	3	57
88	18911A0589	5	0	2	2	4	5	4	5	2	0	2	4	4	5	56
89	18911A0590	5	0	2	2	3	3	3	5	1	0	2	3	3	3	45
90	18911A0591	5	0	2	2	3	3	2	5	1	0	2	2	3	3	52
91	18911A0592	5	1	2	2	4	4	4	5	2	1	2	4	4	4	46
92	18911A0593	5	1	2	2	4	4	4	5	2	1	2	4	4	4	55
93	18911A0594	5	1	2	2	5	5	4	5	1	1	2	4	5	5	50
94	18911A0595	5	1	2	2	3	3	3	5	1	1	2	1	3	3	61
95	18911A0596	5	2	2	2	1	3	3	5	1	2	2	3	1	3	46
96	18911A0597	5	2	2	2	3	3	4	5	2	2	2	4	3	3	48
97	18911A0598	5	2	2	2	4	2	4	5	2	2	2	4	4	5	53
98	18911A0599	5	1	2	2	5	3	4	5	2	2	2	4	5	5	45
99	18911A05A0	5	1	2	2	3	3	3	5	2	0	2	1	3	3	51
100	18911A05A1	5	1	2	2	1	3	3	5	2	0	2	3	1	3	40
101	18911A05A2	5	0	2	2	3	3	4	5	2	0	2	4	3	3	56
102	18911A05A3	5	0	2	2	4	5	4	5	2	0	2	4	4	5	53
103	18911A05A4	5	0	2	2	3	3	3	5	1	0	2	3	3	3	57
104	18911A05A5	5	0	2	2	3	3	2	5	1	1	2	2	3	3	54
105	18911A05A6	5	0	2	2	4	4	4	5	2	1	2	4	4	4	69
106	18911A05A7	5	1	2	2	4	4	4	5	2	1	2	4	4	4	48
107	18911A05A8	5	1	2	2	5	5	4	5	1	1	2	4	5	5	51
108	18911A05A9	5	1	2	2	3	3	3	5	1	1	2	1	3	3	55
109	18911A05B0	5	1	2	2	1	3	3	5	1	0	2	3	1	3	59
110	18911A05B1	5	2	2	2	3	3	4	5	2	0	2	4	3	3	47
111	18911A05B2	5	2	2	2	4	2	4	5	0	0	2	4	4	5	59
112	18911A05B3	5	2	2	2	5	3	4	5	0	0	2	4	5	5	71
113	18911A05B4	5	1	2	2	3	3	3	5	0	0	2	1	3	3	30
114	18911A05B6	5	1	2	2	1	3	3	5	0	1	2	3	1	3	57
115	18911A05B7	5	1	2	2	3	3	4	5	0	1	2	4	3	3	69
116	18911A05B8	5	0	2	2	4	5	4	5	1	1	2	4	4	5	50
117	18911A05B9	5	0	2	2	3	3	3	5	1	1	2	3	3	3	66
118	18911A05C1	5	0	2	2	3	3	2	5	1	2	2	2	3	3	64
119	18911A05C2	5	0	2	2	4	4	4	5	1	2	2	4	4	4	67
120	18911A05C3	5	0	2	2	3	3	3	5	1	0	2	3	3	3	65
121	18911A05C4	5	1	2	2	4	4	4	5	0	0	1	4	4	4	35
122	18911A05C5	5	1	2	2	2	2	2	5	0	0	1	3	2	2	58
123	18911A05C6	5	1	2	2	2	2	2	5	0	0	1	2	2	2	30
124	18911A05C7	5	1	2	0	2	2	2	5	0	0	1	2	2	2	51
125	18911A05C8	5	2	1	1	2	4	4	5	0	1	1	4	2	4	27
126	18911A05C9	5	1	1	1	3	3	3	5	1	1	2	3	3	3	35
127	18911A05D0	5	2	1	1	3	3	2	5	1	1	2	2	3	3	53
128	18911A05D1	5	1	0	0	4	4	4	5	1	1	2	4	4	4	48
129	18911A05D2	5	1	0	0	5	5	4	5	1	1	2	4	5	5	34

130	18911A05D4	5	1	0	0	0	3	3	3	3	5	1	0	2	1	3	3	47
153	18911A05D5	5	0	0	0	0	1	3	3	3	5	1	0	2	3	1	3	32
132	18911A05D7	5	0	0	0	0	3	3	3	4	5	2	0	2	4	3	3	51
133	18911A05D8	5	0	1	1	1	4	2	4	5	5	2	0	2	4	4	5	52
134	18911A05D9	5	0	1	1	1	5	3	4	5	5	0	0	2	4	5	5	47
135	18911A05E0	5	0	1	1	1	3	3	3	3	5	0	1	2	1	3	3	17
157	18911A05E1	5	1	1	1	1	1	3	3	3	5	0	0	2	3	1	3	41
159	18911A05E2	5	1	2	2	3	3	4	4	5	5	0	1	2	4	3	3	56
158	18911A05E3	5	1	2	2	4	5	4	5	4	5	0	1	2	4	4	5	26
151	18911A05E4	5	1	2	2	3	3	3	3	3	5	1	2	2	3	3	3	56
140	18911A05E5	5	0	1	1	3	3	3	2	2	5	1	0	2	2	3	3	53
141	18911A05E6	5	2	1	1	4	4	4	4	5	5	1	0	2	4	4	4	59
142	18911A05E7	5	2	1	1	3	3	3	3	5	5	1	0	2	3	3	3	48
143	18911A05E8	5	1	0	0	4	4	4	4	5	5	1	0	1	4	4	4	15
156	18911A05E9	5	1	0	0	2	2	2	3	5	5	0	0	1	3	2	2	34
145	18911A05F0	5	1	0	0	2	2	2	2	5	5	0	1	1	2	2	2	59
146	18911A05F1	5	0	0	0	2	2	2	2	5	5	0	1	1	2	2	2	35
147	18911A05F2	5	0	0	0	2	4	4	4	5	5	0	1	1	4	2	4	26
148	18911A05F3	5	0	1	1	3	3	3	3	5	5	0	1	2	3	3	3	18
149	18911A05F4	5	0	1	1	3	3	3	2	5	5	1	1	2	2	3	3	42
150	18911A05F5	5	0	1	1	4	4	4	4	5	5	1	0	2	4	4	4	32
151	18911A05F6	5	1	1	1	5	5	5	4	5	5	1	0	2	4	5	5	32
152	18911A05F7	5	1	2	2	3	3	3	3	5	5	1	0	2	1	3	3	34
153	18911A05F8	5	1	2	2	1	3	3	3	5	5	1	0	2	3	1	3	51
154	18911A05F9	5	1	2	2	3	3	3	4	5	5	2	0	2	4	3	3	43
155	18911A05G0	5	2	2	1	4	2	4	4	5	5	2	1	2	4	4	5	32
156	18911A05G1	5	2	2	1	5	3	4	4	5	5	2	1	2	4	5	5	35
157	18911A05G2	5	2	1	1	3	3	3	3	5	5	2	1	2	1	3	3	45
158	18911A05G3	5	2	1	0	1	3	3	3	5	5	2	1	2	3	1	3	42
159	18911A05G4	5	2	1	0	3	3	4	4	5	5	2	2	2	4	3	3	30
160	18911A05G5	5	2	0	0	4	5	4	4	5	5	2	2	2	4	4	5	29
161	18911A05G6	5	2	0	0	3	3	3	3	5	5	1	2	2	3	3	3	35
162	18911A05G7	5	2	0	0	3	3	3	2	5	5	1	2	2	2	3	3	58
163	18911A05G8	5	1	0	1	4	4	4	4	5	5	2	2	2	4	4	4	56
164	18911A05G9	5	1	0	1	3	3	3	3	5	5	2	2	2	3	3	3	50
165	18911A05H0	5	1	1	1	4	4	4	4	5	5	2	2	1	4	4	4	48
166	18911A05H1	5	1	1	1	2	2	3	3	5	5	2	2	1	3	2	2	56
167	18911A05H2	5	1	1	2	2	2	2	2	5	5	2	2	1	2	2	2	48
168	18911A05H3	5	1	1	1	2	2	2	2	5	5	2	2	1	2	2	2	42
169	18911A05H4	5	0	2	2	2	4	4	4	5	5	0	2	1	4	2	4	40
170	18911A05H5	5	0	2	2	3	3	3	3	5	5	0	2	2	3	3	3	32
171	18911A05H6	5	0	2	2	3	3	3	2	5	5	0	2	2	2	3	3	58
172	18911A05H7	5	0	2	2	4	4	4	4	5	5	0	0	2	4	4	4	27
173	18911A05H8	5	0	2	2	5	5	5	4	5	5	0	2	2	4	5	5	54
174	18911A05H9	5	1	2	2	3	3	3	3	5	5	1	2	2	1	3	3	47
175	18911A05J0	5	1	2	2	1	3	3	3	5	5	1	2	2	3	1	3	51
176	18911A05J1	5	0	2	2	3	3	3	3	5	5	1	2	2	4	3	3	59
176	18911A05J2	5	0	2	2	3	3	3	4	5	5	1	2	2	4	3	3	59

177	18911A05J3	5	0	2	2	2	4	2	4	5	1	2	2	4	4	5	55
178	18911A05J4	5	0	2	2	2	5	3	4	5	1	2	2	4	5	5	49
179	18911A05J5	5	0	2	2	2	3	3	3	5	0	2	2	1	3	3	56
180	18911A05J6	5	0	2	2	2	1	3	3	5	0	2	2	3	1	3	62
181	18911A05J7	5	1	2	2	2	3	3	4	5	0	2	2	4	3	3	49
182	18911A05J8	5	1	2	2	2	4	5	4	5	0	2	2	4	4	5	50
183	18911A05J9	5	1	2	2	2	3	3	3	5	0	2	2	3	3	3	52
184	18911A05K0	5	1	2	2	2	3	3	2	5	1	2	2	2	3	3	46
185	18911A05K1	5	2	2	2	2	4	4	4	5	1	2	2	4	4	4	55
186	18911A05K2	5	2	2	2	2	3	3	3	5	1	2	2	3	3	3	60
187	18911A05K4	5	2	2	2	2	4	4	4	5	1	2	2	4	4	4	56
188	18911A05K5	5	2	2	2	2	2	2	3	5	2	2	2	3	2	2	49
189	18911A05K6	5	2	2	2	2	2	2	2	5	2	2	2	2	2	2	45
190	18911A05K7	5	2	2	2	0	2	2	2	5	2	2	2	2	2	2	56
191	18911A05K8	5	2	2	2	2	0	4	4	5	2	2	2	4	2	4	48
192	18911A05K9	5	2	2	2	2	0	3	3	5	2	2	2	3	3	3	61
193	18911A05L0	5	2	2	2	2	0	3	2	5	2	2	2	2	3	3	53
194	18911A05L1	5	2	2	0	0	0	4	4	5	2	2	2	4	4	4	52
195	18911A05L2	5	2	2	0	0	0	5	4	5	1	2	2	4	5	5	49
196	18911A05L3	5	1	0	0	0	1	3	3	5	1	2	2	1	3	3	45
197	18911A05L4	5	2	0	0	0	1	3	3	5	1	2	2	3	1	3	59
198	18911A05L5	5	0	0	0	0	1	3	4	5	2	2	2	4	3	3	50
199	18911A05L7	5	0	1	1	1	1	2	4	5	2	2	2	4	4	5	32
200	18911A05L8	5	0	1	1	1	1	3	4	5	2	2	2	4	5	5	33
201	18911A05L9	5	0	1	1	1	0	3	3	5	2	2	2	1	3	3	32
202	18911A05M0	5	0	1	1	1	0	3	3	5	2	2	2	3	1	3	42
203	18911A05M1	5	1	1	1	1	0	3	4	5	2	2	2	4	3	3	43
204	18911A05M2	5	2	0	0	0	0	5	4	5	2	2	2	4	4	5	41
205	18911A05M3	5	1	0	0	0	0	3	3	5	1	2	2	3	3	3	34
206	18911A05M4	5	1	0	0	0	1	3	2	5	1	2	2	2	3	3	35
207	18911A05M5	5	1	0	0	0	1	4	4	5	2	2	2	4	4	4	50
208	18911A05M6	5	0	0	0	0	1	3	3	5	2	2	2	3	3	3	58
209	18911A05M7	5	0	1	1	1	1	4	4	5	2	2	2	4	4	4	57
210	18911A05M8	5	0	1	1	1	2	2	3	5	2	2	2	3	2	2	34
211	18911A05M9	5	0	1	1	1	2	2	2	5	2	2	2	2	2	2	57
212	18911A05N0	5	0	1	1	1	2	2	2	5	2	2	2	2	2	2	26
213	18911A05N1	5	1	2	2	2	2	4	4	5	2	2	2	4	2	4	40

214	18911A05N2	5	1	1	1	2	3	3	3	5	2	2	2	3	3	3	29
215	18911A05N3	5	1	1	1	2	3	3	3	5	2	2	2	2	3	3	42
216	18911A05N5	5	1	1	1	2	4	4	4	5	2	2	2	4	4	4	42
217	18911A05N6	5	2	0	0	2	5	5	4	5	1	2	2	4	5	5	59
218	18911A05N7	5	0	0	0	2	3	3	3	5	1	2	2	1	3	3	30
219	18911A05N8	5	0	0	0	2	1	3	3	5	1	2	2	3	1	3	26
220	18911A05N9	5	0	0	0	2	3	3	4	5	2	2	2	4	3	3	43
221	18911A05P0	5	0	0	0	2	4	2	4	5	2	2	2	4	4	5	34
222	18911A05P1	5	0	1	2	2	5	3	4	5	2	2	2	4	5	5	28
223	18911A05P2	5	0	1	2	2	3	3	3	5	2	2	2	1	3	3	51
224	18911A05P3	5	2	1	1	2	1	3	3	5	2	2	2	3	1	3	42
225	18911A05P4	5	1	1	1	2	3	3	4	5	2	2	2	4	3	3	58
226	18911A05P5	5	1	2	2	2	4	5	4	5	2	2	2	4	4	5	35
227	18911A05P6	5	1	2	2	2	3	3	3	5	1	2	2	3	3	3	50
228	18911A05P7	5	1	2	2	2	3	3	2	5	1	2	2	2	3	3	51
229	18911A05P8	5	1	2	2	2	4	4	4	5	2	2	2	4	4	4	30
230	18911A05P9	5	2	0	2	2	3	3	3	5	2	2	2	3	3	3	51
253	18911A05Q0	5	1	0	0	0	4	4	4	5	2	2	2	1	4	4	29
232	19915A0501	5	1	0	0	0	2	2	3	5	2	2	2	1	3	2	47
233	19915A0502	5	1	0	0	0	0	2	2	5	2	2	2	1	2	2	27
234	19915A0503	5	1	0	0	0	0	2	2	5	2	2	2	1	2	2	46
235	19915A0504	5	0	1	0	0	0	4	4	5	2	2	2	1	4	2	32
257	19915A0505	5	0	1	1	1	0	3	3	5	2	2	2	2	3	3	34
259	19915A0506	5	2	1	1	1	0	3	2	5	2	2	2	2	3	3	59
258	19915A0507	5	0	1	1	1	1	4	4	5	2	2	2	4	4	4	58
251	19915A0508	5	0	1	1	1	1	5	4	5	1	1	2	4	5	5	33
240	19915A0509	5	2	0	1	1	1	3	3	5	1	1	2	1	3	3	42
241	19915A0510	5	2	0	0	0	1	3	3	5	1	1	2	3	1	3	28
242	19915A0511	5	2	0	0	0	1	3	4	5	2	1	2	4	3	3	45
243	19915A0512	5	0	0	0	0	0	2	4	5	2	1	2	4	4	5	52
256	19915A0513	5	0	0	0	0	0	3	4	5	2	1	2	4	5	5	67
245	19915A0514	5	0	1	0	0	0	3	3	5	2	1	2	1	3	3	57
246	19915A0515	5	0	1	1	1	0	3	3	5	2	1	2	3	1	3	60
247	19915A0516	5	0	1	1	1	0	3	4	5	2	1	2	4	3	3	55
248	19915A0517	5	2	1	1	1	1	5	4	5	2	2	2	4	4	5	48
249	19915A0518	5	2	2	2	1	1	3	3	5	1	2	2	3	3	3	52
250	19915A0519	5	2	2	2	2	1	3	2	5	1	2	2	2	3	3	46
251	19915A0520	5	2	2	2	2	1	4	4	5	2	2	2	4	4	4	56
252	19915A0521	5	2	2	2	2	3	3	3	5	2	2	2	3	3	3	54
253	19915A0522	5	2	2	2	2	4	4	4	5	2	2	1	4	4	4	45
254	19915A0523	5	2	2	2	2	2	2	3	5	2	2	1	3	2	2	45
255	19915A0524	5	2	2	2	2	2	2	2	5	2	2	1	2	2	2	63
Average marks		5	1.3	1.5	1.5	2.7	3.2	3.4	3.4	5	1.4	1.2	1.9	3.1	3.1	3.5	49.7
No of students attempted		255	255	255	255	255	255	255	255	255	255	255	255	255	255	255	254
No of students scored		255	128.00	169.00	177.00	167.00	218.00	219.00	219.00	255	140.00	113.00	217.00	191.00	197.00	236.00	190.00
% of students scored		100	50.20	66.27	69.41	65.49	85.49	85.88	85.88	100	54.90	44.31	85.10	74.90	77.25	88.63	74.80
CO ATTAINMENT LEVEL		3	1.0	2.0	2.0	2.0	3.0	3.0	3.0	3	1.0	0.0	3.0	3.0	3.0	3.0	3.0

ASSESSMENT OF COs FOR THE COURSE						
CO	Method	value	Avg	CO Attainment	CO Attainment (End)	Overall CO Attainment
CO 1	ASM I	3	2.0	2.43	3.00	2.86
	MID I - PART A - Q1	1.0				
	MID I - PART B - Q4	2.0				
	ASM I	3				
CO	MID I - PART A - Q2	2.0	2.7			
	MID I - PART B - Q5	3.0				
	ASM I	3				
	ASM II	3				
CO	MID I - PART A - Q3	2.0	2.5			
	MID I - PART B - Q6	3.0				
	MID II - PART A - Q1	1.0				
	MID II - PART B - Q4	3.0				
	ASM II	3				
	MID II - PART A - Q2	0.0				
CO 4	MID II - PART B - Q5	3.0	2.0			
	ASM II	3				
CO 5	MID II - PART A - Q3	3.0	3.0			
	MID II - PART B - Q6	3.0				

COURSE-COORDINATOR

HOD-CSE



Vidya Jyothi Institute of Technology

(An Autonomous Institution)
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Aziz Nagar Gate, C.B. Post, Hyderabad-500 075

Department of Computer Science & Engineering
(Accredited by NBA)

BATCH: 2017-21

Academic Year: 2019-20

III B.Tech- II Sem

Course: CD

Branch: CSE

S.No	Reg. No	MID I Threshold 60%						MID II Threshold 60%					Threshold 60%	
		ASMT - I (5)	PART-A			PART-B			ASMT - II (5)	PART-A		PART-B		
			Q1(2M)/C (O1)	Q2(2M)/C (O2)	Q3(2M)/C (O3)	Q4(5M)/C (O1)	Q5(5M)/C (O2)	Q6(4M)/C (O3)		Q1(6M)/C (O3)	Q2(7M)/C (O4)	Q3(7M)/C (O4)		Q4(7M)/C (O5)
1	14911.N0516	5	1	1	1	2	2	1	5	4	7		7	1
2	16911.N0531	5	2	2	2	5	5	3	5	5		7		41
3	16911.N0547	5	0	0	0	0	0	0	5	0	0	0		29
4	16911.N0557	5	1	1	1	3	3	2	5	4	7		7	48
5	16911.N0515	5	1	1	1	2	2	1	5	4	7		7	54
6	16911.N0519	5	2	2	2	5	5	3	5	5		7	7	46
7	16911.N05K9	5	0	0	0	0	0	0	5	0	0	0		52
8	16911.N05N5	5	1	1	1	3	3	2	5	4	7		7	45
9	17601.N0511	5	1	1	1	2	2	1	5	4	7		7	53
10	17601.N0575	5	2	2	2	5	5	3	5	5		7	7	44
11	17911.N0501	5	0	0	0	0	0	0	5	0	0	0		52
12	17911.N0502	5	1	1	1	3	3	2	5	4	7		7	53
13	17911.N0503	5	1	1	1	2	2	1	5	4	7		7	48
14	17911.N0504	5	2	2	2	5	5	3	5	5		7	7	49
15	17911.N0505	5	0	0	0	0	0	0	5	0	0	0		56
16	17911.N0506	5	1	1	1	3	3	2	5	4	7		7	42
17	17911.N0507	5	1	1	1	2	2	1	5	4	7		7	40
18	17911.N0508	5	2	2	2	5	5	3	5	5		7	7	45
19	17911.N0509	5	0	0	0	0	0	0	5	0	0	0		53
20	17911.N0510	5	1	1	1	3	3	2	5	4	7		7	37
21	17911.N0511	5	1	1	1	2	2	1	5	4	7		7	40
22	17911.N0512	5	2	2	2	5	5	3	5	5		7	7	53
23	17911.N0513	5	0	0	0	0	0	0	5	0	0	0		39
24	17911.N0514	5	1	1	1	3	3	2	5	4	7		7	48
25	17911.N0515	5	1	1	1	5	5	1	5	2		6		37
26	17911.N0516	5	1	1	1	5	5	1	5	1	7		7	44
27	17911.N0517	5	2	2	2	5	5	3	5	2		7		52
28	17911.N0518	5	2	2	2	5	5	2	5	5		7	7	53
29	17911.N0520	5	2	2	2	5	5	3	5	5		7	7	53
30	17911.N0521	5	2	2	2	5	5	3	5	5		7	7	55

[illegible]

78	17911.N0574	5	1	1	1	5	5	1	5	4	7	7	7	62
79	17911.N0575	5	1	1	1	3	3	2	5	3		7	7	64
80	17911.N0576	5	1	1	1	4	4	2	5	2		7	7	61
81	17911.N0577	5	1	1	1	2	2	2	5	3		7	7	26
82	17911.N0578	5	1	1	2	5	5	2	5	2	4		4	55
83	17911.N0579	5	1	1	2	5	5	2	5	2	5	5		59
84	17911.N0580	5	1	1	1	3	3	1	5	5		7	7	67
85	17911.N0581	5	2	2	2	5	5	1	5	4	7		7	56
86	17911.N0582	5	2	2	2	5	5	1	5	2	5			59
87	17911.N0583	5	1	1	1	3	3	1	5	2		6	5	31
88	17911.N0584	5	2	2	2	5	5	1	5	1		5	5	56
89	17911.N0585	5	2	2	2	5	5	1	5	4	7		7	58
90	17911.N0586	5	1	1	1	4	4	1	5	2	4		4	11
91	17911.N0587	5	1	1	2	5	5	2	5	2	4		4	35
92	17911.N0589	5	1	1	1	3	3	2	5	4			7	63
93	17911.N0590	5	1	1	1	4	4	2	5	2		7	7	29
94	17911.N0591	5	2	2	2	5	5	2	5	6		7	7	57
95	17911.N0594	5	1	1	2	5	5	2	5	6		7	7	59
96	17911.N0595	5	1	1	1	5	5	1	5	3		7	7	42
97	17911.N0596	5	1	1	1	3	3	2	5	5		7	7	31
98	17911.N0597	5	1	1	1	4	4	2	5	6		7	7	64
99	17911.N0598	5	2	2	2	5	5	1	5	6		7	7	54
100	17911.N0599	5	1	1	1	4	4	1	5	5		7	7	63
101	17911.N05A0	5	2	2	2	5	5	2	5	0	0			31
102	17911.N05A2	5	1	1	1	4	4	2	5	2		6	6	55
103	17911.N05A3	5	1	1	1	5	5	1	5	6		7	7	40
104	17911.N05A5	5	1	1	1	5	5	2	5	2		6	6	65
105	17911.N05A6	5	1	1	1	5	5	1	5	5		7	7	40
106	17911.N05A7	5	2	2	2	5	5	1	5	6		7	7	59
107	17911.N05A8	5	1	1	1	5	5	2	5	4	7		7	34
108	17911.N05A9	5	1	1	2	5	5	2	5	2		6	5	38
109	17911.N05B0	5	1	1	1	2	2	2	5	2		6	5	A
110	17911.N05B1	5	1	1	1	5	5	1	5	2		6	5	
111	17911.N05B2	5	2	2	2	5	5	2	5	6		7	7	47
112	17911.N05B3	5	1	1	1	2	2	2	5	5		7	7	26
113	17911.N05B4	5	2	2	2	5	5	1	5	1		5	5	48
114	17911.N05B6	5	1	1	2	1	5	2	5	6		7	7	61
115	17911.N05B7	5	1	1	1	1	5	2	5	3		7	7	46
116	17911.N05B8	5	1	1	1	1	5	1	5	2		6	6	47
117	17911.N05B9	5	1	1	1	2	5	1	5	4	7		7	54
118	17911.N05C0	5	2	2	2	2	5	1	5	6		7	7	37
119	17911.N05C1	5	1	1	2	2	5	2	5	4	7		7	47
120	17911.N05C2	5	1	1	2	2	5	2	5	2		2	2	53
121	17911.N05C3	5	1	1	2	2	5	2	5	5		7	7	56
122	17911.N05C4	5	2	2	2	1	5	1	5	4	7		7	67
123	17911.N05C5	5	1	1	1	2	3	2	5	1	7		7	71
124	17911.N05C7	5	2	2	2	2	5	4	5	6		7	7	64

125	17911.N05C8	5	2	2	2	2	2	5	5	2	5	6		7	7		53
126	17911.N05C9	5	2	2	2	2	1	5	5	4	5	4	7			7	63
127	17911.N05D0	5	1	1	1	1	1	5	5	2	5	0	5	0		0	64
128	17911.N05D1	5	1	1	1	1	1	4	5	2	5	2	5	5			71
129	17911.N05D2	5	1	1	1	1	2	2	5	2	5	2	5	5			59
130	17911.N05D3	5	2	2	2	2	5	5	5	1	5	6		7	7		68
131	17911.N05D4	5	1	1	1	1	4	4	5	2	5	2		7	7		68
132	17911.N05D5	5	1	1	1	1	1	5	5	2	5	5		7		67	
133	17911.N05D6	5	2	2	2	2	1	5	5	1	5	5		7		73	
134	17911.N05D7	5	1	1	1	2	1	5	5	2	5	5		7		70	
135	17911.N05D8	5	2	2	2	2	2	5	5	1	5	6		7		70	
136	17911.N05D9	5	1	1	1	1	4	4	5	2	5	4	7		7	72	
137	17911.N05E0	5	2	2	2	2	2	5	5	1	5	0	0		0	33	
138	17911.N05E1	5	2	2	2	2	2	5	5	4	5	5		7		38	
139	17911.N05E2	5	2	2	2	2	2	5	5	1	5	4	7			69	
140	17911.N05E3	5	2	2	2	2	1	5	5	3	5	6		7		52	
141	17911.N05E4	5	1	1	1	2	2	5	5	2	5	4	7			68	
142	17911.N05E5	5	2	2	2	2	2	5	5	1	5	5		7		46	
143	17911.N05E6	5	1	1	1	1	2	4	5	1	5	5		7		67	
144	17911.N05E7	5	2	2	2	2	1	5	5	2	5	6		7		72	
145	17911.N05E8	5	2	2	2	2	1	5	5	3	5	2		6		43	
146	17911.N05E9	5	2	2	2	2	1	5	5	3	5	4	7			62	
147	17911.N05F0	5	1	1	1	1	1	5	5	0	5	1		4		63	
148	17911.N05F1	5	1	1	1	1	4	4	5	1	5	2	5			67	
149	17911.N05F2	5	2	2	2	2	5	5	5	3	5	5		7		60	
150	17911.N05F3	5	2	2	2	2	5	5	5	4	5	6		7		46	
151	17911.N05F4	5	2	2	2	2	5	5	5	4	5	5		7		67	
152	17911.N05F5	5	2	2	2	2	5	5	5	1	5	6		7		75	
153	17911.N05F6	5	2	2	2	2	5	5	5	3	5	5		7		36	
154	17911.N05F7	5	1	1	1	1	2	2	5	1	5	6		7		46	
155	17911.N05F8	5	2	2	2	2	5	5	5	4	5	6		7		68	
156	17911.N05F9																
157	17911.N05G0																
158	17911.N05G1																
159	17911.N05G2																
160	17911.N05G3																
161	17911.N05G4																
162	17911.N05G5																
163	17911.N05G6																
164	17911.N05G7																
165	17911.N05G8																
166	17911.N05G9	5	2	2	2	2	5	5	5	3	5	4	7			49	
167	17911.N05H0	5	1	1	1	2	5	5	5	2	5	2		6		45	
168	17911.N05H1	5	1	1	1	1	1	5	5	1	5	4	7			39	
169	17911.N05H2	5	1	1	1	1	1	5	5	2	5	1		5		68	
170	17911.N05H3	5	1	1	1	1	1	5	5	2	5	4	7			69	
171	17911.N05H4	5	1	1	1	1	2	3	5	2	5	3		7		60	

172	17911.M05H5	5	1	1	1	2	5	1	5	5	4	7		7	57
173	17911.M05H6	5	2	2	2	2	5	3	5			7		7	38
174	17911.M05H7	5	2	2	2	2	5	4	5	2		6	5		45
175	17911.M05H8	5	2	2	2	2	5	2	5	5		7		7	50
176	17911.M05H9	5	2	2	2	1	5	2	5	6		7	7		63
177	17911.M05I1	5	1	1	1	2	5	1	5	1		5	5		45
178	17911.M05I2	5	2	2	2	2	5	2	5	4	7			7	47
179	17911.M05I3	5	1	1	1	2	4	2	5	2	4		4	7	74
180	17911.M05I4	5	2	2	2	1	5	3	5	5		7		7	55
181	17911.M05I5	5	2	2	2	2	1	4	5	3		7		7	65
182	17911.M05I6	5	1	1	2	2	5	2	5	2	7	7	7		75
183	17911.M05I7	5	2	2	2	5	5	4	5	4	7			7	74
184	17911.M05I8	5	2	2	2	2	5	4	5	5		7		7	65
185	17911.M05I9	5	1	1	1	1	1	0	5	2	6		6		69
186	17911.M05K0	5	1	1	1	4	4	1	5	2	3		3		61
187	17911.M05K1	5	2	2	2	2	5	4	5	4	7			7	67
188	17911.M05K2	5	2	2	2	2	5	4	5	5	7			7	63
189	17911.M05K3	5	1	1	1	1	5	1	5	4	7			7	75
190	17911.M05K4	5	2	2	2	5	5	1	5	6		7	7		64
191	17911.M05K5	5	1	1	1	1	5	2	5	6		7		7	39
192	17911.M05K6	5	1	1	1	1	5	2	5	6	7	7		7	61
193	17911.M05K7	5	2	2	2	2	5	1	5	6		6	6		70
194	17911.M05K8	5	1	1	1	1	5	2	5	6		5	5		40
195	17911.M05K9	5	1	1	1	1	5	1	5	6	5	5			50
196	17911.M05L0	5	2	2	2	2	5	3	5	6		7	7		73
197	17911.M05L1	5	1	1	1	1	5	2	5	6	7			7	47
198	17911.M05L2	5	1	1	1	1	5	1	5	6		6	5		57
199	17911.M05L3	5	2	2	2	2	5	3	5	6		7		7	67
200	17911.M05L4	5	2	2	2	2	5	1	5	6		6	6		57
201	17911.M05L5	5	1	1	1	1	2	2	5	6	3			3	54
202	17911.M05L6	5	2	2	2	2	5	1	5	6	7			7	68
203	17911.M05L7	5	2	2	2	2	5	1	5	6	7			7	73
204	17911.M05L8	5	1	1	1	2	5	2	5	6		6			53
205	17911.M05L9	5	2	2	2	2	5	2	5	6	7		7		54
206	17911.M05M0	5	2	2	2	2	5	3	5	6	7			7	53
207	17911.M05M1	5	1	1	1	1	5	2	5	6	7			7	46
208	17911.M05M2	5	2	2	2	2	5	4	5	6		7	7		48
209	17911.M05M3	5	2	2	2	2	5	4	5	6	7			7	29
210	17911.M05M4	5	1	1	1	2	5	4	5	6	5	5			44
211	17911.M05M5	5	1	1	1	1	3	4	5	6	4			4	60
212	17911.M05M6	5	1	1	1	1	3	4	5	6			5		49
213	17911.M05M7	5	1	1	1	1	5	4	5	6		7	7		38
214	17911.M05M8	5	1	1	1	2	5	4	5	6		7	7		49
215	17911.M05M9	5	1	1	1	2	5	4	5	6		7	7		53
216	17911.M05N0	5	1	1	1	1	4	4	5	6	7			7	41
217	17911.M05N1	5	2	2	2	2	5	4	5	6	7			7	46
218	17911.M05N2	5	1	1	1	1	4	4	5	6		6	5		44

219	17911.N05N3	5	1	1	1	3	3	4	5	6	7		7	55	
220	17911.N05N5	5	1	1	1	2	2	4	5	6		6		44	
221	17911.N05N6	5	1	1	1	4	4	4	5	6		7	5	44	
222	17911.N05N7	5	2	2	2	5	5	4	5	6	7		7	36	
223	17911.N05N8	5	1	1	1	5	5	4	5	6	7		7	44	
224	17911.N05N9	5	1	1	1	5	5	4	5	6	7		7	53	
225	17911.N05P0	5	2	2	2	5	5	4	5	6	7		7	43	
226	17911.N05P2	5	2	2	2	5	5	4	5	6	7		7	49	
227	17911.N05P3	5	1	1	1	5	5	4	5	6	7		7	34	
228	17911.N05P4	5	1	1	1	4	4	4	5	6	7		7	41	
229	17911.N05P5	5	2	2	2	5	5	4	5	6	7		7	44	
230	17911.N05P6	5	2	2	2	5	5	4	5	6		6	6	42	
231	17911.N05P7	5	1	1	1	5	5	4	5	6	7		7	32	
232	17911.N05P8	5	1	1	1	0	0	4	5	6		3	3	31	
233	17911.N05P9	5	1	1	1	3	3	4	5	6		6	5	41	
234	17911.N05Q0	5	1	1	1	4	4	4	5	6		6	6	48	
235	18915.N0501	5	2	2	2	5	5	4	5	6		7	7	47	
236	18915.N0502	5	1	1	1	5	5	4	5	6		2	2	55	
237	18915.N0503	5	1	1	1	3	3	4	5	6	4		4	42	
238	18915.N0504	5	2	2	2	5	5	4	5	6	7		7	40	
Average marks		5	1.346491	1.346491	1.447368	3.526316	4.166667	2.135965	5	3.973684	5.610526	5.922581	6.043956	6.782609	50.96476
No of students attempted		228	228	228	228	228	228	228	228	228	95	155	91	115	227
No of students scored 60%		100	93.850649	93.850649	93.850649	67.105263	86.842105	66.666667	100	69.298246	87.368421	90.967742	96.703297	99.130435	81.50
CO ATTAINMENT		3	3.0	3.0	3.0	2.0	3.0	2.0	3	2.0	3.0	3.0	3.0	3.0	3.0

ASSESSMENT OF COs FOR THE COURSE						
CO	Method		value	CO Attainment (internal)	CO Attainment (End Exam)	Overall CO Attainment
CO 1	ASM I		3	2.67		
	MID I - PART A Q1		3.0			
	MID I - PART B Q4		2.0			
	ASM I		3			
CO 2	MID I - PART A Q2		3.0	3.00		
	MID I - PART B Q5		3.0			
	ASM I		3			
	ASM II		3.0			
CO 3	MID I - PART A Q3		3.0	2.6	3.00	2.96
	MID I - PART B Q6		2.0			
	MID II - PART A - Q1		2.0			
	ASM II		3			
CO 4	MID II - PART B - Q2		3.0	3		
	MID II - PART B Q3		3.0			
	ASM II		3			
	MID II - PART B Q4		3.0			
CO 5	MID II - PART B Q5		3.0	3		

Anderson
COURSE COORDINATOR

HOD-CSE

Head of the Department
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VIDYA JYOTHI INSTITUTE OF TECHNOLOGY
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

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CD					
	OP1	OP2	OP3	TOTAL	ATTAIN
CO1	11	12	212	235	2.86
CO2	11	24	200	235	2.80
CO3	12	24	199	235	2.80
CO4	10	24	201	235	2.81
CO5	25	20	190	235	2.70
					2.79

CO OVERALL ATTAINMENT=80% of Direct + 20% of Indirect
2.20

[Signature]

[Signature]
Head of the Department
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VJIT, Hyderabad-50075.