

CHIEF PATRON

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Mrs. S. Neelima
Joint Secretary, VJES

PATRONS

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Dr. A. Padmaja, Principal

Head of the Department

Dr.K.Vasanth,Professor/ECE

COORDINATOR

Dr.P. Ganesan,Professor/ECE

ADVISORY COMMITTEE

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Dr. G.Sreeram Reddy, HoD/MECH
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Dr.Siddhartha Ghosh, HoD/AI
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Dr. S.Saravanan, Asso.Professor/ECE
Mrs.G.Srilatha, Academic Coordinator
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ORGANIZING COMMITTEE

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Mr. Maznu Shaik, Assoc.Professor
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Mrs. E. Supraja, Assistant Professor
Mr. G. Parameshwar, Assistant Professor
Mr. Md. Akram Ahmed, Assistant Professor
Mr. A.Satheesh, Assistant Professor
Mrs. Y. Aruna, Assistant Professor

GENERAL INSTRUCTIONS

- The Faculty members of the AICTE approved Institutions, Research Scholars, PG Scholars and Faculty of host Institutions are also eligible to attend the program.
- Maximum 100 participants may be allowed to attend online FDP on a first come first serve basis.
- E-certificate will be issued to those participants who have attended the program with minimum 80% Attendance and scored minimum 60% marks in the online test which will be conducted at end of the Program.

REGISTRATION

- Registration for all the participants is mandatory.
- All the participants are kindly requested to register for this FDP through online by visiting <https://forms.gle/ryDbHMKZBWCcx698>

REGISTRATION FEE

- There is no registration fee for participants as the FDP is sponsored by AICTE.

RESOURCE PERSONS

- Eminent Professors, Scientists and Industry Experts from leading organizations with an unmatched experience and knowledge in the field of Artificial Intelligence, Digital Signal and Image Processing .

ADDRESS FOR COMMUNICATION

Dr. Ganesan. P
Professor and Coordinator,
Department of Electronics and Communication Engineering,
Vidya Jyothi Institute of Technology (A),
Hyderabad - Telangana
Email: drganesan@vjit.ac.in
Mobile: 9884161831



**AICTE Sponsored
Two Week
Online Faculty Development Programme (FDP)
On
“Implementation of Artificial Intelligence for
Solving Digital Signal and Image Processing
Problems”**

16-11-2020 to 28-11-2020

FN: 10.00 AM To 12.00 PM

AN: 02.00 PM To 04.00 PM

Organized by

Department of Electronics and Communication Engg

**VIDYA JYOTHI INSTITUTE OF
TECHNOLOGY**

Aziz Nagar Gate C.B. Post, Hyderabad-500 075, Telangana, India
(An Autonomous Institution)

Accredited by NBA & NAAC



ABOUT THE COLLEGE

Vidya Jyothi Institute of Technology (VJIT) was established in 1999. It is situated in the backdrop of Osmansagar (Gandipet) lake in the serene surroundings of Chilkur Balaji Temple, VJIT has a sprawling and lush green campus with architecturally splendid buildings in an area of 12 acres. The college has got a rich library of books, a state-of-art internet lab, and modern labs for each department, a central workshop, sports and games facilities. The college is accredited by NAAC & NBA of AICTE. The college prides on the fact that it has a very senior and highly accomplished faculty. VJIT is rated as one of the best engineering colleges in the region.

ABOUT THE DEPARTMENT

The department of ECE is a major strength of the institute. The department has an excellent group of faculty having teaching, industry and research experience. The department of ECE offers P.G/U.G Programs M.Tech/B.Tech with an intake of 36 seats in M.Tech / 240 seats in B.Tech. The department has well equipped laboratories. We nurture the young talent available in the country and transform them into enterprising technologists so that they contribute immensely to the technological development and prosperity of the country and provide dynamic leadership to others. The department is also equipped with exclusive research labs like NI Labview center of excellence, ARM University Program & Cypress PSOC Semiconductors Laboratory.

ABOUT FDP

Artificial intelligence (AI) is the simulation of human intelligence processes by machines, especially computer systems. These processes include learning the acquisition of information and rules for using the information, reasoning using rules to reach approximate or definite conclusions and self-correction. Particular applications of AI include expert systems, speech recognition and machine vision. The main objective of this FDP is to provide a forum to participants an opportunity to discuss and exchange the current trends in AI and also review the challenges faced by the community in 21st century. This FDP covers an overview of Digital Signal and Image Processing, AI and their applications. The participants can obtain the deep insights of Artificial Intelligence, which in turn helps them to apply it in practice to a deeper study in this area.



OBJECTIVES OF FDP

This FDP introduces fundamental concepts of Artificial Intelligence. It also covers an overview of Artificial Intelligence algorithms and their applications. The main objectives of our FDP is

- To expose the participants in emerging technologies in the areas of Artificial Intelligence.
- To understand how to solve real world problems using Artificial Intelligence
- To familiar with the concept of soft computing techniques to simulate digital signal and image processing applications

TOPICS TO BE COVERED

- Fundamentals of Signal and Image Processing
- AI for Digital Signal Processing
- Emerging Trends in Signal Processing and Applications
- Transform Analysis of Signals
- Understanding SVM for Image Classification
- Big Image Data Processing using Map Reduce
- Recent Advances in Automatic Face Recognition: Issues, challenges and Alternate Applications.
- Implementation of Artificial Intelligence for Video Analytics Research
- AI and ML in Speech Processing Applications
- Practical Aspects of Deep Learning in Vision System
- Underwater Signal Processing
- New Dimensions of Problem-Solving Through Machine Learning
- AI for Food and Security Applications
- Implementation of Competitive Neural Networks for Image Processing – Hands on Training
- Deep Learning Techniques for Signal Processing and Computer Vision
- Industrial Applications of Machine learning

