

## Vidya Jyothi Institute of Technology

Minutes of the Board of studies of Dept. of Electronics and Communication  
Engineering meeting held on 1<sup>st</sup> APRIL 2019

at 1:00pm in Board Room

The following members were present in the meeting.

S.No	Name of the Member	Designation	Signature
1	Dr. HariKrishna Kamatham, Professor & Head ECE, VJIT.	Chairman	
2	Dr. M Madhavi Latha, Professor, JNTUH.	University Nominee	
3	Dr. K. S. Rao, Director, Anurag Group of Institutions.	External Member	
4	Dr. G. Laxmi Narayana, Principal, Anurag College of Engg., Aushapur.	External Member	
5	Dr. P. Chandra Sekhar, Dept. of ECE, Osmania University	External Member	
6	Mr. Ch. Satyanarayana, Scientist G, RCI, Hyderabad.	External Member	
7	Mr. N. Venkatesh, Sr. Vice – President, Redpine Signals, Hyderabad.	External Member	
8	Dr. S. Thulasi Prasad, Professor, ECE, VJIT.	Internal Member	
9	Dr. K. Vasanth, Professor, ECE, VJIT.	Internal Member	
10.	Mr. M Rajendra Prasad, Assoc. Professor, ECE, VJIT.	Internal Member	
11.	Mrs. A. Jaya Lakshmi, Assoc. Professor, ECE, VJIT.	Internal Member	

A. Prasad

PRINCIPAL  
Vidya Jyothi Institute of Technology  
Himayatnagar (VIII), C. B. Road  
Hyderabad-75

**Item No. 1: Presentation of 2<sup>nd</sup>, 3<sup>rd</sup> & 4<sup>th</sup> Year ECE (R18) B.Tech Course Structure**

The Chairman presented the B.Tech course structure of all three years under R18 Regulations for ECE students as per ANNEXURE - I.

**Item No. 2: Presentation of 2<sup>nd</sup>, 3<sup>rd</sup> & 4<sup>th</sup> Year ECE (R18) B.Tech Syllabi**

The Chairman presented the detailed syllabi of all subjects of 2<sup>nd</sup>, 3<sup>rd</sup> & 4<sup>th</sup> B.Tech (ECE) under R18 Regulations along with prerequisites, course objectives and course outcomes as per ANNEXURE - II.

**Item No. 3: Presentation of Engineering Science Subjects offered to other Departments under R18 regulations.**

- a. The Chairman presented the detailed syllabi of the following subjects offered to EEE Department as per ANNEXURE - III
  - i. Electronic Devices and Circuits for II Year II Semester
  - ii. Electronic Devices and Circuits Lab for II Year II Semester
  - iii. Switching Theory and Logic Design for III Year I Semester
  - iv. Integrated Circuits and Applications for III Year II Semester
  - v. Microprocessors and Interfacing for IV Year I Semester
  - vi. Microprocessors and Interfacing Lab for IV Year I Semester
- b. The Chairman presented the detailed syllabi of the following subjects offered to CSE/IT Departments as per ANNEXURE - III
  - i. Electronic Devices and Circuits for II Year I Semester
  - ii. Digital Logic Design for II Year I Semester
  - iii. EDC & DLD Lab for II Year I Semester

**Item No. 4: Presentation of 1<sup>st</sup> & 2<sup>nd</sup> Year ECE (R19) M.Tech Course Structure**

The Chairman presented the Course Structure (R19 regulation) for the following PG programs offered by ECE Department as per ANNEXURE - IV.

- a. VLSI System Design
- b. Embedded Systems

**Item No. 5: Presentation of 1<sup>st</sup> Year ECE (R19) M.Tech Syllabi.**

The Chairman presented the detailed syllabi of all subjects of 1<sup>st</sup> year for the following PG Courses as per ANNEXURE - V.

- a. VLSI System Design
- b. Embedded Systems

*A. Kumar*  
2

PRINCIPAL  
Vidya Jyothi Institute of Technology  
Himayatnagar (Vill), C. B. Post  
Hyderabad-75

**Item No. 6: Discussion regarding the Panel of examiners**

The Chairman emphasized the necessity of Panel of Examiners. Their services will be utilized in the preparation of End-Semester Question paper(s), Evaluation of End-Semester Examination Answer Scripts. They will be paid remuneration as per the recommendations of College Finance Committee.

**RESOLUTIONS**

After discussing various aspects of the syllabi the committee passed the following resolution.

**Resolution (1):** The members after thorough discussion approved the course structure and syllabi of 2<sup>nd</sup>, 3<sup>rd</sup> & 4<sup>th</sup> Year B.Tech (R18) as per Annexure - I, II & III.

**Noted and Approved.**

**Resolution (2):** The members after thorough discussion approved the course structure and syllabi of M.Tech (R19) Programs (VLSISD & ES) as per Annexure - IV & V.

**Noted and Approved.**

**Resolution (3):** The BoS Chairman is authorized to prepare the Panel of examiners for both B.Tech & M.Tech.

**Noted and Approved.**

**Signatures of the Members Present:**

1. 

2. M. N. Chaitanya

3. 

4. 

5. 

6. 

7. 

8. 

9. 

10. 

11. 



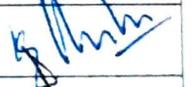
3  
**PRINCIPAL**  
Vidya Jyothi Institute of Technology  
Himayatnagar (VIII), C. B. Post  
Hyderabad-73

# Vidya Jyothi Institute of Technology

Minutes of the Board of studies of Dept. of Electronics and Communication  
Engineering meeting held on 2<sup>nd</sup> March 2018

at 2.00 P.M in Board Room

The following members were present in the meeting.

S.No	Name of the Member	Designation	Signature
1	Dr. Harikrishna Kamatham, Professor & Head ECE, VJIT.	Chairman	
2	Dr. M.Madhavi Latha, Professor, JNTUH.	University Nominee	
3	Dr. K S Rao, Director, Anurag Group of Institutions.	External Member	
4	Dr. G. Laxmi Narayana, Principal, Anurag College of Engg., Aushapur.	External Member	
5	Dr. P. Chandra Sekhar, Dept. of ECE, Osmania University	External Member	
6	Mr. Ch. Satyanarayana, Scientist G, RCI, Hyderabad.	External Member	
7	Mr. N. Venkatesh, Sr. Vice – President, Redpine Signals, Hyderabad.	External Member	
8	Dr. K. Vasanth, Professor, ECE, VJIT.	Internal Member	
9	Dr. P. Ganesan, Professor, ECE, VJIT.	Internal Member	
10.	Mr. M Rajendra Prasad, Assoc. Professor, ECE, VJIT.	Internal Member	
11.	Mrs. A. Jaya Lakshmi, Assoc. Professor, ECE, VJIT.	Internal Member	

  
PRINCIPAL  
Vidya Jyothi Institute of Technology  
Himaynagar (Viii), C. B. Post  
Hyderabad-75 1

**Item No. 1: Presentation of 4<sup>th</sup> Year I Semester B.Tech Syllabi**

a). The Chairman presented the syllabi of three subjects of B.Tech IV Year I Semester viz.:

1. Digital Communications
2. Embedded System Design
3. Antennas & Microwave Engineering

b). The Chairman also presented the syllabi of Professional and Open Electives to be offered during IV year I semester. The titles of the subjects are;

**Professional Elective - 3**

1. Digital Image Processing
2. Spread Spectrum Communications
3. Multimedia Signal Coding

**Professional Elective - 4**

1. DSP Architectures
2. Telecommunication Switching Systems and Networks
3. Low Power VLSI

**Open Elective - 3**

1. Introduction to MATLAB
2. Circuits Simulation using PSpice

c). Thirdly, the Chairman presented the syllabi of Labs of IV year I semester. The Labs are:

1. Embedded System Design Lab
2. Microwave Engineering and Digital Communications Lab

d). Industry Oriented Mini Project

**Item No. 2: Presentation of 4<sup>th</sup> Year II Semester B.Tech Syllabi**

a). The Chairman presented the syllabi of three subjects of B.Tech IV Year II Semester viz.:

1. Cellular and Mobile Communications
2. Computer Networks
3. Radar Engineering

b). Major Project

c). Seminar

d). Comprehensive Viva - Voce

**Item No. 3: Presentation of 4<sup>th</sup> Year I Semester B.Tech EEE Syllabi.**

The Chairman presented the syllabi offered to EEE Department during IV year I semester

1. Microprocessor and Interfacing Devices
2. Microprocessor and Interfacing Devices Lab

**Item No. 4: Discussion regarding the Panel of examiners**

The Chairman emphasized the necessity of Panel of Examiners. Their services will be utilized in the preparation of End-Semester Question paper(s), Evaluation of End-Semester Examination Answer Scripts. They will be paid remuneration as per the recommendations of College Finance Committee.

**Resolutions**

After discussing various aspects of the syllabi the committee passed the following resolution.

**Resolution (1):** The members after thorough discussion approved the syllabi of IV – I, IV – II theory and labs (Annexure – II, Annexure – III) as per the subject titles in Annexure – I, and the subject offered for IV – I EEE theory and Labs as per Annexure -IV.

**Noted and Approved.**

**Resolution (2):** The BoS Chairman is authorized to prepare the Panel of examiners for both B.Tech IV Year I & II Semesters and IV – I EEE theory and Lab.

**Noted and Approved.**

**Signatures of the Members Present:**

1. 
2. 
3. 
4. 
5. 
6. 
7. 
8. 
9. 
10. 
11. 

  
**PRINCIPAL**  
Vidya Jyothi Institute of Technology  
Himayalnagar (VII), C. B. Post  
Hyderabad-75

# Vidya Jyothi Institute of Technology

Minutes of the Board of studies of Dept. of Electronics and Communication Engineering  
meeting held on 7<sup>th</sup> April 2017

at 12.00 P.M in Board Room

The following members were present in the meeting.

S.No	Name of the Member	Designation	Signature
1	Dr. Harikrishna Kamatham, Professor & Head ECE, VJIT.	Chairman	
2	Dr. P. Chandra Sekhar Reddy, Assoc. Professor, JNTUH.	University Nominee	
3	Dr. K S Rao, Director, Anurag Group of Institutions.	Member	
4	Dr. G. Laxmi Narayana, Principal, Anurag College of Engg., Aushapur.	Member	
5	Dr. P. Chandra Sekhar, Dept. of ECE, Osmania University	Member	
6	Mr. Ch. Satyanarayana, Scientist G, RCI, Hyderabad.	Member	
7	Mr. N. Venkatesh, Sr. Vice - President, Redpine Signals, Hyderabad.	Member	
8	Dr. V. Usha Shree, Professor, ECE, VJIT.	Member	
9	Dr. K. Vasanth, Professor, ECE, VJIT.	Member	
10	Mr. M Rajendra Prasad, Assoc. Professor, ECE, VJIT.	Member	
11	Mrs. A. Jaya Lakshmi, Assoc. Professor, ECE, VJIT.	Member	
12	Mrs. O. Jaya Madhuri, Assoc. Professor, ECE, VJIT.	Member	
13	Dr. Z. Nain, Professor, ECE, VJIT.	Special Invitee	
14	Dr. P. Ganesan, Professor, ECE, VJIT.	Special Invitee	

PRINCIPAL

Vidya Jyothi Institute of Technology  
Himayatnagar (Vill), C B. Pur  
Hyderabad-75

**Item No. 1: Presentation of 3<sup>rd</sup> Year I Semester B.Tech Syllabi**

- a). The Chairman presented the syllabi of three subjects of B.Tech III Year I Semester viz
1. Analog Communications
  2. Linear & Digital IC Applications
  3. Control Systems Engineering
- b). The Chairman also presented the syllabi of Professional and Open Electives to be offered during III year I semester. The titles of the subjects are;

**Professional Elective - 1**

1. Computer Organization and Architecture
2. Soft Computing
3. Biomedical Instrumentation

**Open Elective - 1**

1. Introduction to Microcontrollers & Applications
  2. Basic Electronics & Instrumentation
- c). Thirdly, the Chairman presented the syllabi of Labs of III year I semester. The Labs are:
1. Analog Communications Lab
  2. Linear & Digital IC Applications Lab

**Item No. 2: Presentation of 3<sup>rd</sup> Year II Semester B.Tech Syllabi**

- a). The Chairman presented the syllabi of three subjects of B.Tech III Year II Semester viz:
1. VLSI Design
  2. Digital Signal Processing
  3. Microprocessors and Microcontrollers
- b). The Chairman also presented the syllabi of Professional and Open Electives to be offered during III year II semester. These are:

**Professional Elective - 2**

1. Optical Communications
2. Programming in MATLAB
3. Satellite & Wireless Communication

**Open Elective - 2**

1. Fundamentals of Embedded Systems
  2. Principles of Communications
- c). Finally, the Chairman also presented the syllabi of III year II semester Labs.

*(Handwritten signatures and initials in blue ink)*

## Laboratories

1. Microprocessors and Microcontrollers Lab
2. Digital Signal Processing & e-CAD Lab

### Item No. 3: Presentation of 4<sup>th</sup> Year I Semester B.Tech Laboratories.

The Chairman presented the syllabi of Labs to be offered during IV year I semester

1. Embedded System Design Lab
2. Microwave & Digital Communications Lab

### Item No. 4: Discussion regarding the Panel of examiners

The Chairman emphasized the necessity of Panel of Examiners. Their services will be utilized in the preparation of End-Semester Question paper(s), Evaluation of End-Semester Examination Answer Scripts. They will be paid remuneration as per the recommendations of College Finance Committee.

#### Resolutions

After discussing various aspects of the syllabi the committee passed the following resolution.

**Resolution (1):** The members after thorough discussion approved the syllabi of III – I, III – II theory and labs as per the subject titles in Annexure – I, and IV – I Labs as per Annexure -IV.

**Noted and Approved.**

**Resolution (2):** The BoS Chairman is authorized to prepare the Panel of examiners for both B.Tech III Year I & II Semesters and IV – I Labs.

**Noted and Approved.**

#### Signatures of the Members Present:

- |   |   |   |
|---|---|---|
| 1.   | 2.   | 3.   |
| 4.   | 5.   | 6.    |
| 7.   | 8.   | 9.   |
| 10.  | 11.  | 12.  |
| 13.  | 14.  |   |

  
PRINCIPAL

Vidya Jyothi Institute of Technology  
Himayatnagar (VIII), C B. Post  
Hyderabad-75

**Minutes of the 3<sup>rd</sup> Board of studies (ECE) meeting held on 24<sup>th</sup> May 2016  
at 10.00 A.M in Board Room**

17

The following members were present in the meeting.

1. Dr M V Krishna Rao, Chairman
2. Dr Chandra Sekhar Reddy, Member
3. Dr K S Rao, Member
4. Dr Kishan Rao, Member
5. Dr.Chandra Sekhar, Member
6. Mr. Ch Satyanarayana, Member
7. Mr. N.Venkatesh, Member
8. Mr M Rajendra Prasad, Member
9. Mr Ravi Kishore, Member
10. Mr Shaik Maznu, Member
11. Ms O Jaya Madhhuri, Member
12. Dr.Usha Sree, Invitee
13. Dr.Hari Krishna, Invitee

**Item No. 1: Approval of 2<sup>nd</sup> Year B.Tech (ECE) Syllabi**

The Chairperson of Board of Studies (BoS) presented the syllabi of B.Tech II Year (ECE-I & II Semesters). Detailed and elaborate discussions were held regarding each and every topic of the syllabi of various papers related to both the Semesters - II/I and II/II as Table-I.

**Table. 1**

S.No.	Subject	
<b>II/I Semester</b>		
1	Electronic Devices and Circuits	3
2	Signals and Systems	4
3	Switching Theory and Logic Design	3
4	Electronic Measurements & Instruments	3
5	Probability Theory and Stochastic	4
6	Electronic Devices and Circuits Lab	2
7	Basic Simulation Lab	2
<b>II/II Semester</b>		
1	Electronic Circuit Analysis	4

*[Handwritten signatures in blue ink]*

2	Pulse and Digital Circuits	4
3	Electromagnetic Theory and Transmission Lines	4
4	Digital System Design	3
5	ECA Lab	2
6	PDC Lab	2

It has been decided to discuss and finalize the syllabi and course structure of III & IV year subjects in the next meeting.

After discussing various aspects of the syllabi the committee passed the following resolution.

**Resolution (1):** The members after thorough discussion approved the syllabi of all the course mentioned in Table: 1 and the details of the syllabi are as per *Annexure: 1*.

**Noted and Approved**

**Item No. 2: Approval of 2<sup>nd</sup> Year I Sem. B.Tech (EEE) & B.Tech (CSE & IT) Syllabi**

The Chairperson of Board of Studies (BoS) presented the 2<sup>nd</sup> Year I Sem. B.Tech (EEE) and 2<sup>nd</sup> Year I Sem. B.Tech (CSE & IT) Syllabi to the Board.

**Table. 2**

S.No.	Subject	Credits	Branches
<b>II/I Semester</b>			
1	Electronic Devices and Circuits	3	EEE, CSE & IT
2	Electronic Devices and Circuits Lab	2	EEE
3	Electronic Devices and Circuits/ Digital Logic Design Lab	2	CSE & IT

**Resolution (2):** The members after thorough discussion approved the syllabi of all the course mentioned in Table: 2 and the details of the syllabi are as per *Annexure: 1*.

**Noted and Approved**

**Item No. 3: Approval of M.Tech. (I year II semester) Syllabi**

The Chairperson of Board of Studies (BoS) presented the syllabi of all the subjects of M.Tech (VLSID & ES) programmes. As decided earlier, for this semester also it is decided to follow JNTU syllabus of M.Tech (VLSID & ES) for all the subjects.

*[Handwritten signatures in blue and green ink]*

PRINCIPAL  
Vedya Bhairavi Institute of Technology  
Himayatnagar (VIII), C.B. Post  
Hyderabad-75

**Resolution (3):** The members after thorough discussion approved the I year II semester syllabi of M.Tech(VLSISD) & M.Tech(ES) as per JNTUH syllabus.

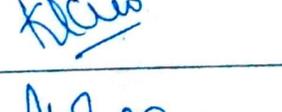
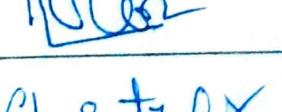
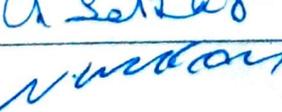
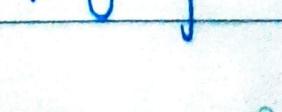
**Noted and Approved.**

**Item No. 4: To approve the Panel of examiners**

The Chairperson emphasized the necessity of Panel of Examiners. Their services shall be utilized in the preparation of End-Semester Question paper(s), Evaluation of End-Semester Examination Answer Scripts and that they will be paid with remuneration as per the recommendations of College Finance Committee.

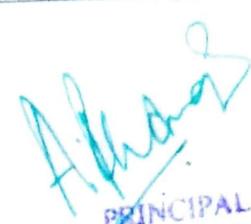
**Resolution (4):** The BoS Chairman is authorized to prepare the Panel of examiners for both B.Tech II Year I&II Semesters. and both M.Tech programmes.

**Noted and Approved.**

Sl. No	Name of the Member	Designation	Signature
1	Dr M V Krishna Rao	Chairman	
2	Dr Chandra Sekhar Reddy	Member	
3	Dr K S Rao	Member	
4	Dr Kishan Rao	Member	
5	Dr.Chandra Sekhar	Member	
6	Mr. Ch Satyanarayana	Member	
7	Mr. N.Venkatesh	Member	
8	Mr M Rajendra Prasad	Member	

  
PRINCIPAL  
Vijaya Jyothi Institute of Technology  
Himayyathapur (Villi), C.B. Post  
Hyderabad-75

9	Mr Ravi Kishore	Member	
10	Mr Shaik Maznu	Member	
11	Ms O Jaya Madhhuri	Member	
12	Dr. V. Usha Sree	Invitee	
13	Dr. K. Hari Krishna	Invitee	 24/5/16

  
PRINCIPAL  
Vidya Jyothi Institute of Technology  
Himayatnagar (VIII), C. B. Post  
Hyderabad-75